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**WINC1500 PICtail™ /PICtail
Plus Daughter Board
User's Guide**

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Object of Declaration: WINC1500 PICtail™/PICtail Plus Daughter Board User's Guide

EU Declaration of Conformity

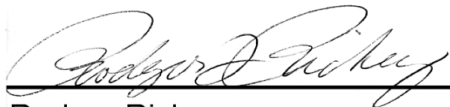
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Signed for and on behalf of Microchip Technology Inc. at Chandler, Arizona, USA.



Rodger Richey
Director of Development Tools



Date

NOTES:



WINC1500 PICTAIL™/PICTAIL PLUS DAUGHTER BOARD USER'S GUIDE

Table of Contents

Chapter 1. Introduction

1.1 Kit Contents	13
1.2 Features	13

Chapter 2. Hardware

2.1 Headers and Connectors	15
2.2 Peripherals	17

Chapter 3. Getting Started

3.1 Plugging into PIC18 Explorer Board	19
3.2 Plugging into Explorer 16 Development Board	20
3.3 Plugging into Explorer 16/32 Development Board	21
A.1 Reference Schematics	23
B.1 WINC1500 PICtail/PICtail Plus Daughter Board Bill Of Materials	27

WINC1500 PICtail™/PICtail Plus Daughter Board User's Guide

NOTES:



WINC1500 PICTAIL™/PICTAIL PLUS DAUGHTER BOARD USER'S GUIDE

Preface

NOTICE TO CUSTOMERS

All documentation becomes dated, and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our web site (www.microchip.com) to obtain the latest documentation available.

Documents are identified with a “DS” number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is “DSXXXXXXXXA”, where “XXXXXXXX” is the document number and “A” is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® X IDE online help. Select the Help menu, and then Topics to open a list of available online help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the WINC1500 PICtail™/PICtail Plus Daughter Board. Items discussed in this chapter include:

- [Document Layout](#)
- [Conventions Used in this Guide](#)
- [Recommended Reading](#)
- [The Microchip Web Site](#)
- [Development Systems Customer Change Notification Service](#)
- [Customer Support](#)
- [Document Revision History](#)

DOCUMENT LAYOUT

This document describes how to use the WINC1500 PICtail™/PICtail Plus Daughter Board (also referred as WINC1500 daughter board or daughter board). This user's guide is composed of the following chapters:

- **Chapter 1. “Introduction”** provides an overview of the WINC1500 PICtail/PICtail Plus Daughter Board and its features.
- **Chapter 2. “Hardware”** provides hardware details of the WINC1500 PICtail/PICtail Plus Daughter Board.
- **Chapter 3. “Getting Started”** describes how to start using the WINC1500 PICtail/PICtail Plus Daughter Board.
- **Appendix A. “Schematics”** provides the WINC1500 PICtail/PICtail Plus Daughter Board reference schematics and PCB information.
- **Appendix B. “Bill of Materials”** provides the bill of materials for the components used in the design and manufacture of the WINC1500 PICtail/PICtail Plus Daughter Board.

WINC1500 PICtail™/PICtail Plus Daughter Board User's Guide

CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Italic characters	Referenced books	<i>MPLAB IDE User's Guide</i>
	Emphasized text	...is the <i>only</i> compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, italic text with right angle bracket	A menu path	<u><i>File > Save</i></u>
Bold characters	A dialog button	Click OK
	A tab	Click the Power tab
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Plain Courier New	Sample source code	#define START
	Filenames	autoexec.bat
	File paths	c:\mcc18\h
	Keywords	_asm, _endasm, static
	Command-line options	-Opa+, -Opa-
	Bit values	0, 1
	Constants	0xFF, 'A'
<i>Italic Courier New</i>	A variable argument	<i>file.o</i> , where <i>file</i> can be any valid filename
Square brackets []	Optional arguments	mcc18 [options] <i>file</i> [options]
Curly brackets and pipe character: { }	Choice of mutually exclusive arguments; an OR selection	errorlevel {0 1}
Ellipses...	Replaces repeated text	var_name [, var_name...]
	Represents code supplied by user	void main (void) { ... }
Notes	A Note presents information that we want to re-emphasize, either to help you avoid a common pitfall or to make you aware of operating differences between some device family members. A Note can be in a box, or when used in a table or figure, it is located at the bottom of the table or figure.	Note: This is a standard note box.
		CAUTION This is a caution note. Note 1: This is a note used in a table.

RECOMMENDED READING

This user's guide describes how to use the WINC1500 PICtail/PICtail Plus Daughter Board. The following Microchip document is available and recommended as supplemental reference resources.

- **WINC1500-MR210PB IEEE 802.11 b/g/n IoT Module Data Sheet (DS70005304)**
- **WINC1500B IEEE 802.11 b/g/n Network Controller SoC Data Sheet**
- **PICDEM™ PIC18 Explorer Demonstration Board User's Guide (DS51721)**
- **Explorer 16 Development Board User's Guide (DS51589)**
- **ATECC508A Product page**
- **WINC1500 PICtail/PICtail Plus Daughter Board Design Package**

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- **Compilers** – The latest information on Microchip C compilers and other language tools
- **Emulators** – The latest information on the Microchip in-circuit emulator, MPLAB REAL ICE™
- **In-Circuit Debuggers** – The latest information on the Microchip in-circuit debugger, MPLAB ICD 3
- **MPLAB X IDE** – The latest information on Microchip MPLAB X IDE, the Windows® Integrated Development Environment for development systems tools
- **Programmiers** – The latest information on Microchip programmers including the PICkit™ 3 development programmer

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- Field Application Engineer (FAE)
- Technical Support

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Technical support is available through the web site at: <http://support.microchip.com>.

DOCUMENT REVISION HISTORY

Revision A (May 2017)

This is the initial released version of this document.

WINC1500 PICtail™/PICtail Plus Daughter Board User's Guide

NOTES:

Chapter 1. Introduction

Thank you for purchasing a WINC1500 PICTail™/PICTail Plus Daughter Board. This document provides detailed information about the Microchip Technology WINC1500 PICTail/PICTail Plus Daughter Board.

The WINC1500 PICTail/PICTail Plus Daughter Board is a demonstration and development board for the WINC1510-MR210PB certified Wi-Fi module.

This daughter board has PICTail and PICTail Plus connectors to interface with a PIC microcontroller on the development boards that supports either PICTail or PICTail Plus interface. The daughter board can be plugged into multiple Microchip Technology demonstration and development boards. For example, the daughter board is appropriate for 8-bit microcontroller development using the PIC18 Explorer Board or for 16-bit microcontroller development using Explorer 16 Development Board.

The supporting software stacks and application notes are available for download from the Microchip web site at:

www.microchip.com/mplab/mplab-harmony/mplab-harmony-2-0-2.

CAUTION

Power to the WINC1500 PICTail/PICTail Plus Daughter Board must be in the range of 3.0V to 3.6V. Ensure that the daughter board is plugged into a development/demonstration board that meets the voltage requirement; otherwise, damage to the WINC1500 board may occur.

This chapter includes the following topics:

1.1 “Kit Contents”

1.2 “Features”

1.1 KIT CONTENTS

The WINC1500 PICTail/PICTail Plus Daughter Board kit contains the following:

- One WINC1500 PICTail/PICTail Plus Daughter Board, which contains the ATWINC1510-MR210PB module

1.2 FEATURES

The following are the key features of the WINC1500 PICTail/PICTail Plus Daughter Board:

- Option to switch power source between the USB and PICTail/PICTail Plus interface base board
- The UART debug interface of the WINC1500 daughter board can be directly accessed from the PC through the USB header (J7)
- User LEDs for indications
- Power measurement header
- I²C debug header for RF validation
- GPIO debug header

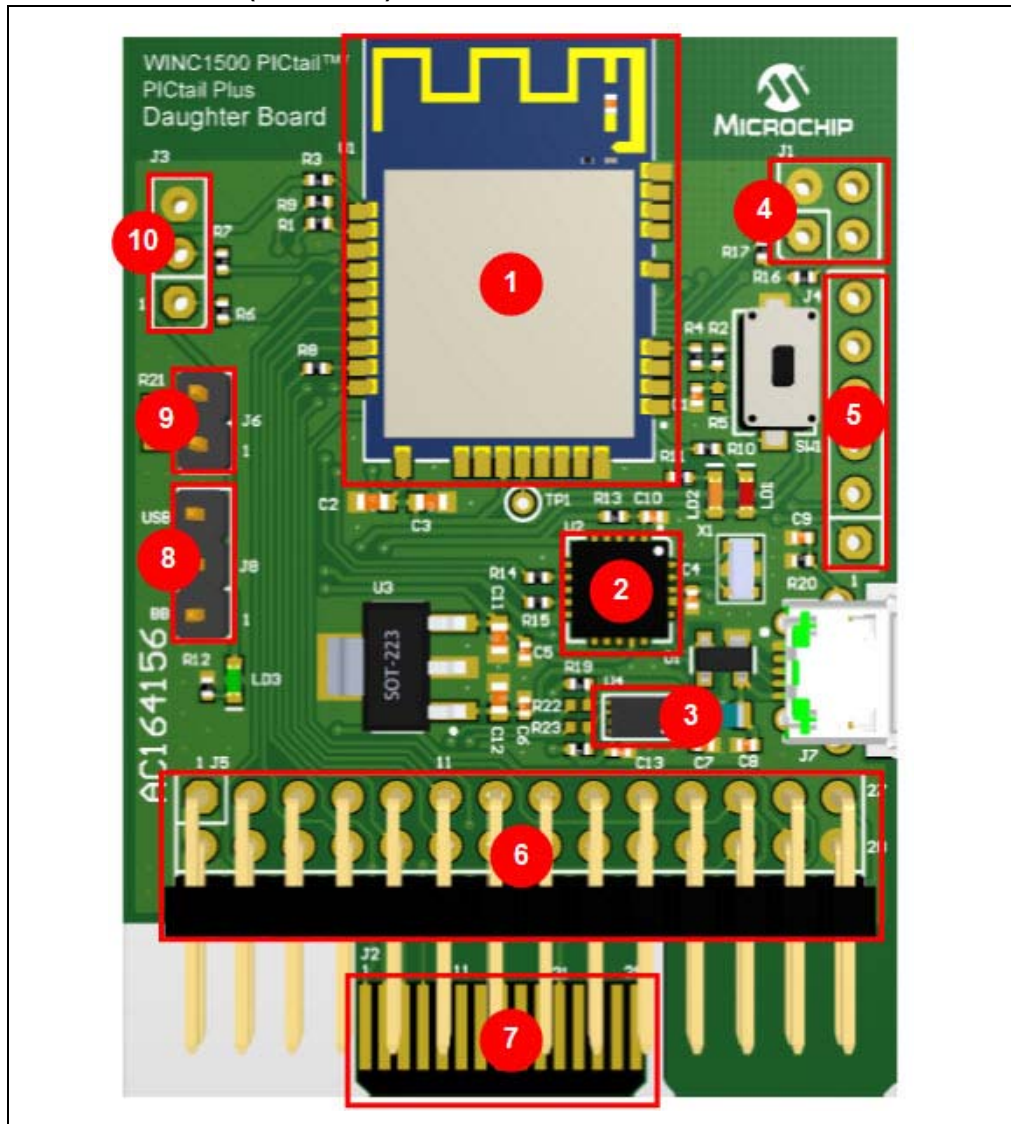
WINC1500 PICtail™/PICtail Plus Daughter Board User's Guide

Figure 1-1 illustrates the top view of the WINC1500 PICtail/PICtail Plus Daughter Board with the following components:

1. ATWINC1510-MR210PB module (U1)
2. MCP2200 (U2)
3. ATECC508A Crypto IC (U4)
4. I²C debug header (J1)
5. GPIO debug header (J4)
6. PICtail header (J5)
7. PICtail Plus connector (J2)
8. Power selection connector (J8)
9. Power measurement connector (J6)
10. UART debug header (J3)

For additional information on these features, refer to [Chapter 2. "Hardware"](#).

FIGURE 1-1: WINC1500 PICTAIL/PICTAIL PLUS DAUGHTER BOARD (TOP VIEW)



Chapter 2. Hardware

This chapter describes the hardware features of the WINC1500 PICtail/PICtail Plus Daughter Board.

2.1 HEADERS AND CONNECTORS

This section provides the details of each component on the WINC1500 PICtail/PICtail Plus Daughter Board. For the location of these components, see [Figure 1-1](#).

2.1.1 PICtail Plus Connector (J2)

The WINC1500 PICtail/PICtail Plus Daughter Board has a 30-pin card edge connector to interface with 16-bit and 32-bit development board's PICtail Plus connector, refer to [Table 2-1](#).

2.1.2 PICtail Connector (J5)

The WINC1500 PICtail/PICtail Plus Daughter Board has a 28-pin right angle connector to interface with the 8-bit development board's PICtail Plus connector.

[Table 2-1](#) provides the pinout details of the PICtail/PICtail Plus connectors.

TABLE 2-1: PICTAIL/PICTAIL PLUS CONNECTOR PINOUT

Pin on PICtail Plus 30-pin connector (J2)	Pin on PICtail 28-pin connector (J5)	Pin on ATWINC1510-MR210PB	Function
28	25	4	ATWINC1500 RESET
14	11	11	ATWINC1500 WAKE
18	27	13	ATWINC1500 IRQ
30	23	22	ATWINC1500 CHIP ENABLE
1	24	16	SPI_SS
7	8	15	SPI_MOSI
5	10	17	SPI_MISO
3	12	18	SPI_SCK
9,10,15,16	28	9,12, 28	GND
22	26	20, 23 ⁽¹⁾	Vcc

Note 1: The Vcc of the base board is connected to the Vcc of the module through a power selection connector when jumper is placed on the 1-2 of the header (J8).

2.1.3 I²C Debug Header (J1)

The WINC1500 PICtail/PICtail Plus Daughter Board has an UART debug header which connects the I²C debug interface to the ATWINC1510-MR210PB module. This header is not populated. [Table 2-2](#) provides the pinout details of the I²C debug header.

TABLE 2-2: I²C DEBUG HEADER PINOUT

Pin on I ² C debug header (J1)	Pin on ATWINC1510-MR210PB	Function
1	2	ATWINC1500 SCL
2	9, 12, 28	GND
3	3	ATWINC1500 SDA
4	—	No connection

2.1.4 GPIO Debug Header (J4)

The WINC1500 PICtail/PICtail Plus Daughter Board has an UART debug header which connects the GPIO debug interface to the ATWINC1510-MR210PB module. This header is not populated. [Table 2-3](#) provides the pinout details of the GPIO debug header.

TABLE 2-3: GPIO DEBUG HEADER PINOUT

Pin on GPIO debug header (J4)	Pin on ATWINC1510-MR210PB	Function
1	11	ATWINC1500 WAKE
2	21	ATWINC1500 GPIO_1
3	25	ATWINC1500 GPIO_3
4	26	ATWINC1500 GPIO_4
5	27	ATWINC1500 GPIO_5
6	1	ATWINC1500 GPIO_6

2.1.5 UART Debug Header (J3)

The WINC1500 PICtail/PICtail plus daughter board has an UART debug header which connects the UART debug interface to the ATWINC1510-MR210PB module. This header is not populated. [Table 2-4](#) provides the pinout of the UART debug header.

TABLE 2-4: UART DEBUG HEADER PINOUT

Pin on I ² C debug header (J1)	Pin on ATWINC1510-MR210PB	Function
1	19	ATWINC1500 UART RX
2	14	ATWINC1500 UART TX
3	9,12, 28	GND

2.1.6 Power Source Selection Connector (J8)

The jumper header J8, on the WINC1500 PICtail/PICtail Plus Daughter Board, is used to select power source for the daughter board. The daughter board can be powered either from the power supply of the base board or from the external USB supply.

Mounting the jumper in position 1-2 (pin 1 marked with overlay “BB” in the PCB) will power the daughter board from the base board, and mounting the jumper in position 2-3 (pin 3 marked with overlay “USB” in the PCB) will power the daughter board from the USB supply.

When the daughter board is powered from the external USB supply, it can still be interfaced to any base board with the PICtail/PICtail Plus connectors. The GND for the PICtail daughter board and base board are common, and the both will operate at 3.3V. A 5V input from the USB supply is provided to LDO (U3) to obtain the 3.3V for supplying the daughter board.

2.1.7 Power Measurement Header (J6)

The jumper header J6, on the WINC1500 PICtail/PICtail Plus Daughter Board, can be used to measure the current consumption of the ATWINC1510-MR210PB module. The power to the ATWINC1510-MR210PB module is routed through this header.

To measure the current consumption of the module, connect a voltmeter directly across the pins of J6. A 1Ω resistor is connected in parallel with the jumper header J6 which provides 1:1 relationship between the voltage and current measured across the jumper.

2.2 PERIPHERALS

2.2.1 USB-to-UART Serial Converter (U2)

The WINC1500 PICtail/PICtail Plus Daughter Board has an on-board USB-to-UART serial bridge/converter, which is used for observing UART debug logs from the ATWINC1510-MR210PB module. The PC console can be connected to the daughter board using a standard-A to micro-B or micro-AB cable.

The following UART configuration to be used in the PC console:

- Baud rate: 115200
- Data: 8 bit
- Parity: None
- Stop bit: 1
- Flow control: None

2.2.2 User LED

Two user LEDs are available on the WINC1500 PICtail/PICtail Plus Daughter Board which can be turned ON and OFF. The LED can be activated by driving the connected I/O line to GND. [Table 2-5](#) provides the pinout details of the user LEDs.

TABLE 2-5: USER LED PINOUT

LED	Pin on ATWINC1510-MR210PB	Function
LD1	1	ATWINC1500 GPIO_6 Red LED
LD2	27	ATWINC1500 GPIO_5 Orange LED

2.2.3 Power LED (LD3)

A power indication LED on the WINC1500 PICtail/PICtail Plus Daughter Board. This LED will be ON when the daughter board is provided with an adequate power supply.

2.2.4 Crypto Device - ATECC508A (U4)

The WINC1500 PICtail/PICtail Plus Daughter Board contains a ATECC508A Crypto authentication device. The I²C pins required for interfacing with the ATECC508A device is connected only to the PICtail Plus connector. The ATECC508A device is interfaced with the host MCU through 0R resistors, R22 and R23.

The 0R resistors are not mounted in the Rev 1 of the board and users need to solder these pads. These resistors are mounted from the Rev 1.2 of the board. The revision of the board could be found from the text in the bottom copper layer "04-10608 REV X.X", where X.X indicates the revision of the board.

[Table 2-6](#) provides the pinout details of the Crypto device.

TABLE 2-6: CRYPTO DEVICE- ATECC508A PINOUT

Pin on PICtail Plus 30-pin connector (J2)	Pin on ATECC508A	Function
6	6	ATECC508A SCL
8	5	ATECC508A SDA

Chapter 3. Getting Started

This chapter demonstrates how the WINC1500 PICtail/PICtail Plus Daughter Board can be plugged into the PIC18 Explorer, Explorer 16, and Explorer 16/32 Development Boards.

The WINC1500 Daughter Board can be plugged into multiple Microchip Technology demonstration and development boards. This allows the developer to choose the microcontroller that best suits the customer's development environment.

The PICtail connector (J5) can be plugged into 8-bit demonstration and development board, such as PIC 18 Explorer Board (DM183032). The PICtail Plus card edge connector (J2) can be plugged into Explorer 16 Development Board (DM240001).

This chapter includes the following topics:

3.1 “Plugging into PIC18 Explorer Board”

3.2 “Plugging into Explorer 16 Development Board”

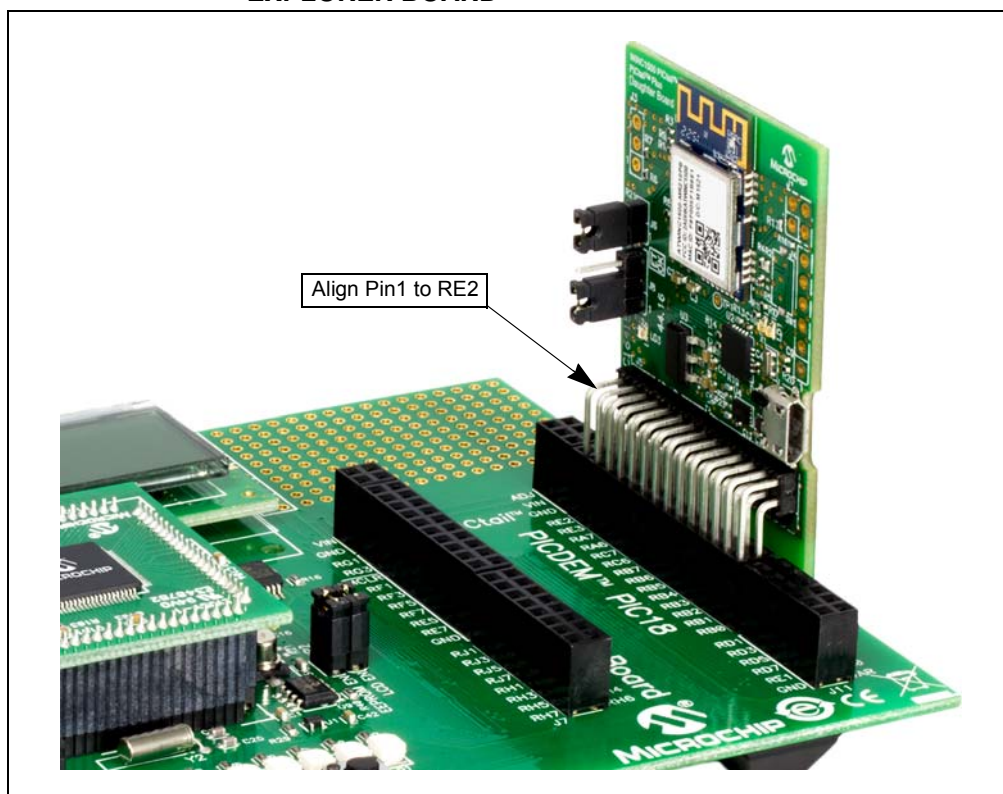
3.3 “Plugging into Explorer 16/32 Development Board”

3.1 PLUGGING INTO PIC18 EXPLORER BOARD

The WINC1500 PICtail/PICtail Plus Daughter Board can be plugged into the PICtail connector (J3) of the PIC18 Explorer Board. Ensure that the pin1 is aligned to RE2 as shown in [Figure 3-1](#).

CAUTION
Ensure that the PIC18F87J11 PIM is plugged into the PIC18 Explorer Board. This sets the system voltage V_{DD} to 3.3V, which is required by the WINC1500 PICtail/PICtail Plus Daughter Board.

FIGURE 3-1: WINC1500 DAUGHTER BOARD PLUGGED INTO PIC18 EXPLORER BOARD

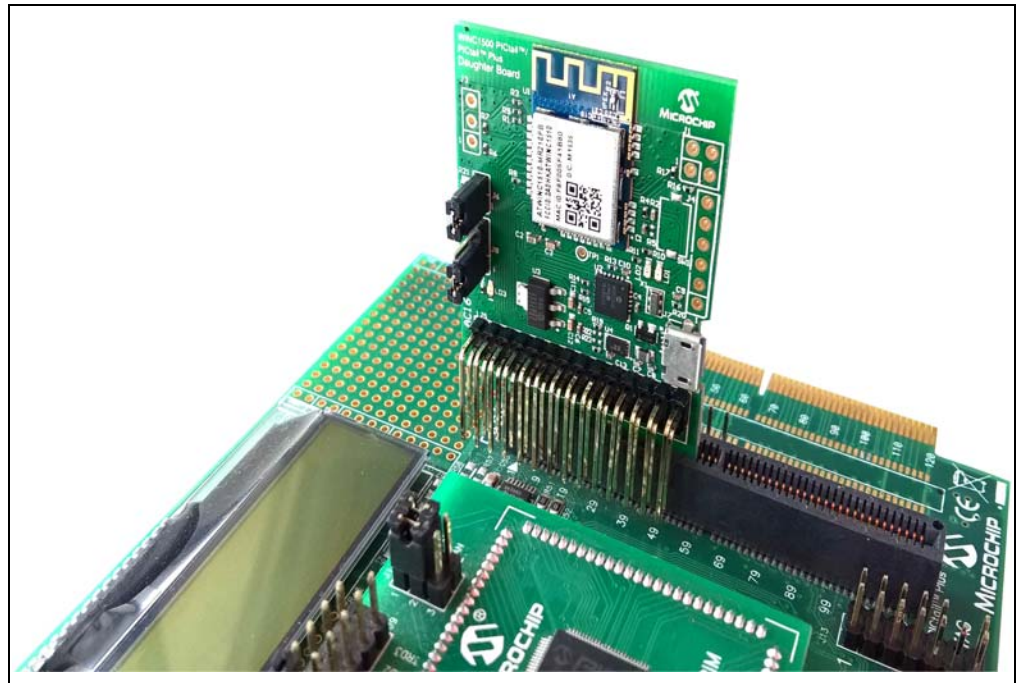


3.2 PLUGGING INTO EXPLORER 16 DEVELOPMENT BOARD

The WINC1500 PICtail/PICtail Plus Daughter Board can be plugged into the Explorer 16 Development Board, as illustrated in [Figure 3-2](#).

The WINC1500 daughter board with 30-pin card edge connector can be plugged into the top (pin 1-30) of the PICtail Plus connector. This will connect the WINC1500 module to the SPI Port 1 on the PIC microcontroller that is plugged into the PIM socket. If the daughter board is plugged into the mid-section of the PICtail Plus connector, this will connect to SPI Port 2 on the PIC microcontroller.

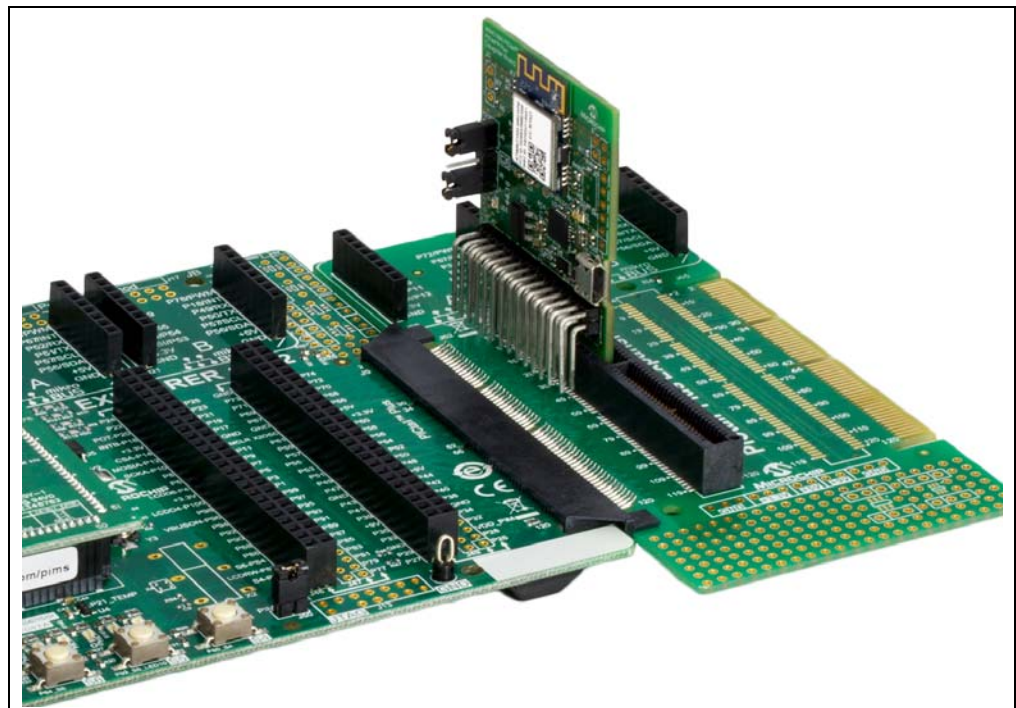
FIGURE 3-2: WINC1500 DAUGHTER BOARD PLUGGED INTO EXPLORER 16 DEVELOPMENT BOARD



3.3 PLUGGING INTO EXPLORER 16/32 DEVELOPMENT BOARD

The WINC1500 PICtail/PICtail Plus Daughter Board can be plugged into the Explorer 16/32 Development Board, as illustrated in [Figure 3-3](#). This setup requires the PICtail Plus expansion board to be plugged into the Explorer 16/32 development board.

FIGURE 3-3: WINC1500 DAUGHTER BOARD PLUGGED INTO EXPLORER 16/32 DEVELOPMENT BOARD



WINC1500 PICtail™/PICtail Plus Daughter Board User's Guide

NOTES:

Appendix A. Schematics

A.1 REFERENCE SCHEMATICS

FIGURE A-1: WINC1500 PICTAIL/PICTAIL PLUS SCHEMATICS

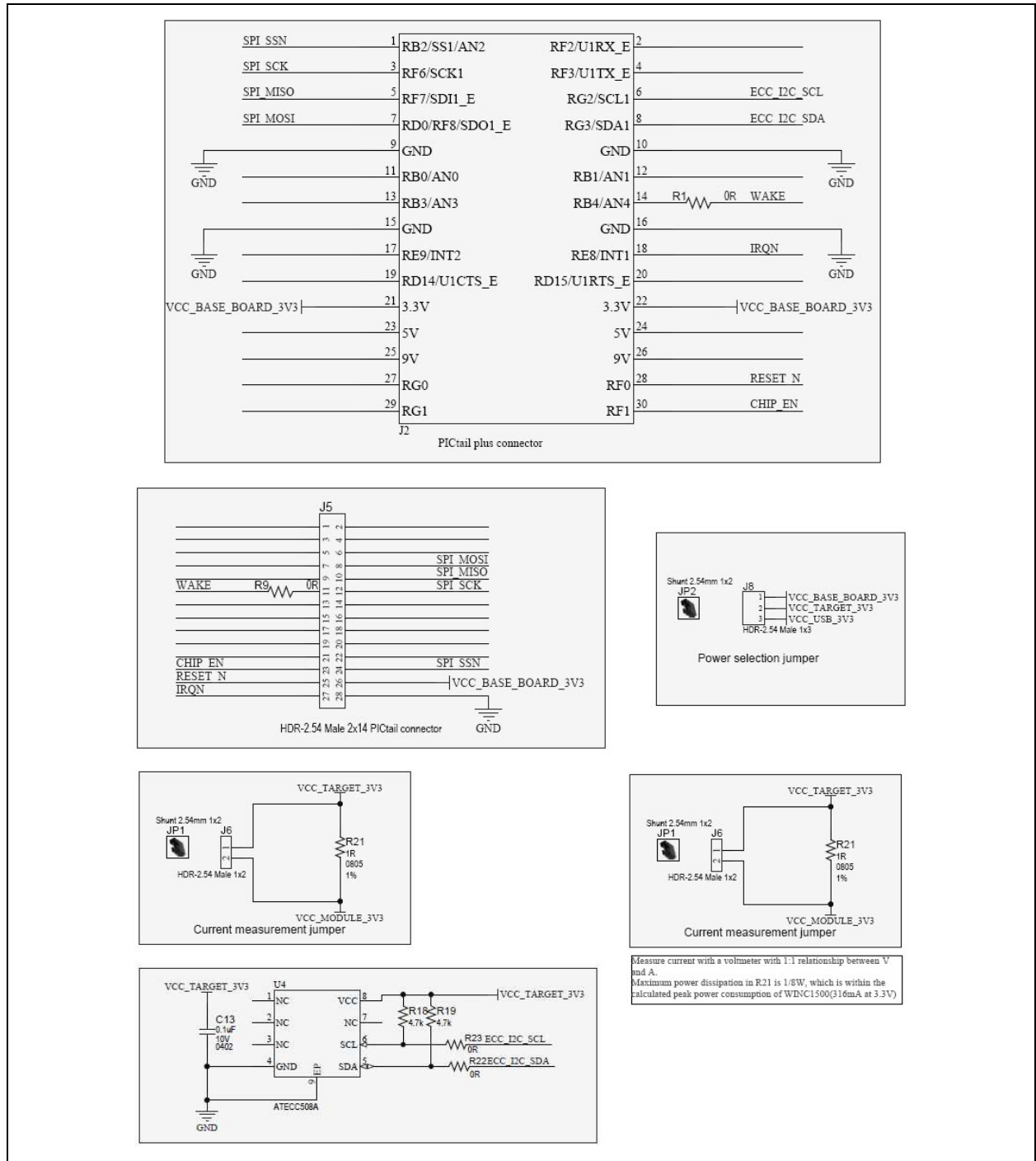
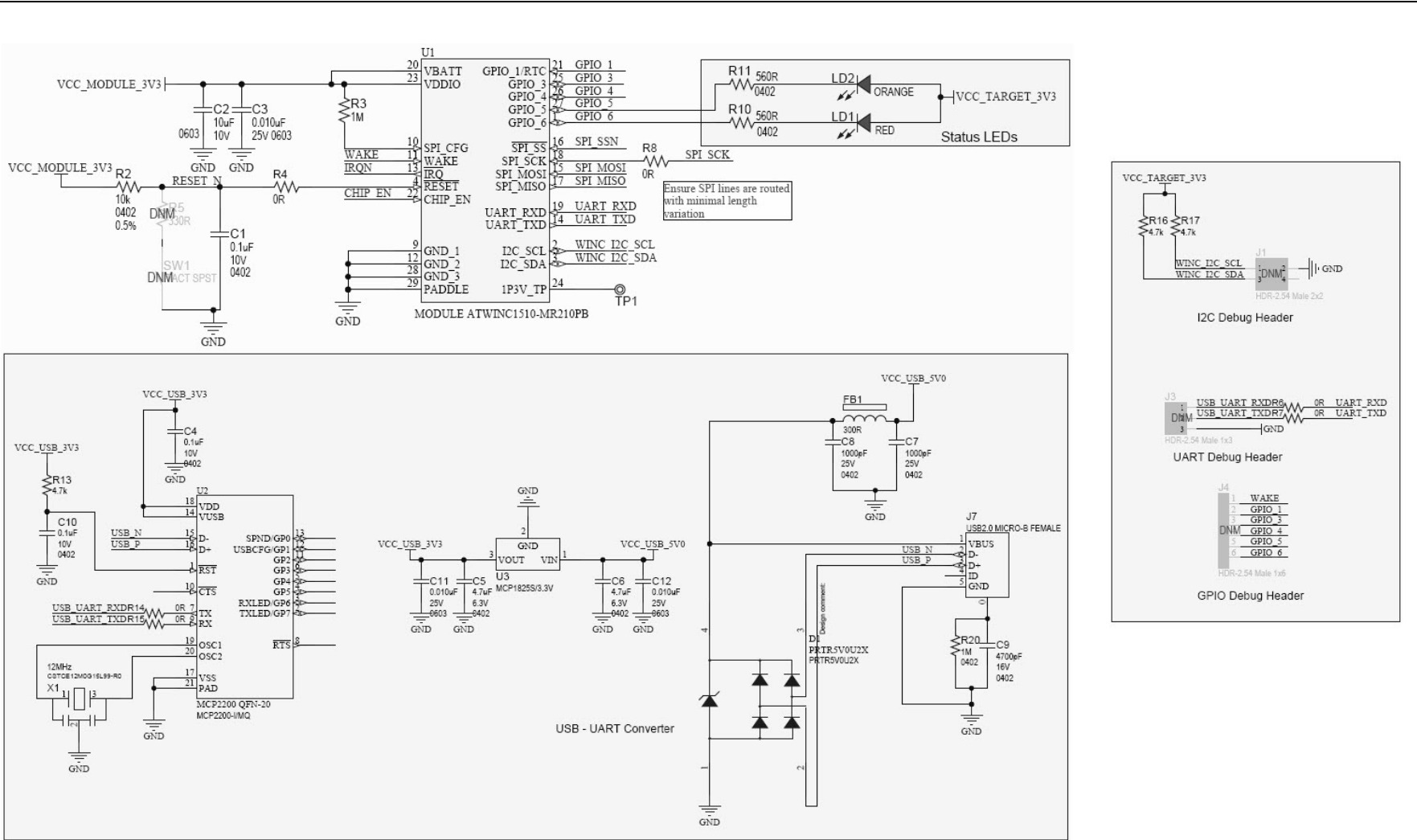


FIGURE A-2: WINC1500 PICTAIL/PICTAIL PLUS MODULE SCHEMATICS



Note: Power to the WINC1500 PICTAIL/PICTAIL Plus Daughter Board must be in the range of 3.0V to 3.6V. Ensure that the daughter board is plugged into a development/demonstration board that meets this voltage requirement; otherwise, damage to the WINC1500 may occur. Only 3.3V I/O compatible MCU must be interfaced with this board, as all the module I/O lines operate at 3.3V.

FIGURE A-3: WINC1500 PICTAIL/PICTAIL PLUS DAUGHTER BOARD SILK SCREEN (TOP VIEW)

