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KSZ8041 PHY Daughter Board Information Sheet

The KSZ8041 PHY Daughter Board (AC320004-5) enables Ethernet communication with a variety of Microchip starter kits. This information sheet provides an overview and a detailed schematic of the KSZ8041 PHY Daughter Board.

This daughter board is designed specifically to be used with the PIC32 Ethernet Starter Kit II (DM320004-2) or the PIC32MZ Embedded Connectivity with Floating Point Unit (EF) Starter Kit (DM320007); however, it can also be used with other Microchip development hardware.

Please visit http://www.microchip.com for additional information on the daughter board.

Features

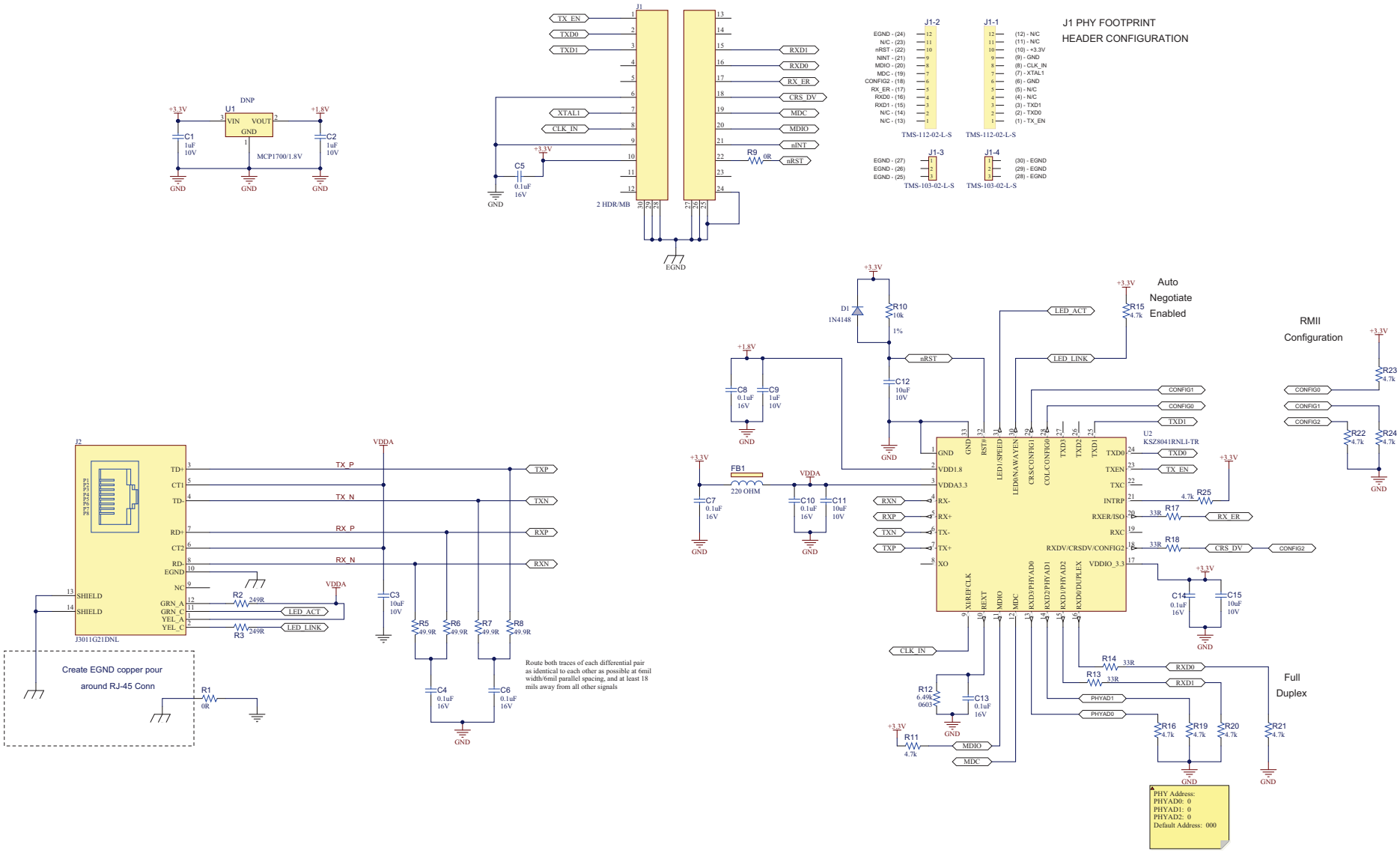
The KSZ8041 PHY Daughter Board features a 22-pin header for connecting the board into compatible PIC32 starter kits. The board has a KSZ8041 Ethernet Physical Layer Chip, configured to use RMII. The daughter board is suitable for use with the PIC32 Ethernet Starter Kit II and PIC32MZ EF Starter Kit.

Features of the KSZ8041 PHY Daughter Board includes:

- RMII interface with MDC/MDIO management interface for register configuration
- Single-Chip 10BASE-T/100BASE-TX Physical Layer Solution
- Fully compliant to IEEE 802.3u standard
- Low-power CMOS design, power consumption of <180 mW
- HP Auto MDI/MDI-X for reliable detection and correction for straight-through, and crossover cables with disable and enable options
- Robust operation over standard cables

Additional Information

For additional information on drivers and examples, please visit:

- http://www.microchip.com/wwwproducts/en/KSZ8041
- http://www.microchip.com/mplab/mplab-harmony

KSZ8041 PHY Daughter Board Schematic