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With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

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## Contact us

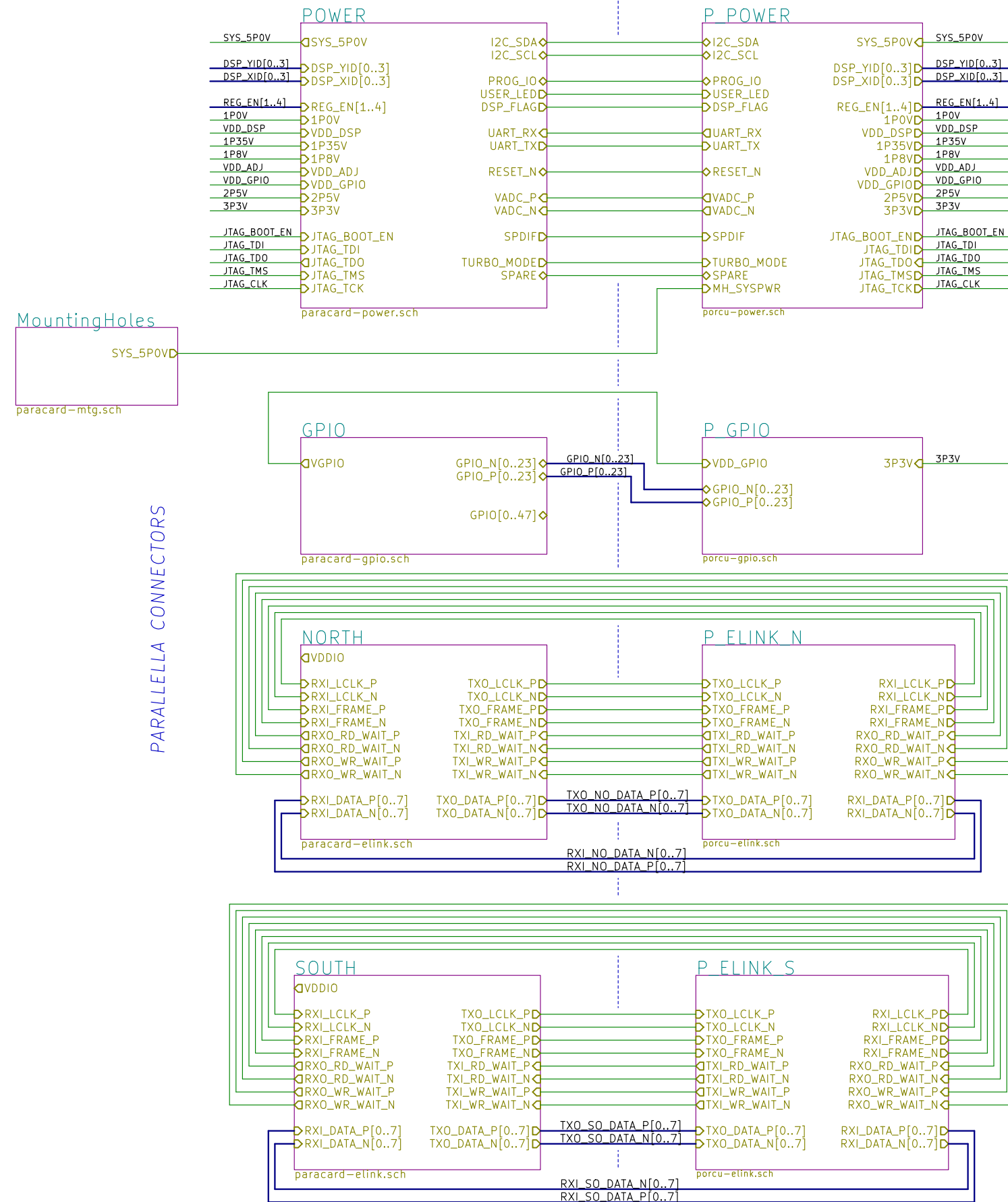
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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

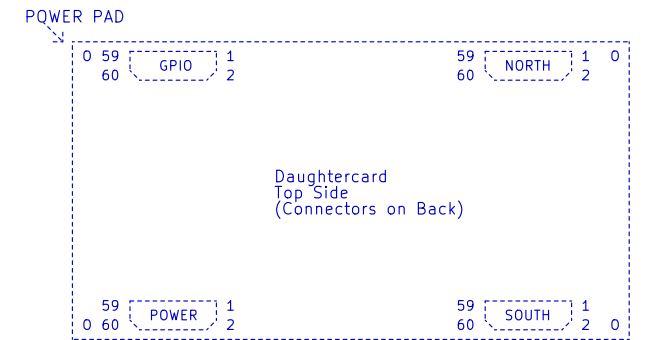
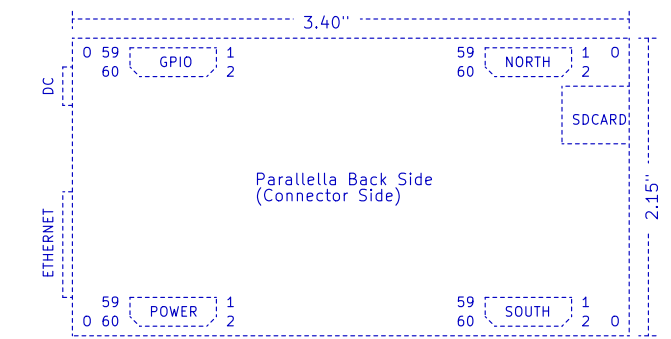


"Porcupine" Breakout Daughtercard for  
Adapteva Parallella-1.



PARALLELLA CONNECTORS

BREAKOUT CONNECTORS



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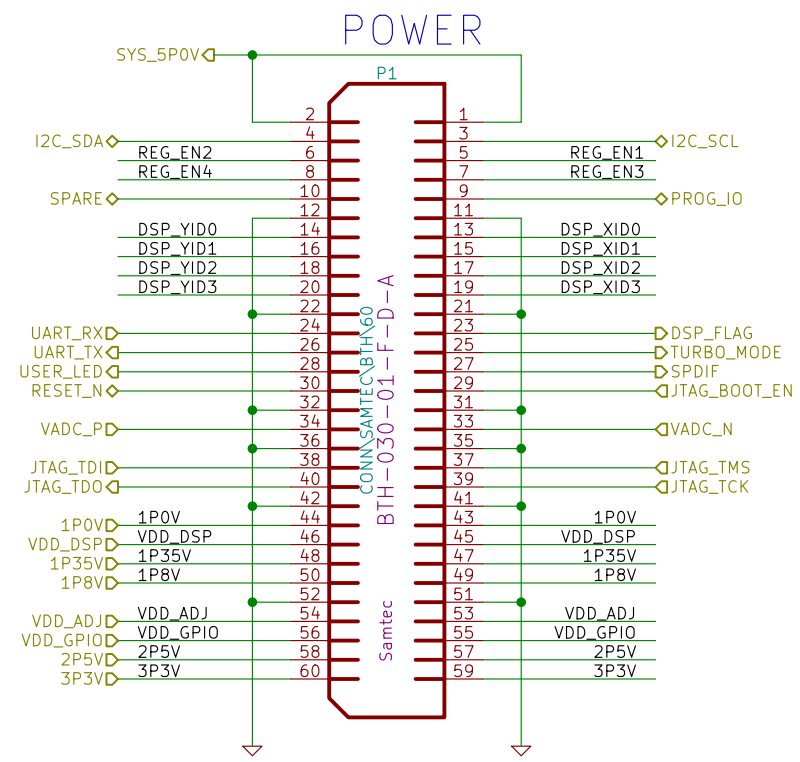
This schematic is **\*NOT SUPPORTED\*** and DOES NOT constitute a reference design. Only **\*community\*** support is allowed via resources at [forums.parallella.org](http://forums.parallella.org). THERE IS NO WARRANTY FOR THIS DESIGN, TO THE EXTENT PERMITTED BY APPLICABLE LAW. EXCEPT WHEN OTHERWISE STATED IN WRITING THE COPYRIGHT HOLDERS AND/OR OTHER PARTIES PROVIDE THE DESIGN **\*AS IS\*** WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. THE ENTIRE RISK AS TO THE QUALITY OF PERFORMANCE OF THE DESIGN IS WITH YOU. SHOULD THE DESIGN PROVE DEFECTIVE, YOU ASSUME THE COST OF ALL NECESSARY SERVICING, REPAIR OR CORRECTION.



Porcupine Top Level  
Adapteva, Inc., 1666 Massachusetts Ave., Lexington, MA, 02420  
Sheet: /  
File: porcupine.sch

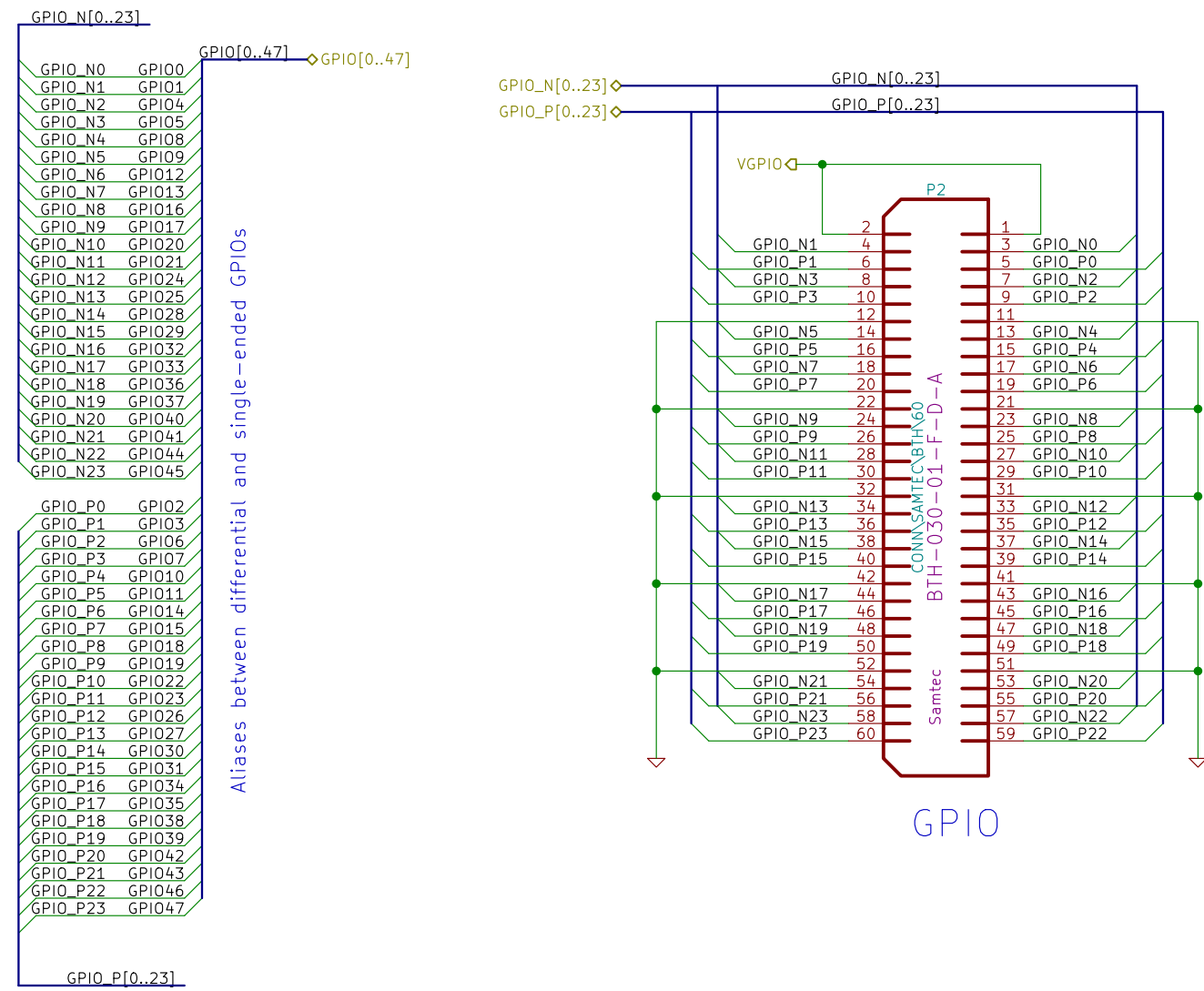
**Title: Parallella Breakout Daughtercard**

Size: B	Date: 19 Jun 2014	Rev: *
KiCad E.D.A.	eeschema (2014-02-06 BZR 4665)-product	Id: 1/10



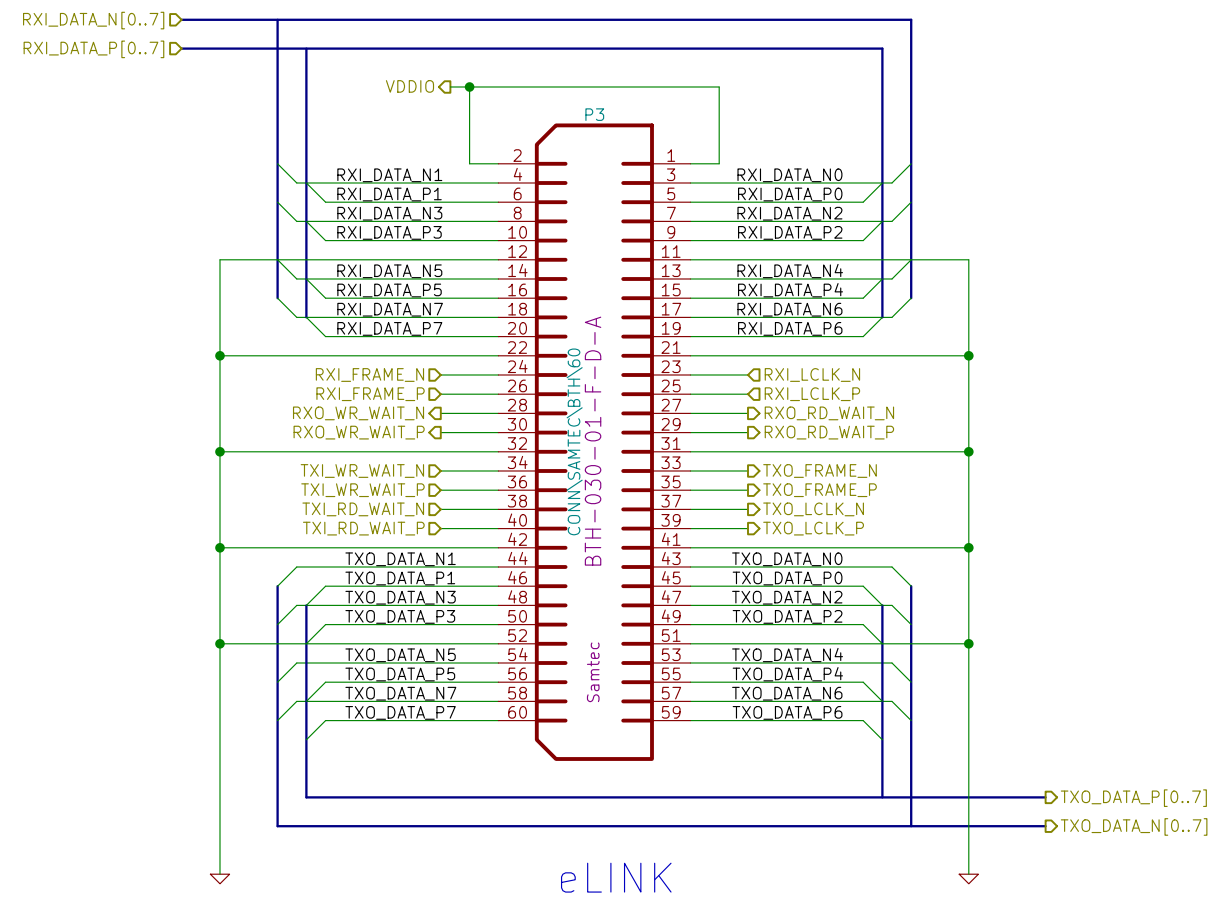
REG\_EN[1..4] → REG\_EN[1..4]  
 DSP\_YID[0..3] → DSP\_YID[0..3]  
 DSP\_XID[0..3] → DSP\_XID[0..3]





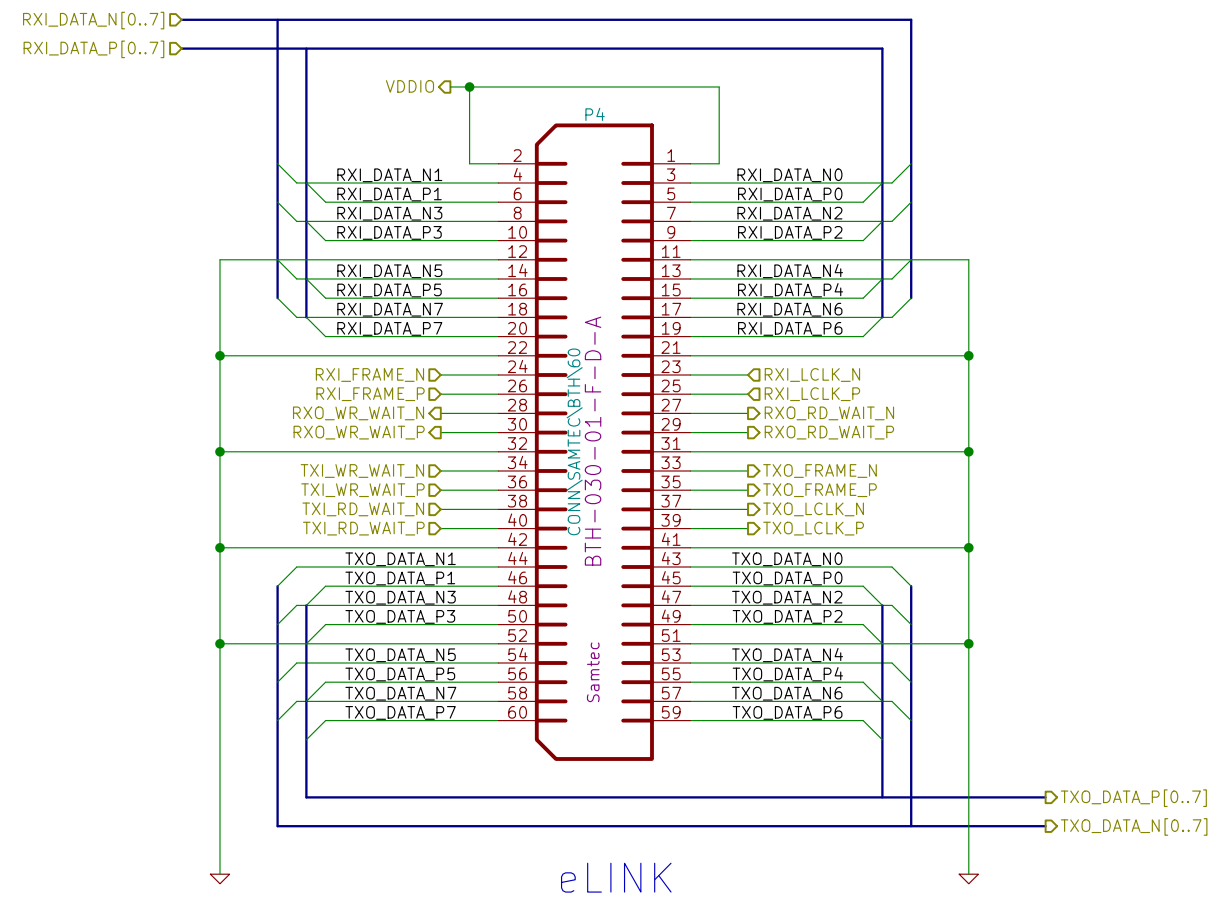
Aliases between differential and single-ended GPIOs





Parallella eLink Connector  
**Adapteva, Inc., 1666 Massachusetts Ave., Lexington, MA, 02420**  
 Sheet: /NORTH/  
 File: paracard-elink.sch

<b>Title: Parallella Breakout Daughtercard</b>		
Size: B	Date: 19 Jun 2014	Rev: *
KiCad E.D.A. eeschema (2014-02-06 BZR 4665)-product		Id: 4/10



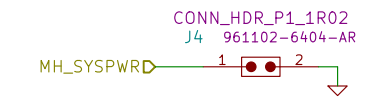
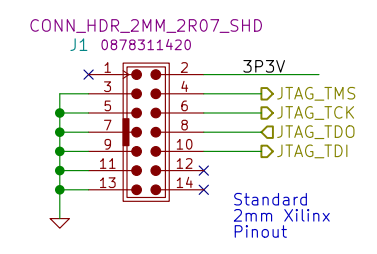
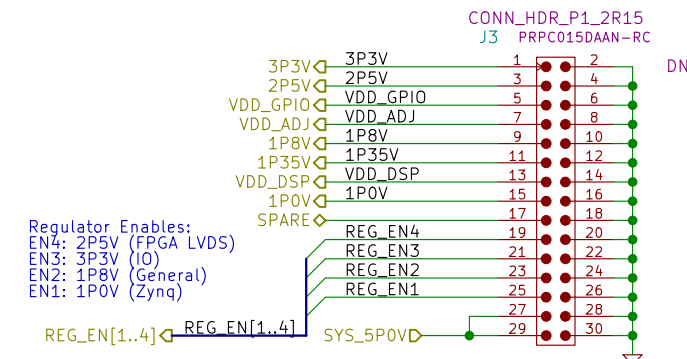
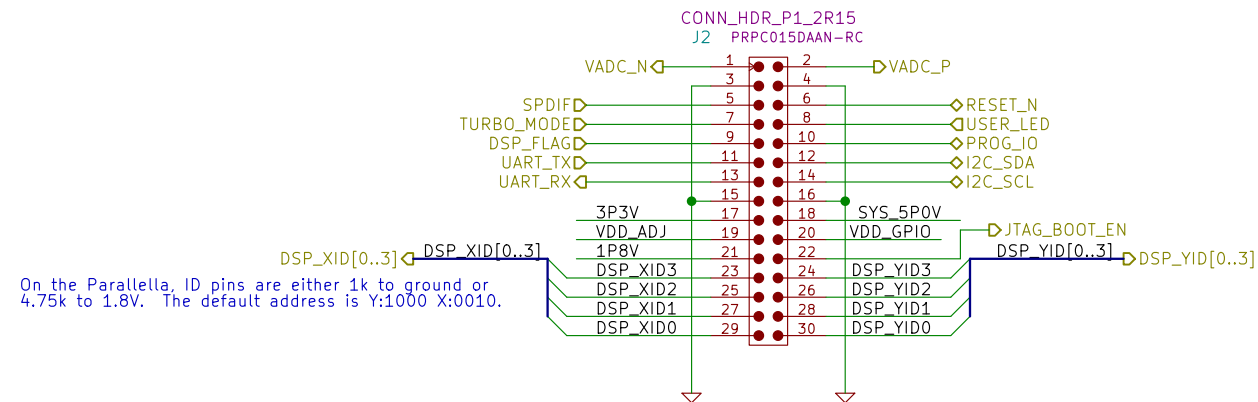
Parallella eLink Connector  
**Adapteva, Inc., 1666 Massachusetts Ave., Lexington, MA, 02420**  
 Sheet: /SOUTH/  
 File: paracard-elink.sch

<b>Title: Parallella Breakout Daughtercard</b>		
Size: B	Date: 19 Jun 2014	Rev: *
KiCad E.D.A. eeschema (2014-02-06 BZR 4665)-product		Id: 5/10

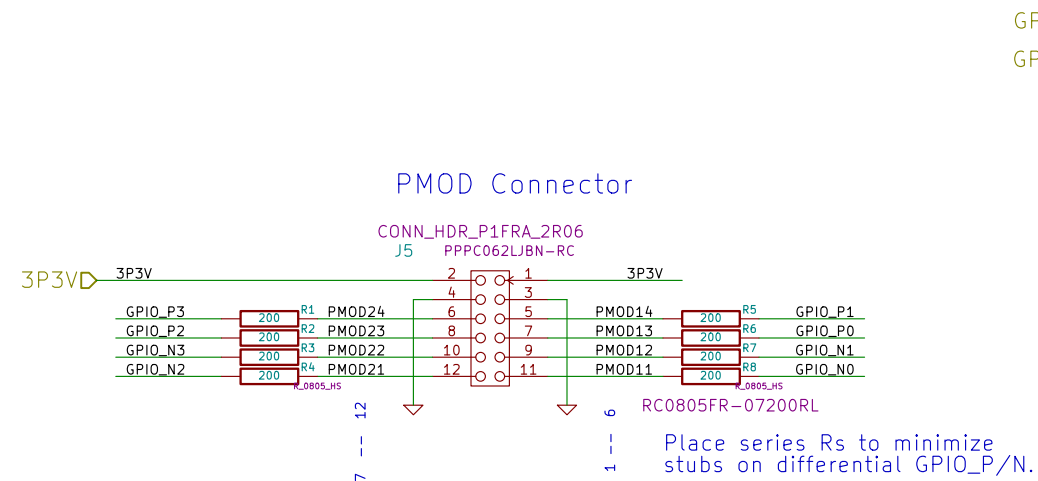


Mounting Holes  
**Adapteva, Inc., 1666 Massachusetts Ave., Lexington, MA, 02420**  
Sheet: /MountingHoles/  
File: paracard-mtg.sch

<b>Title: Parallella Breakout Daughtercard</b>		
Size: B	Date: 19 Jun 2014	Rev: *
KiCad E.D.A. eeschema (2014-02-06 BZR 4665)-product		Id: 6/10



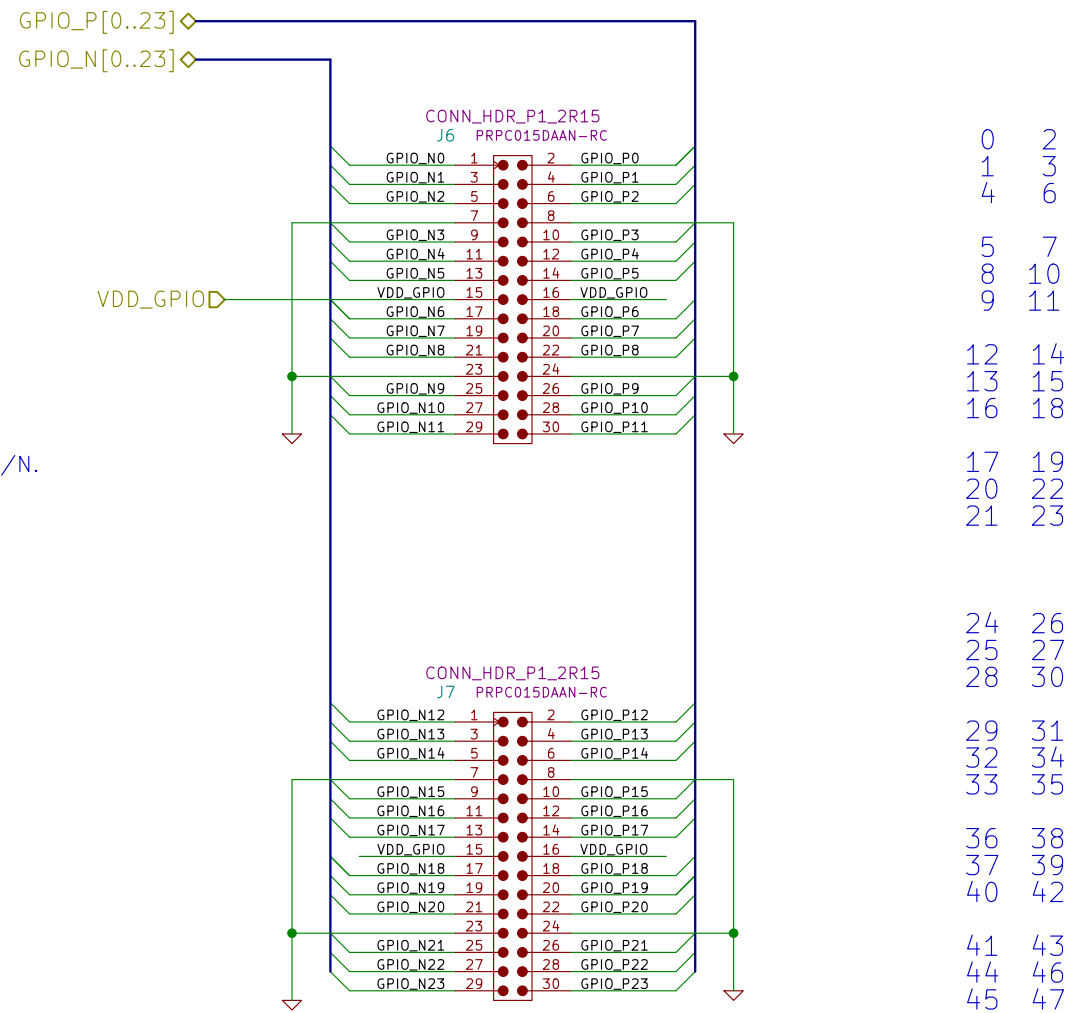




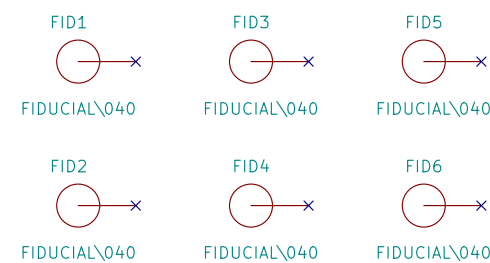
NOTE: Pin numbering for PMODs is non-standard. The numbering looking into the host (female) connector:

STD	1	3	5	7	9	11
PMOD	6	5	4	3	2	1
PMOD	12	11	10	9	8	7
STD	2	4	6	8	10	12

NOTE:  
GPIO assignments are made to facilitate differential signaling (e.g. twisted-pair). For single-ended GPIOs, the assignments are as follows:



Global Fiducial Markers Top / Bottom



GPIO Connectors  
 Adapteva, Inc., 1666 Massachusetts Ave., Lexington, MA, 02420  
 Sheet: /P\_GPIO/  
 File: porcu-gpio.sch

<b>Title: Parallella Breakout Daughtercard</b>		
Size: B	Date: 19 Jun 2014	Rev: *
KiCad E.D.A. eeschema (2014-02-06 BZR 4665)-product		Id: 8/10

Use Molex 0875683093  
as IDT mating connector.

