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## ACS108

## Overvoltage protected AC switch (ACS ${ }^{\top M}$ )

$\qquad$


## Features

- Enables equipment to meet IEC 61000-4-5 surge with overvoltage crowbar technology
- High noise immunity against static $\mathrm{dV} / \mathrm{dt}$ and IEC 61000-4-4 burst
- Needs no external protection snubber or varistor
- Reduces component count by up to $80 \%$ and Interfaces directly with the micro-controller
- Common package tab connection supports connection of several alternating current switches on the same cooling pad
- $\mathrm{V}_{\mathrm{CL}}$ gives headroom before clamping then crowbar action


## Applications

- Alternating current on/off static switching in appliances and industrial control systems
- Driving low power high inductive or resistive loads like:
- relay, valve, solenoid, dispenser,
- pump, fan, low power motor, door lock
- lamp


## Description

The ACS108 belongs to the AC switch range (built with A. S. D. ${ }^{\circledR}$ technology). This high performance switch can control a load of up to 0.8 A. The ACS108 switch includes an overvoltage crowbar structure to absorb the inductive turn-off energy, and a gate level shifter driver to separate the digital controller from the main switch. It is triggered with a negative gate current flowing out of the gate pin.

Figure 1. Functional diagram


Table 1. Device summary

| Symbol | Value | Unit |
| :---: | :---: | :---: |
| $\mathrm{I}_{\mathrm{T}(\mathrm{RMS})}$ | 0.8 | A |
| $\mathrm{~V}_{\mathrm{DRM}}, \mathrm{V}_{\text {RRM }}$ | 600 and 800 | V |
| $\mathrm{I}_{\mathrm{GT}}$ | 10 | mA |

®: A.S.D. is a registered trademark of STMicroelectronics
TM: ACS is a trademark of STMicroelectronics

## 1 Characteristics

Table 2. Absolute maximum ratings ( $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$, unless otherwise specified)

| Symbol | Parameter |  |  | Value | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\text {( } \mathrm{RMS} \text { ) }}$ | On-state rms current (full sine wave) | TO-92 | $\mathrm{T}_{\mathrm{amb}}=64^{\circ} \mathrm{C}$ | 0.45 | A |
|  |  |  | $\mathrm{T}_{\text {lead }}=76{ }^{\circ} \mathrm{C}$ | 0.8 | A |
|  |  | $\begin{aligned} & \text { SOT-223 } \\ & \mathrm{S}=5 \mathrm{~cm}^{2} \end{aligned}$ | $\mathrm{T}_{\text {amb }}=76^{\circ} \mathrm{C}$ |  |  |
|  |  |  | $\mathrm{T}_{\text {tab }}=104^{\circ} \mathrm{C}$ |  |  |
| $\mathrm{I}_{\text {TSM }}$ | Non repetitive surge peak on-state current (full cycle sine wave, $\mathrm{T}_{\mathrm{j}}$ initial $=25^{\circ} \mathrm{C}$ ) | $F=60 \mathrm{~Hz}$ | $\mathrm{t}=16.7 \mathrm{~ms}$ | 13.7 | A |
|  |  | $\mathrm{F}=50 \mathrm{~Hz}$ | $\mathrm{t}=20 \mathrm{~ms}$ | 13 |  |
| $1^{2} \mathrm{t}$ | ${ }^{12 t}$ Value for fusing |  | $\mathrm{t}_{\mathrm{p}}=10 \mathrm{~ms}$ | 1.1 | $A^{2} \mathrm{~s}$ |
| dl/dt | Critical rate of rise of on-state current $\mathrm{I}_{\mathrm{G}}=2 \mathrm{xI}_{\mathrm{GT}}, \mathrm{t}_{\mathrm{r}} \leq 100 \mathrm{~ns}$ | $\mathrm{F}=120 \mathrm{~Hz}$ | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | 100 | A/ $/ \mathrm{s}$ |
| $V_{P P}$ | Non repetitive mains peak mains voltage ${ }^{(1)}$ |  |  | 2 | kV |
| $\mathrm{I}_{\mathrm{GM}}$ | Peak gate current | $\mathrm{t}_{\mathrm{p}}=20 \mu \mathrm{~s}$ | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | 1 | A |
| $\mathrm{V}_{\mathrm{GM}}$ | Peak positive gate voltage |  | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | 10 | V |
| $\mathrm{P}_{\mathrm{G}(\mathrm{AV})}$ | Average gate power dissipation |  | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | 0.1 | W |
| $\begin{gathered} \mathrm{T}_{\text {stg }} \\ \mathrm{T}_{\mathrm{j}} \end{gathered}$ | Storage junction temperature range Operating junction temperature range |  |  | $\begin{aligned} & -40 \text { to }+150 \\ & -30 \text { to }+125 \end{aligned}$ | ${ }^{\circ} \mathrm{C}$ |

1. According to test described by IEC 61000-4-5 standard and Figure 18

Table 3. Electrical characteristics ( $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$, unless otherwise specified)

| Symbol | Test conditions | Quadrant |  | Value | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{IGT}^{(1)}$ | $\mathrm{V}_{\text {OUT }}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=33 \Omega$ | II - III | Max. | 10 | mA |
| $\mathrm{V}_{\mathrm{GT}}$ |  | II - III | Max. | 1 | V |
| $V_{G D}$ | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {DRM }}, \mathrm{R}_{\mathrm{L}}=3.3 \mathrm{k} \Omega, \mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | II - III | Min. | 0.15 | V |
| $\mathrm{I}_{\mathrm{H}}$ | $\mathrm{I}_{\text {OUT }}=100 \mathrm{~mA}$ |  | Max. | 10 | mA |
| IL | $\mathrm{I}_{\mathrm{G}}=1.2 \times \mathrm{I}_{\mathrm{GT}}$ |  | Max. | 25 | mA |
| dV/dt | $\mathrm{V}_{\text {OUT }}=402 \mathrm{~V}$, gate open, $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ |  | Min. | 2000 | $\mathrm{V} / \mathrm{\mu s}$ |
|  | $\mathrm{V}_{\text {OUT }}=536 \mathrm{~V}$, gate open, $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ |  | Min. | 400 | V/us |
| (dI/dt) C | Without snubber ( $15 \mathrm{~V} / \mu \mathrm{s}$ ), $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$, turn-off time $\leq 20 \mathrm{~ms}$ |  | Min. | 2 | A/ms |
| $V_{C L}$ | $\mathrm{I}_{\mathrm{CL}}=0.1 \mathrm{~mA}, \mathrm{t}_{\mathrm{p}}=1 \mathrm{~ms}$, ACS 108-6 |  | Min. | 650 | V |
|  | $\mathrm{I}_{\mathrm{CL}}=0.1 \mathrm{~mA}, \mathrm{t}_{\mathrm{p}}=1 \mathrm{~ms}$, ACS108-8 |  | Min. | 850 | V |

[^0]Table 4. Static electrical characteristics

| Symbol | Parameter and test conditions |  |  | Value | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {TM }}{ }^{(1)}$ | $\mathrm{I}_{\text {TM }}=1.1 \mathrm{~A}, \mathrm{t}_{\mathrm{p}}=500 \mu \mathrm{~s}$ | $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ | Max. | 1.3 | V |
| $\mathrm{V}_{\mathrm{t} 0}{ }^{(1)}$ | Threshold voltage | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | Max. | 0.85 | V |
| $\mathrm{R}_{\mathrm{D}}{ }^{(1)}$ | Dynamic resistance | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | Max. | 300 | $\mathrm{m} \Omega$ |
| IDRM <br> IRRM | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {DRM }}=\mathrm{V}_{\text {RRM }}$ | $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ | Max. | 2 | $\mu \mathrm{A}$ |
|  |  | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ |  | 0.2 | mA |

1. For both polarities of OUT referenced to COM

## Table 5. Thermal resistance

| Symbol | Parameter |  |  |  | Value | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{R}_{\text {th ( } \mathrm{j}-\mathrm{l})}$ | Junction to lead (AC) |  | TO-92 | Max. | 60 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\mathrm{R}_{\text {th (j-t) }}$ | Junction to tab (AC) |  | SOT-223 | Max. | 25 |  |
| $\left.\mathrm{R}_{\text {th ( }} \mathrm{j}-\mathrm{a}\right)$ | Junction to ambient |  | TO-92 | Max. | 150 |  |
|  |  | $\mathrm{S}=5 \mathrm{~cm}^{2}$ | SOT-223 | Max. | 60 |  |

Figure 2. Maximum power dissipation versus on-state rms current


Figure 3. On-state rms current versus case temperature (SOT223)


Figure 4. On-state rms current versus ambient temperature (free air convection)

Figure 5. Relative variation of thermal impedance junction to ambient versus pulse duration


Figure 6. Relative variation of holding and latching current versus junction temperature

Figure 7. Relative variation of $\mathrm{I}_{\mathrm{GT}}$ and $\mathrm{V}_{\mathrm{GT}}$ versus junction temperature


Figure 8. Surge peak on-state current versus number of cycles

Figure 9. Non repetitive surge peak on-state current for a sinusoidal pulse, and corresponding value of $\mathbf{l}^{2} \mathbf{t}$


Figure 10. On-state characteristics (maximum values)

Figure 11. Relative variation of critical rate of decrease of main current versus junction temperature


Figure 12. Relative variation of static $\mathrm{dV} / \mathrm{dt}$ immunity versus junction temperature ${ }^{(1)}$


1. $\mathrm{V}_{\mathrm{D}}=\mathrm{V}_{\mathrm{R}}=402 \mathrm{~V}$ : Typical values above $5 \mathrm{kV} / \mu \mathrm{s}$. Beyond equipment capability

Figure 14. Relative variation of critical rate of decrease of main current (di/dt)c versus (dV/dt)c

Figure 15. Thermal resistance junction to ambient versus copper surface under tab (SOT-223)


## 2 Alternating current mains switch - basic application

The ACS108 switch is triggered by a negative gate current flowing from the gate pin $G$. The switch can be driven directly by the digital controller through a resistor as shown in Figure 16.
Thanks to its overvoltage protection and turn-off commutation performance, the ACS108 switch can drive a small power high inductive load with neither varistor nor additional turn-off snubber.

Figure 16. Typical application schematic


### 2.1 Protection against overvoltage: the best choice is ACS

In comparison with standard Triacs the ACS108 is over-voltage self-protected, as specified by the new parameter $\mathrm{V}_{\mathrm{CL}}$. This feature is useful in two operating conditions: in case of turnoff of very inductive load, and in case of surge voltage that can occur on the electrical network.

### 2.1.1 High inductive load switch-off: turn-off overvoltage clamping

With high inductive and low rms current loads the rate of decrease of the current is very low. An overvoltage can occur when the gate current is removed and the OUT current is lower than $\mathrm{I}_{\mathrm{H}}$.
As shown in Figure 17, at the end of the last conduction half-cycle, the load current decreases (1). The load current reaches the holding current level $\mathrm{I}_{\mathrm{H}}$ (2), and the ACS turns off (3). The water valve, as an inductive load (up to 15 H ), reacts as a current generator and an overvoltage is created, which is clamped by the ACS (4). The current flows through the ACS avalanche and decreases linearly to zero. During this time, the voltage across the switch is limited to the clamping voltage $\mathrm{V}_{\mathrm{CL}}$. The energy stored in the inductance of the load is dissipated in the clamping section that is designed for this purpose. When the energy has been dissipated, the ACS voltage falls back to the mains voltage value ( 230 V rms , 50 Hz ) (5).

Figure 17. Switching off of a high inductive load - typical clamping capability of ACS108 ( $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$ )


### 2.1.2 Alternating current mains transient voltage ruggedness

The ACS108 switch is able to withstand safely the AC mains transients either by clamping the low energy spikes or by breaking-over when subjected to high energy shocks, even with high turn-on current rises.

The test circuit shown in Figure 18 is representative of the final ACS108 application, and is also used to test the AC switch according to the IEC 61000-4-5 standard conditions. Thanks to the load limiting the current, the ACS108 switch withstands the voltage spikes up to 2 kV above the peak mains voltage. The protection is based on an overvoltage crowbar technology. Actually, the ACS108 breaks over safely as shown in Figure 19. The ACS108 recovers its blocking voltage capability after the surge (switch off back at the next zero crossing of the current).

Such non-repetitive tests can be done 10 times on each AC mains voltage polarity.
Figure 18. Overvoltage ruggedness test circuit for resistive and inductive loads, $\mathrm{T}_{\mathrm{amb}}$ $=25^{\circ} \mathrm{C}$ (conditions equivalent to IEC 61000-4-5 standard)


Figure 19. Typical current and voltage waveforms across the ACS108 (+2 kV surge, IEC 61000-4-5 standard)


## 3 Package information

- Epoxy meets UL94, V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK ${ }^{\circledR}$ packages, depending on their level of environmental compliance. ECOPACK ${ }^{\circledR}$ specifications, grade definitions and product status are available at: www.st.com. ECOPACK ${ }^{\circledR}$ is an ST trademark.

Figure 20. TO-92 dimension definitions


Table 6. TO-92 dimension values

| Ref | Dimensions |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Millimeters |  |  |  | Mases |  |
|  | Min. | Typ. | Max. | Min. | Typ. | Max. |
| A |  | 1.35 |  |  | 0.053 |  |
| B |  |  | 4.70 |  |  | 0.185 |
| C |  | 2.54 |  |  | 0.100 |  |
| D | 4.40 |  |  | 0.173 |  |  |
| E | 12.70 |  |  | 0.500 |  |  |
| F |  |  | 3.70 |  |  | 0.146 |
| a |  |  | 0.50 |  |  | 0.019 |

Figure 21. SOT-223 dimension definitions


Table 7. SOT-223 dimension values

| Ref. | Millimeters |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Myp. |  |  |  | Max. | Min. |
|  | Min. | Typ. | Max. |  |  |  |
| A |  |  | 1.80 |  |  | 0.071 |
| A1 |  | 0.02 | 0.10 |  | 0.001 | 0.004 |
| B | 0.60 | 0.70 | 0.85 | 0.024 | 0.027 | 0.033 |
| B1 | 2.90 | 3.00 | 3.15 | 0.114 | 0.118 | 0.124 |
| c | 0.24 | 0.26 | 0.35 | 0.009 | 0.010 | 0.014 |
| D $^{(1)}$ | 6.30 | 6.50 | 6.70 | 0.248 | 0.256 | 0.264 |
| e |  | 2.3 |  |  | 0.090 |  |
| e1 |  | 4.6 |  |  | 0.181 |  |
| $E^{(1)}$ | 3.30 | 3.50 | 3.70 | 0.130 | 0.138 | 0.146 |
| H | 6.70 | 7.00 | 7.30 | 0.264 | 0.276 | 0.287 |
| V | $10^{\circ} \max$ |  |  |  |  |  |

1. Do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15 mm ( 0.006 inches)

Figure 22. SOT-223 footprint (dimensions in mm)


## 4 Ordering information

Figure 23. Ordering information scheme


TR = Tape and reel 7" (SOT-223, 1000 pieces) 13" (TO-92, 2000 pieces)
AP = Ammopack (TO-92, 2000 pieces)
Blank = bulk (TO-92, 2500 pieces)

Table 8. Ordering information

| Order code | Marking | Package | Weight | Base Qty | Delivery mode |
| :---: | :---: | :---: | :---: | :---: | :---: |
| ACS108-6SA | ACS1 086SA | TO-92 | 0.2 g | 2500 | Bulk |
| ACS108-6SA-TR |  | TO-92 | 0.2 g | 2000 | Tape and reel |
| ACS108-6SA-AP |  | TO-92 | 0.2 g | 2000 | Ammopack |
| ACS108-6SN-TR | ACS 108 6SN | SOT-223 | 0.11 g | 1000 | Tape and reel |
| ACS108-8SA | ACS1 088SA | TO-92 | 0.2 g | 2500 | Bulk |
| ACS108-8SA-TR |  | TO-92 | 0.2 g | 2000 | Tape and reel |
| ACS108-8SA-AP |  | TO-92 | 0.2 g | 2000 | Ammopack |
| ACS108-8SN-TR | ACS 108 8SN | SOT-223 | 0.11 g | 1000 | Tape and reel |

## 5 Revision history

Table 9. Document revision history

| Date | Revision | Changes |
| :---: | :---: | :--- |
| Apr_2004 | 1 | Initial release. This datasheet covers order codes previously <br> described in the datasheet for ACS108-6S, Doc ID 11962, Rev 3 <br> December 2010. |
| 21-Jun-2005 | 2 | Marking information updated from ACSxxxx to ACS1xxx. |
| 11-Jul-2012 | 3 | Removed 500 V devices and added 600 V and 800 V devices. |
| 27-Sep-2013 | 4 | Corrected typographical error in Figure 4. |
| 31-Oct-2013 | 5 | Corrected character formatting issues in Section 2.1.1. |

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[^0]:    1. Minimum $\mathrm{I}_{\mathrm{GT}}$ is guaranteed at $10 \%$ of $\mathrm{I}_{\mathrm{GT}}$ max
