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EVALUATING THE AD9652 16 bit, 310 MSPS DUAL ANALOG-TO-DIGITAL CONVERTERS

Preface

This user guide describes the [AD9652](#) evaluation board, [AD9652-310EBZ](#), which provides all of the support circuitry required to operate this part in its various modes and configurations. The application software used to interface with the devices is also described.

The [AD9652](#) data sheet provides additional information and should be consulted when using the evaluation board. All documents and software tools are available at www.analog.com/hsadcevalboard. For additional information or questions, send an email to highspeed.converters@analog.com.

Features

- Full featured evaluation board for the [AD9652](#)
- SPI interface for setup and control
- Balun/transformer or amplifier input drive option
- On-board LDO regulator needing a single external 6 V, 2 A dc supply
- VisualAnalog® and SPI controller software interfaces

Helpful Documents

- [AD9652](#) data sheet
- High Speed ADC Capture Evaluation Kit Rev C ([HSC-ADC-EVALCZ](#))
- [AN-905 Application Note](#), *VisualAnalog Converter Evaluation Tool Version 1.0 User Manual*
- [AN-878 Application Note](#), *High Speed ADC SPI Control Software*
- [AN-877 Application Note](#), *Interfacing to High Speed ADCs via SPI*
- [AN-835 Application Note](#), *Understanding ADC Testing and Evaluation*

Quick Start User Guides

User Guide for the AD9652 [quickstartguide_ad9652_custbrdr3_revision0.pdf](#)

AD9250/AD6673 Evaluation Board Files

AD9652 Evaluation Board Schematic PCB12048 Rev C [ad9652_evb_12048_c.pdf](#)

AD9652 Evaluation Board BOM PCB12048 Rev C [ad9652_evb_12048_c_bom.xls](#)

AD9652 Evaluation Board Gerber Files : Upon request

AD9652 Evaluation Setup Files

File required for **HSC-ADC-EVALCZ**: FPGA-BASED DATA CAPTURE KIT

Visual Analog Files : [ad9652customer_visualanalog_files.zip](#)

SPI Controller Files : [ad9652customer_spi_controller_files.zip](#)

AD9652 Ibis Model Files

AD9652 Ibis Model : TBD

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