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We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Dual-Channel CCD Signal Processor with *Precision Timing* Core

Data Sheet ADDI7013

FEATURES

Differential analog inputs
CDS or SHA (CDS bypass) with 7 gain settings
0 dB to 36 dB, 10-bit variable gain amplifier (VGA)
16-bit, 75 MSPS analog-to-digital converter (ADC)
Precision Timing core with 210 ps resolution at 75 MHz
8 independent H-clock phases with programmable drive strength (3.6 V maximum swing)
4 general-purpose outputs (GPO)
On-chip sync generator with external sync input
Reduced range LVDS outputs with single clock lane
6 mm × 6 mm CSP_BGA package with 0.5 mm pitch

APPLICATIONS

Industrial cameras
Surveillance cameras
Medical imaging
Professional photography

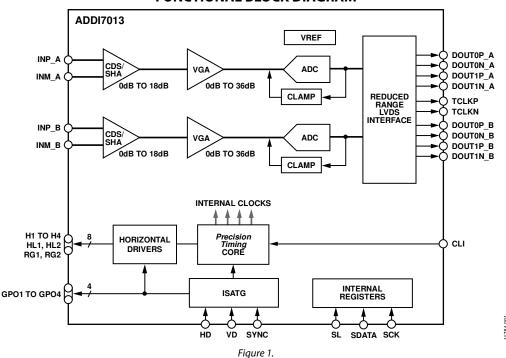
GENERAL DESCRIPTION

The ADDI7013 is a highly integrated, dual-channel, CCD signal processor for high speed digital imaging applications. Each channel is specified at pixel rates of up to 75 MHz and consists of a complete analog front end (AFE) with analog-to-digital conversion. The *Precision Timing** core allows adjustment of the correlated double sampler (CDS) and horizontal clocks with 210 ps resolution at 75 MHz operation. There are eight independent horizontal clock outputs with programmable drive strength to support a variety of CCD timing requirements.

Each analog front end includes black level clamping; a CDS; a VGA; and a 16-bit, 75 MSPS analog-to-digital converter (ADC). Operation is programmed using a 3-wire serial interface.

Packaged in a space-saving, 6 mm \times 6 mm, CSP_BGA, the ADDI7013 is specified over an operating temperature range of -40° C to $+85^{\circ}$ C.

FUNCTIONAL BLOCK DIAGRAM



For more information about the ADDI7013, contact Analog Devices, Inc., at afe.ccd@analog.com.

ADDI7013* PRODUCT PAGE QUICK LINKS

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COMPARABLE PARTS 🖵

View a parametric search of comparable parts.

DOCUMENTATION

Data Sheet

 ADDI7013: Dual-Channel CCD Signal Processor with Precision Timing Core Data Sheet

DESIGN RESOURCES

- · ADDI7013 Material Declaration
- · PCN-PDN Information
- · Quality And Reliability
- Symbols and Footprints

DISCUSSIONS

View all ADDI7013 EngineerZone Discussions.

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ADDI7013 Data Sheet

NOTES