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# MCP3913 ADC Evaluation Board for 16-Bit MCUs User's Guide

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# Object of Declaration: MCP3913 ADC Evaluation Board for 16-Bit MCUs User's Guide

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16-July - 2013 Date

Signed for and on behalf of Microchip Technology Inc. at Chandler, Arizona, USA

Derek Carlson

Derek Carlson

**VP Development Tools** 

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# **Preface**

# **NOTICE TO CUSTOMERS**

All documentation becomes dated, and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our web site (www.microchip.com) to obtain the latest documentation available.

Documents are identified with a "DS" number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is "DSXXXXXA", where "XXXXX" is the document number and "A" is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB<sup>®</sup> IDE online help. Select the Help menu, and then Topics to open a list of available online help files.

# INTRODUCTION

This chapter contains general information that will be useful to know before using the MCP3913 ADC Evaluation Board for 16-Bit MCUs. Items discussed in this chapter include:

- · Document Layout
- · Conventions Used in this Guide
- · Recommended Reading
- The Microchip Web Site
- · Customer Support
- · Document Revision History

### **DOCUMENT LAYOUT**

This document describes how to use the MCP3913 ADC Evaluation Board for 16-Bit MCUs as a development tool to emulate and debug firmware on a target board. The manual layout is as follows:

- Chapter 1. "Product Overview" Provides important information about the MCP3913 ADC Evaluation Board for 16-Bit MCUs hardware.
- Chapter 2. "Hardware Description" Provides information about the evaluation board software.
- Chapter 3. "Firmware" Describes the MCP3913 ADC Evaluation Board for 16-Bit MCUs firmware.
- Appendix A. "Schematics and Layouts" Shows the schematic and board layouts for the MCP3913 ADC Evaluation Board for 16-Bit MCUs.
- Appendix B. "Bill of Materials (BOM)" Lists the parts used to build the MCP3913 ADC Evaluation Board for 16-Bit MCUs.

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# **CONVENTIONS USED IN THIS GUIDE**

This manual uses the following documentation conventions:

# **DOCUMENTATION CONVENTIONS**

Description	Represents	Examples		
Arial font:				
Italic characters	Referenced books	MPLAB <sup>®</sup> IDE User's Guide		
	Emphasized text	is the <i>only</i> compiler		
Initial caps	A window	the Output window		
	A dialog	the Settings dialog		
	A menu selection	select Enable Programmer		
Quotes	A field name in a window or dialog	"Save project before build"		
Underlined, italic text with right angle bracket	A menu path	File>Save		
Bold characters	A dialog button	Click <b>OK</b>		
	A tab	Click the <b>Power</b> tab		
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1		
Text in angle brackets < > A key on the keyboard		Press <enter>, <f1></f1></enter>		
Courier New font:				
Plain Courier New	Sample source code	#define START		
	Filenames	autoexec.bat		
	File paths	c:\mcc18\h		
	Keywords	_asm, _endasm, static		
	Command-line options	-0pa+, -0pa-		
	Bit values	0, 1		
	Constants	0xFF, 'A'		
Italic Courier New	A variable argument	file.o, where file can be any valid filename		
Square brackets []	Optional arguments	<pre>mcc18 [options] file [options]</pre>		
Curly brackets and pipe character: {   }	Choice of mutually exclusive arguments; an OR selection	errorlevel {0 1}		
Ellipses	Replaces repeated text	var_name [, var_name]		
	Represents code supplied by user	void main (void) { }		

# RECOMMENDED READING

This user's guide describes how to use MCP3913 ADC Evaluation Board for 16-Bit MCUs. Another useful document is listed below. The following Microchip document is available and recommended as a supplemental reference resource:

• MCP3913 Data Sheet - "3V Six-Channel Analog Front End" (DS20000000)

### THE MICROCHIP WEB SITE

Microchip provides online support via our web site at <a href="https://www.microchip.com">www.microchip.com</a>. This web site is used as a means to make files and information easily available to customers. Accessible by using your favorite Internet browser, the web site contains the following information:

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- Technical Support

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Technical support is available through the web site at: http://support.microchip.com.

# **DOCUMENT REVISION HISTORY**

# **Revision A (October 2013)**

· Initial release of this document.

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# **Chapter 1. Product Overview**

# 1.1 OVERVIEW

The MCP3913 ADC Evaluation Board for 16-Bit MCUs system provides the opportunity to evaluate the performance of the MCP3913 six-channel Analog Front End (AFE). It also provides a development platform for 16-bit microcontroller-based applications, using the existing 100-pin PIC<sup>®</sup> microcontroller Plug-in Module (PIM) systems that are compatible with the Explorer 16 and other high pin count PIC device based demo boards. The system comes with a programmed PIC24FJ256GA110 PIM module that communicates with the Energy Management Utility software for data exchange and ADC setup.

The MCP3913 ADC Evaluation Board for 16-Bit MCUs uses the Energy Management Utility software for evaluation of the MCP3913 via a USB connection to the board. A download link for this software can be found on the evaluation board's web page. For instructions on the use of this software, refer to the supporting documentation included within the application install package.

# 1.1.1 Feature Highlights

- Six-channel ADC MCP3913 output display using serial communication to the PC software interface.
- Simultaneous 57 ksps at OSR32 address loop ALL or 95 dB SINAD at OSR512 performance on MCP3913.
- System and ADC performance analysis through graphical PC tools showing noise histogram, frequency domain (FFT), time domain scope plot, and statistical numerical analysis.
- Robust hardware design with analog grounding and analog/digital separation, allowing low noise evaluation of the MCP3913 device; includes separate power supplies and power planes on a four-layer board.
- PICtail™ Plus connectors for Explorer 16 daughter board compatibility.

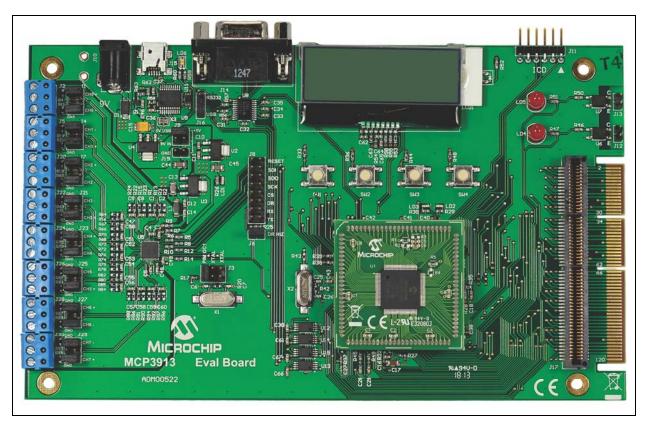


FIGURE 1-1: MCP3913 Evaluation Board.

# 1.2 WHAT THE MCP3913 ADC EVALUATION BOARD FOR 16-BIT MCUS CONTAINS

This MCP3913 ADC Evaluation Board for 16-Bit MCUs kit includes:

- MCP3913 ADC Evaluation Board for 16-Bit MCUs (Part number ADM00522)
- PIC24FJ256GA110 PIM
- USB Cable
- Important Information Sheet



# **Chapter 2. Hardware Description**

# 2.1 PIM MODULE/MCP3913 CONNECTION AND PERIPHERAL USAGE OVERVIEW

The MCP3913 ADC Evaluation Board for 16-Bit MCUs contains a 100-pin PIM socket compatible with Microchip's PIM modules. The system comes with one PIM module, the PIC24FJ256GA110.

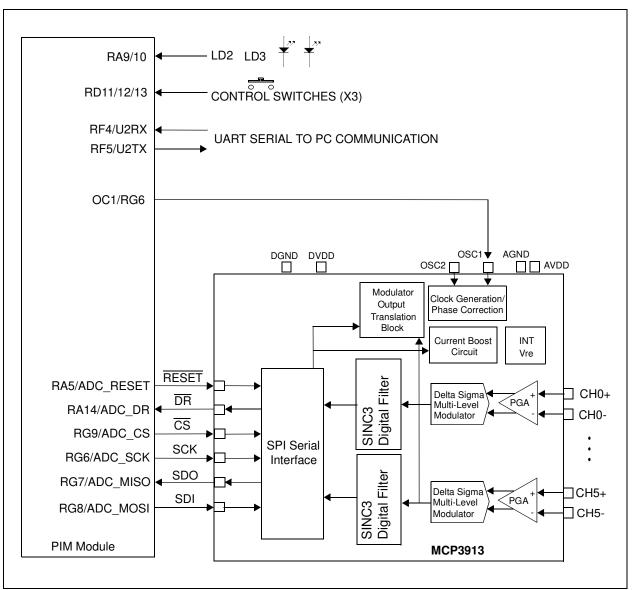


FIGURE 2-1: Digital Connection Overview PIM/MCP3913 Connections.

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Ports A, D, and G are used for signals such as push buttons, output LEDs,  $\overline{\text{CS}}$  and  $\overline{\text{MCLR}}$  (for the MCP3913 data mode setting). Output Compare 1 is used for clock generation in the MCP3913. Serial communication is achieved through the MSSP module 1.

The MCP3913 device is an AFE with six ADCs featuring a second order modulator and a third order sinc filter, plus a first order sinc filter used for higher OSR's values. The delta-sigma A/D converters have an adjustable oversampling ratio. The CLKIN pin of the MCP3913 is the oversampling clock (MCLK) input. The MCP3913 ADC Evaluation Board for 16-Bit MCUs offers two different options for the MCP3913 master clock (MCLK). The default setting for the ADC internal clock is an external clock driven by the MCU.

# 2.1.1 Using the Crystal X1

The MCP3913 ADC Evaluation Board for 16-Bit MCUs is populated with a 10 MHz crystal, used as a clock source, by placing jumpers in the following position on the MCP3913 Digital I/O header block:

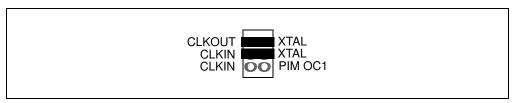


FIGURE 2-2: ADC Clock Selection Jumpers – External Crystal.

# 2.1.2 Driving the Clock with the PIM Module

The PIC MCU can be used to generate the CLKIN (MCLK) signal for the MCP3913, setting the ADC sample rate through the use of the output compare module OC1. To use this feature, make the following jumper change to the MCP3913 Digital I/O header block:

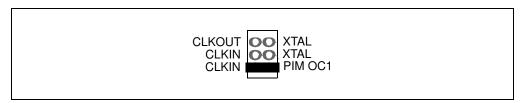


FIGURE 2-3: ADC Clock Selection Jumpers – Clock from MCU.

The frequency of the OC1 output is based on the PR1 bit settings in the firmware.

### 2.2 ANALOG INPUT STRUCTURE

Six differential input paths allow external signal sources to be easily connected to the MCP3913 inputs. Screw-terminal connectors J1, J4, J7, J21, J23, J25, J27 and J29 are 3-pin connectors that act as both screw-type and clip-on post connectors.

**Note:** To use a screw-terminal connector as a post connector, pull up the blue plastic top to access the posts.

The connectors J1, J4, J7, J21, J23 and J25 can be used to force either channel from a differential to single-ended configuration. R4 and R11 (on CH0), R19 and R21 (on CH1) R18 and R65 (on CH2), R69 and R71 (on CH3), R75 and R77 (on CH4), and R81 and RR83 (on CH5) act as locations for burden resistor connectors for any current transformer inputs.

# 2.3 UNIVERSAL SERIAL BUS (USB)

The MCP3913 ADC Evaluation Board for 16-Bit MCUs also contains a USB connection for connecting the evaluation board to a PC. On the board, there is an MCP2200 USB to UART converter that creates a virtual COMM port on the PC. The MCP3913 ADC Evaluation Board for 16-Bit MCUs also features an RS-232 connector, just in case it is required. The RS-232 line driver is connected to the same UART pins of the MCU. For this reason, a 3-pin jumper (J16) is present on the evaluation board to select which serial communication will be used: USB or RS-232. The following figure summarizes the connections between the ADC, MCU, USB to serial converter and the RS-232 line driver.

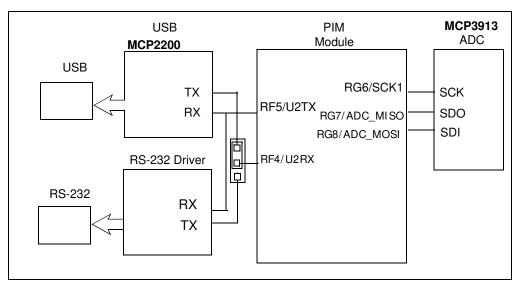


FIGURE 2-4: USB Block Diagram.

The MCP2200 is powered from the USB with 5V. The Q1 transistor (see **Appendix A.**) is used to disconnect the board from the PC when it is powered down to avoid power consumption. By changing the jumper J9 setting, the user can select the source of the power supply for the board to be either +5V derived from the USB or an external +9V power supply.

Since the PIC24F runs at 3.3V, a level shifter was used to modify the signal level to 5V as required by the MCP2200; this is done with U11 (see **Appendix A.**).

The 7.3728 MHz value of the crystal is required to achieve the correct baud rate values for higher speed. This design uses a baud rate of 921.6 kbaud and for this baud rate, the register (U1BRG) value is 3 (decimal).

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# Chapter 3. Firmware

# 3.1 PIC24FJ256GA110 FIRMWARE DESCRIPTION

### 3.1.1 MCU Initialization

The microcontroller used for the code example is the 16-bit XLP with 16 MIPS PIC24FJ256GA110. The MCU has remappable pins and output compare with a dedicated time base.

The MCU uses a 7.3278 MHz crystal for the clock and the internal PLL to increase the frequency by four times. The uncommon crystal value was chosen to obtain the correct baud rate, even for high baud rates. The remappable pins are configured to make the PIM compatible with the Explorer 16 development board. The MCP3913 is linked with the MCU through the SPI1 port. The ADC clock is given by OC1. The DR pin of the MCP3913 is tied to external Interrupt 2.

Serial data transmission is carried out through the UART2 module. For transmission, OC2 interrupt is used; for receiving, the \_U1RX Interrupt is used. The UART communication speed is 921.6 kbaud. OC2 is used for the serial transmission to create short bursts of data that can be processed by the MCP2200. Sending too many characters, with no time between them at the current baud rate, will cause the MCP2200 to lose data.

**Note:** The PIC24FJ256GA110 must be Rev. A5 or newer, in order for this code example to work.

# 3.2 DATA ACQUISITION

External Interrupt 2 is used for detecting the end of conversion on the MCP3913. In the INT2 Interrupt, the data samples are read from the ADC using SPI.

When a new buffer begins to be read, Timer4 is started. This is set as a 32-bit timer together with Timer5. It will be stopped when the buffer is full and is used to measure the sampling speed.

Before reading the date samples, the MCU investigates the address loop setting (READ <1:0> bits) in the STATUSCOM register and adjusts the read sequence accordingly (when the address is not incremented automatically in hardware, the MCU has to do that in firmware, leading to a longer read sequence). The acquired samples are written in six long vectors created in the RAM of the MCU. The six vectors hold the first, second and third byte from both channels.

The vectors have a length of 2048. The value of Timer 4, and the values of the other registers inside the ADC, are stored in a vector called "internal registers[26]".

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# 3.3 UART COMMUNICATION PROTOCOL

The serial connection is used to send the acquired data to the PC at a speed of 921.6 kbaud, and the MCP2200 is used for the UART-to-USB conversion.

The communication uses handshaking, meaning that the board will not send data to the PC unless the board receives a command from the PC.

The UART transmission is triggered by the output compare module OC2. This allows the data to be transmitted to the PC in short bursts that are separated by a dead time. This method of sending data allows the MCP2200 to handle the high throughput. If the entire amount of data is sent continuously to the PC at the current baud rate, the MCP2200 will not be able to process the data for correct USB transfer.

After a complete transmission, the UART RX interrupt is enabled by increasing its priority, while all other interrupts have lower priorities. In this interrupt, the MCU needs to receive the values of the internal registers as the user is setting them in the PC GUI.



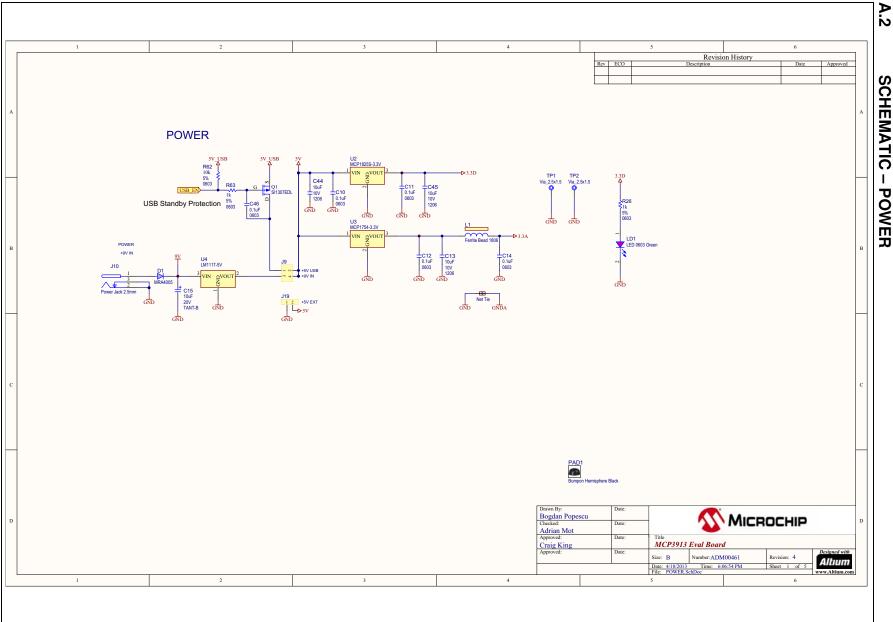
# **Appendix A. Schematics and Layouts**

# A.1 INTRODUCTION

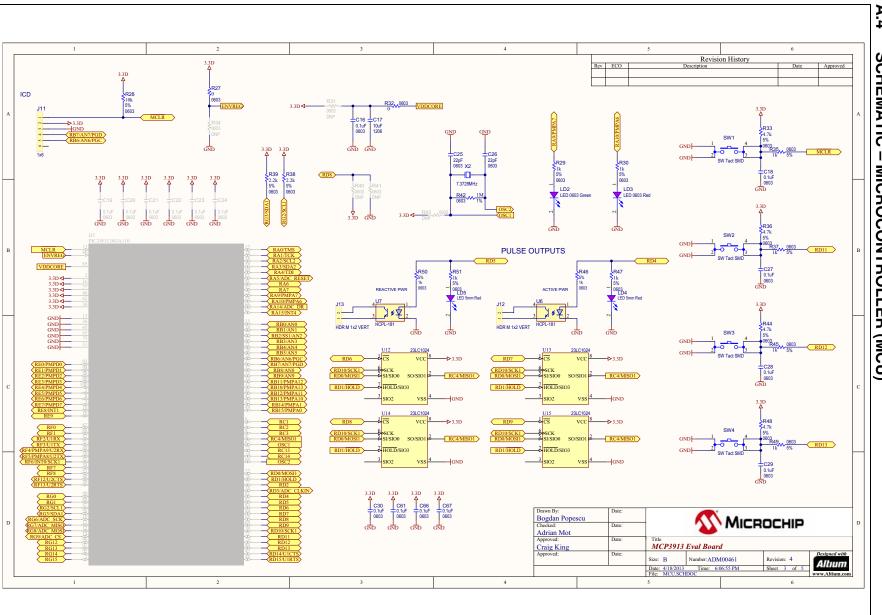
This appendix contains the following schematics of the MCP3913 ADC Evaluation Board for 16-Bit MCUs:

- Schematic Power
- Schematic ADC
- Schematic Microcontroller (MCU)
- Schematic PIM Module
- Schematic LCD and UART
- Board Top Trace and Top Silk
- Board Bottom Trace and Bottom Silk
- Board Layer #2 GND
- Board Layer #3 V DD
- Board Top Silk and Pads
- Board Bottom Silk and Pads

# SCHEMATIC - POWER

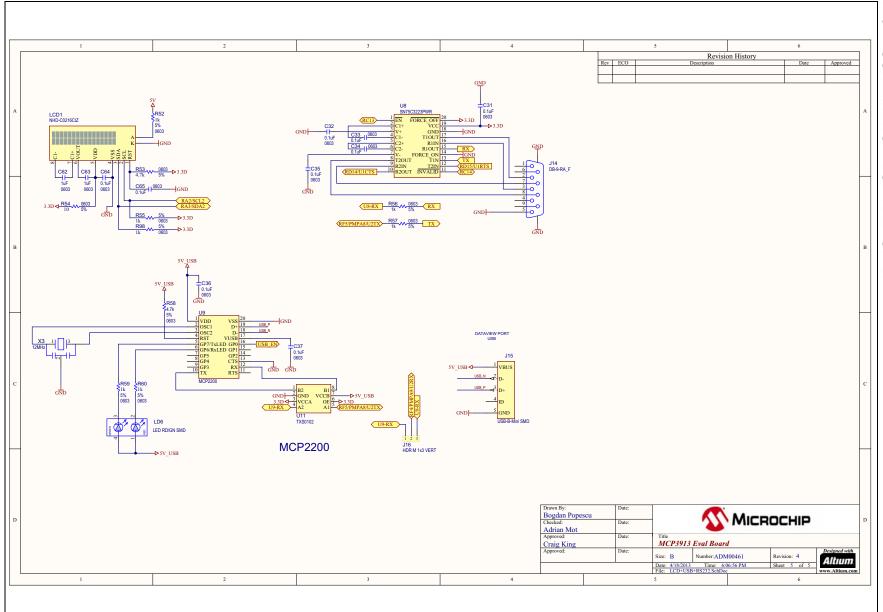


# **A** SCHEMATIC - MICROCONTROLLER (MCU)

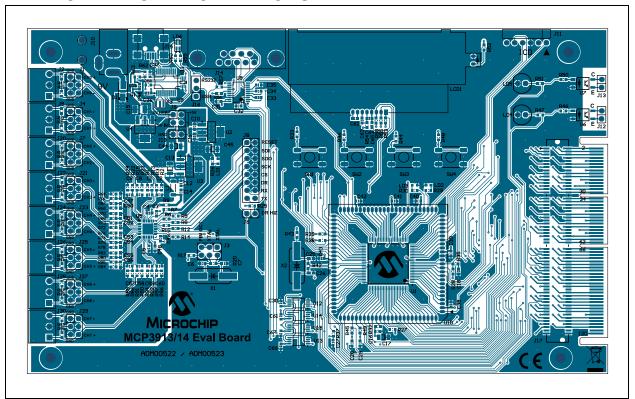


# **A.5** S Revision History Rev ECO CHEMATIC PIM MODULE U10 PIC32MX775 TMS/RA0 TCK/RA1 SCL2/RA2 SDA2/RA3 SDA2fRA3 4 TDIRA4 2 TDORAS 3 TRCLK/RA6 TRD3/RA7 VREF-/CVREF-/AERXD2/PMA/RA9 VREF+/CVREF+/AERXD3/PMA/RA10 AETXCLK-SCLI/MT3/RA14 AETXCLK-SCLI/MT3/RA14 VUSB VCAP/VDDCORE PIM MODULE PGEDI/ANDCAZER POPEDI/ANDCAZER T3CK/AC2TX/RC2 T4CK/AC2RX/RC3 T5CK/SDI1/RC4 OSC1/CLKI/RC12 OSC2/CLKO/RC15 OSCICLKORCI) SDOI-OCI/INTORDO OCERDIA PRIDICA ETXELE/PRDIA ETXELE/PRDIA ETXELE/PRDIA ETXELE/PRDIA ETXELE/PRDIA ETXELE/PRDIA ETXELE/PRDIA ETXELE/PRDIA STITICZED STITICZED STITICZED SCKLI/C3/PRCS2/PRAIA 14RDII OCERDIA ETXDA/SSPRIJI 26RDI 18RDII 36RDII 47RDII 26RDII 26R CIRX/ETXDI/PMD11/RF0 CITX/ETXD0/PMD10/RF1 SDA3/SDI3/U1RX/RF2 SDAS/SIJJ/URZRYPMA9/CN17/RF4 SDAS/SD14/U2RX/PMA9/CN17/RF4 SCLS/SD03/U1TX/PMA8/CN18/RF5 ACIRX/SS4/U5RX/U2CTS/RF12 ACITX/SCK4/U5TX/U2RTS/RF13 C2RX/PMD8/RG0 C2TX/ETXERR/PMD9/RG1 CTX/ETYCER/PMD/9/G1 D1-RG2 D1-RG2 ECOLL-SCC2/U6/YGJBFT-PMAS-CNSRG6 ECESSDA-SDD-UBR/PMA4-CNSRG7 ECESSDA-SDD-UBR/PMA4-CNSRG7 EERXDV/AERXDV/ECRSDV/AERSDV/SCL4/SD02/UJTX/PMA3/CN10/RG8 ERXCLKA/ERSDV/AERXDV/AERSDV/AERSDV/SCL4/SD02/UJTX/PMA3/CN10/RG8 ERXCLKA/ERSDV/AERSDV/AERSDV/SCL4/SD02/UJTX/PMA3/CN10/RG9 TRDD/RG12 TRDD/RG13 AEP/ TRDD/RG13 PMD0/RE0 PMD1/RE1 PMD1/RE1 PMD2/RE2 PMD3/RE3 PMD4/RE4 PMD5/RE5 PMD6/RE6 PMD7/RE7 J17 MEC1-160-02-L-D-A J18 CON MEC1-160-02-X-D-A AERXD0/INT1/RE8 AERXD1/INT2/RE9 TRD2/RG14 AERXERR/RG15 MICROCHIP Adrian Mot C38 0.1uF 0603 C39 0.1uF 0603 C40 0.1uF 0603 C41 0.1uF 0603 C42 0.1uF 0603 C43 0.1uF 0603 MCP3913 Eval Board Craig King Approved: Number: ADM00461 Revision: 4 Altıum. Sheet 4 of 5

# A.6 SCHEMATIC - LCD AND UART



# A.7 BOARD - TOP TRACE AND TOP SILK



# A.8 BOARD - BOTTOM TRACE AND BOTTOM SILK

