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date 09/13/2017

page 1 of 7

SERIES: AE10-EW-DIN | DESCRIPTION: DC-DC CONVERTER

FEATURES

- 10 watts
- high operating temp -40 to +70°C
- 4,000 Vac isolation
- extra wide input voltage 10:1
- input voltage up to 1 kVdc
- OVP protection
- output short circuit protection
- DIN-rail mounted
- EN 62109 approved



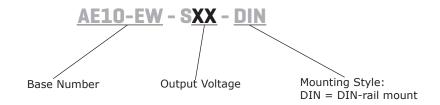


| MODEL | input voltage | | output current | | • | | output power | ripple & noise¹ | efficiency ² |
|-----------------|------------------|-------|-------------------|------------|------------|-----------------------|-------------------|--------------------|-------------------------|
| | range (Vdc) | (Vdc) | min (A) | max (A) | max (W) | max (mVp-p) | typ (%) | | |
| AE10-EW-S5-DIN | 100~1000 | 5 | 0 | 2.00 | 10 | 200 | 72 | | |
| AE10-EW-S9-DIN | 100~1000 | 9 | 0 | 1.11 | 10 | 200 | 76 | | |
| AE10-EW-S24-DIN | 100~1000 | 24 | 0 | 0.42 | 10 | 200 | 80 | | |

Notes:

- 1. Measured at nominal input, 20 MHz bandwidth oscilloscope, with 10 μF electrolytic and 1 μF ceramic capacitors on the output.
- 2. Measured at 200 Vdc input voltage, full load.
- 3. All specifications are measured at Ta=25°C, humidity < 75%, nominal input voltage, and rated output load unless otherwise specified.

PART NUMBER KEY



INPUT

| parameter | conditions/description | min | typ | max | units |
|-------------------------|---------------------------|-----|-----|------|-------|
| operating input voltage | | 100 | | 1000 | Vdc |
| | at 200 Vdc | | | 75 | mA |
| current | at 600 Vdc | | | 25 | mA |
| | at 1000 Vdc | | | 16 | mA |
| | at 200 Vdc | | 7 | | Α |
| inrush current | at 600 Vdc | | 20 | | Α |
| | at 1000 Vdc | | 30 | | Α |
| input fuse | 1 A / 1000 Vdc (external) | | | | |

OUTPUT

| parameter | conditions/description | min | typ | max | units |
|-------------------------|--|-----|-------|-------|-------|
| | 5 Vdc output model | | | 6,000 | μF |
| maximum capacitive load | 9 Vdc output model | | | 4,000 | μF |
| | 24 Vdc output model | | | 470 | μF |
| voltage accuracy | | | ±1 | ±2 | % |
| line regulation | from low line to high line, full load | | ±0.5 | ±1 | % |
| load regulation | from 0% to full load | | ±0.5 | ±1 | % |
| delay time | from $Vin = 0 V to 90\%$ of rated ouptut voltage | | | 1 | S |
| switching frequency | | | | 75 | kHz |
| temperature coefficient | at full load | | ±0.02 | | %/°C |

PROTECTIONS

| parameter | conditions/description | min | typ | max | units |
|--------------------------|--------------------------------|-----|-----|-----|-------|
| | 5 Vdc output model | , | | 7.5 | Vdc |
| over voltage protection | 9 Vdc output model | | | 12 | Vdc |
| 5 . | 24 Vdc output model | | | 28 | Vdc |
| over current protection | automatic recovery | 110 | | | % |
| short circuit protection | continuous, automatic recovery | | | | |

SAFETY AND COMPLIANCE

| parameter | conditions/description | min | typ | max | units |
|---------------------|---|---------------------------|----------|-----|-------|
| isolation voltage | input to output for 1 minute | 4,000 | | | Vac |
| safety approvals | EN 62109 | | | | |
| conducted emissions | CISPR22/EN55022, class A (external circui | t required, see Figure 2 | 2) | | |
| radiated emissions | CISPR22/EN55022, class A (external circui | t required, see Figure 2 | 2) | | |
| ESD | IEC/EN61000-4-2, contact \pm 6kV/air \pm 8k | V, class B | | | |
| radiated immunity | IEC/EN61000-4-3, 10V/m, class A | | | | |
| EFT/burst | IEC/EN61000-4-4, ± 4kV, class B (external circuit required, see Figure 2) | | | | |
| surge | IEC/EN61000-4-5, ± 2kV, class B (externa | l circuit required, see F | igure 2) | | |
| conducted immunity | IEC/EN61000-4-6, 10 Vr.m.s, class A | | | | |
| MTBF | as per MIL-HDBK-217F, 25°C | 300,000 | | | hours |
| RoHS | 2011/65/EU | | | | |

ENVIRONMENTAL

| parameter | conditions/description | min | typ | max | units |
|-----------------------|------------------------|-----|-----|------|-------|
| operating temperature | see derating curves | -40 | | 70 | °C |
| storage temperature | | -40 | | 105 | °C |
| storage humidity | non-condensing | | | 95 | % |
| altitude | | | | 2000 | m |

MECHANICAL

| parameter | conditions/description | min | typ | max | units |
|---------------|--|-----|-----|-----|-------|
| dimensions | 96.10 x 54.00 x 36.60 [3.783 x 2.126 x 1.441 inch] | | | | mm |
| case material | black flame-retardant heat-proof plastic (UL94V-0) | | | | |
| weight | | | 190 | | g |

MECHANICAL DRAWING

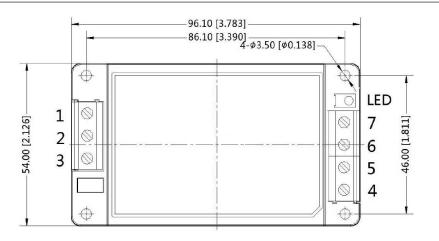
units: mm [inch]

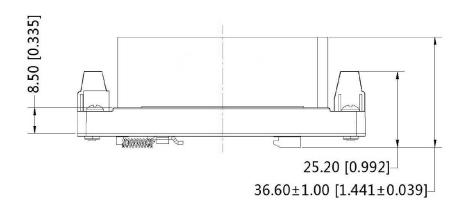
tolerance: $\pm 0.50[\pm 0.020]$

installed on DIN rail TS35 wire range: 24~12 AWG tightening torque: max 0.4 N*m

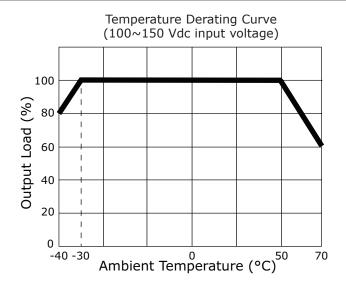
| PIN CONNECTIONS | | | |
|-----------------|----------|--|--|
| PIN | Function | | |
| 1 | -Vin | | |
| 2 | NC | | |
| 3 | +Vin | | |
| 4 | +Vout | | |
| 5 | NC | | |
| 6 | NC | | |
| 7 | -Vout | | |

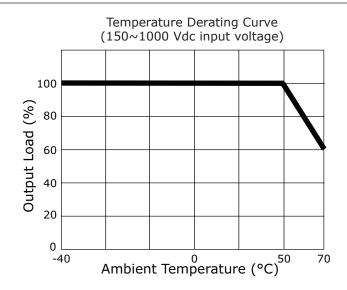
NC=no connection

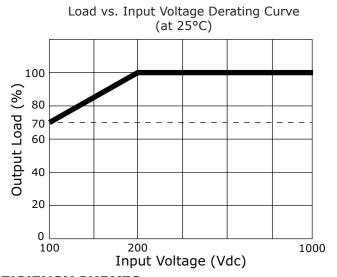




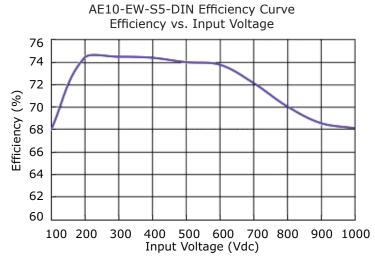
DERATING CURVES

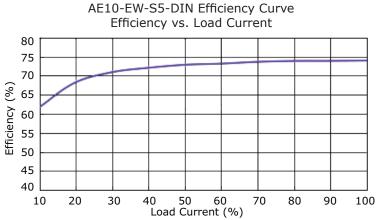




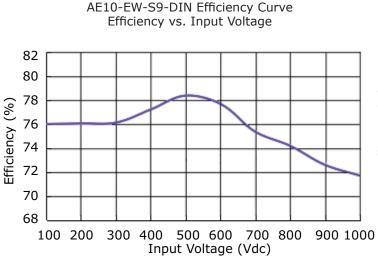


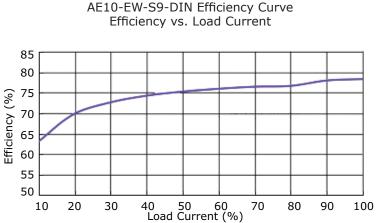
EFFICIENCY CURVES





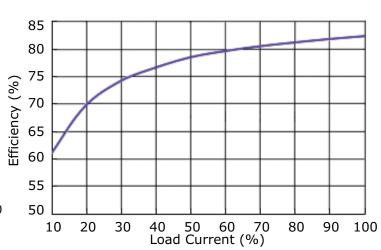
EFFICIENCY CURVES (CONTINUED)





Efficiency vs. Input Voltage 86 84 82 Efficiency (%) 80 78 76 74 72 70 100 200 300 400 500 600 700 800 900 1000 Input Voltage (Vdc)

AE10-EW-S24-DIN Efficiency Curve



AE10-EW-S24-DIN Efficiency Curve

Efficiency vs. Load Current

APPLICATION CIRCUIT

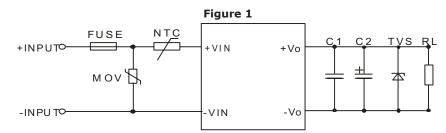


Table 1

| Vout (Vdc) | Fuse | MOV | NTC | C1 (µF) | C2 (µF) | TVS |
|---------------|----------------|---------|--------|------------|------------|----------|
| 5 | 1 A / 1000 Vdc | S14K880 | 10D-11 | 1 | 220 | SMBJ7.0A |
| 9 | 1 A / 1000 Vdc | S14K880 | 10D-11 | 1 | 120 | SMBJ12A |
| 24 | 1 A / 1000 Vdc | S14K880 | 10D-11 | 1 | 68 | SMBJ33A |

EMC RECOMMENDED CIRCUIT

Figure 2

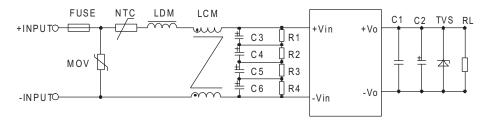


Table 2

| Recommended External Circuit Components | | | | |
|---|---------------|--|--|--|
| FUSE | 1 A/1000 Vdc | | | |
| MOV | S14K880 | | | |
| C3, C4, C5, C6 | 47 μF/400 Vdc | | | |
| R1, R2, R3, R4 | 1 MΩ/2 W | | | |
| NTC | 10D-11 | | | |
| LDM | 4.7 mH/0.38 A | | | |
| LCM | 10 mH | | | |

Note: See also Table 1.

Notes:

C1 is a ceramic capacitor used to filter high frequency noise.
C2 is electrolytic and is recommended to be high frequency and low resistance. For capacitance and current of the capacitor, refer to the datasheet provided by the manufacturer. Capacitance withstand voltage derating should be 80% or above.

REVISION HISTORY

| rev. | description | date |
|------|-----------------|------------|
| 1.0 | initial release | 09/13/2017 |

The revision history provided is for informational purposes only and is believed to be accurate.



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