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## AFBR-3950xxRZ

## High Voltage Galvanic Insulation Link for DC to 50MBaud

## Data Sheet

## Description

Avago Technolgies' AFBR-3950xxRZ is a high voltage galvanic insulation link for DC to 50 MBaud. The AFBR3950xxRZ consists of an optical transmitter and receiver operating at 650 nm wavelength. Pin to pin distance of approximately 25 to 101 mm provides transient voltage suppression in the range of 15 kV to 50 kV .

## Applications

- Drives/Inverters
- Galvanic insulation on one single PCB
- Medium Voltage Power Distributions
- Regulated Distribution Transformers
- Smart Grid on-board Insulations


## Ordering Information

| Part Number | Length | $\mathbf{m m}$ | Voltage Suppression |
| :--- | :---: | :---: | :---: |
| AFBR-395025RZ | 1 inch | 25 | 15 kV |
| AFBR-395050RZ | 2 inch | 50.4 | 27 kV |
| AFBR-395075RZ | 3 inch | 75.8 | 40 kV |
| AFBR-395000RZ | 4 inch | 101.2 | 50 kV |



## Features

- Data transmission at signal rates of DC to 50MBaud
- DC coupled transmitter and receiver with CMOS/TTL input-output for easy designs: no data encoding or digitizing circuitry required
- High noise immunity through receiver IC with integrated photodiode
- RoHS compliant
- Transient voltage suppression in the range of 15 kV up to 50 kV according IEC 60644
- Laser class 1 according to IEC-60825
- Certified according to IEC-60747-5-5
- Housing Material UL-V0 with $\mathrm{CTI} \geq 600$
- Optional 3.3 V or 5 V power supply


## AFBR-3950xxRZ DC to 5MBaud Data Link

## Absolute Maximum Ratings

| Parameter | Symbol | Min. | Max. | Units |
| :--- | :--- | :--- | :--- | :--- |
| Signaling Rate | $\mathrm{f}_{\mathrm{S}}$ | DC | 50 | MBd |
| Storage and Operating Temperature | $\mathrm{T}_{\mathrm{S}, \mathrm{O}}$ | -40 | +85 | ${ }^{\circ} \mathrm{C}$ |
| Receiver Supply Voltage | $\mathrm{V}_{\mathrm{CCRx}}$ | -0.5 | +5.5 | V |
| Receiver Supply Current | $\mathrm{I}_{\mathrm{CCRx}}$ |  | 30 | mA |
| Receiver Output Current | $\mathrm{I}_{\mathrm{OAV}}$ |  | 10 | mA |
| Transmitter Supply Voltage | $\mathrm{V}_{\mathrm{CCTx}}$ | -0.5 | +5.5 | V |
| Transmitter Supply Current |  | $\mathrm{I}_{\mathrm{CCTx}}$ |  | 31 |
| Lead Soldering Cycle ${ }^{[1,2]}$ | Temp | $\mathrm{T}_{\text {SOL }}$ |  | +260 |
|  |  |  | 10 | mA |

## Notes:

1. 1.6 mm below seating plane; wave soldering only
2. MSL class 3

## Attention

Stresses above those listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## Recommended Operating Conditions

| Parameter | Symbol | Min. | Max. | Units |
| :--- | :--- | :--- | :--- | :--- |
| Ambient Temperature | $\mathrm{T}_{\mathrm{A}}$ | -40 | 85 | ${ }^{\circ} \mathrm{C}$ |
| Receiver Power Supply Voltage ${ }^{[1]}$ | V $_{\text {CCRx }}$ | 3.135 | 3.465 | V |
|  |  | 4.75 | 5.25 |  |
| Transmitter Supply Voltage | V $_{\text {CCTx }}$ | 3.135 | 3.465 | V |
|  |  | 4.75 | 5.25 |  |
| Signaling Rate | fs | DC | 50 | MBd |

Notes:

1. $<100 \mathrm{mV}_{\mathrm{p}-\mathrm{p}}$ Noise

All the data in this specification refers to the operating conditions above and over lifetime unless otherwise stated.

## Insulation Characteristics

| Parameter | Symbol | Min. | Max. | Units |
| :---: | :---: | :---: | :---: | :---: |
| Apparent charge at Sample Test stage and Type Test stage after subgroup 1 (method a) ${ }^{[1]}$ | 9pd |  | 5 | pC |
| Apparent charge at Routine Test stage and Type Test stage, Preconditioning (method b) [2] | qpd |  | 5 | pC |
| Maximum Transient Voltage, peak [3] | $V_{\text {IOTM_1 inch }}$ <br> VIOTM_2inch <br> VIOTM_3inch <br> VIOTM_4inch | $\begin{aligned} & 15 \\ & 27 \\ & 40 \\ & 50 \end{aligned}$ |  | kV |
| Maximum Transient Voltage, effective ${ }^{[3]}$ | VISO_1inch <br> VISO_2inch <br> VISO_3inch <br> VISO_4inch | $\begin{aligned} & 10.5 \\ & 19 \\ & 28.1 \\ & 35.2 \end{aligned}$ |  | kV |
| Maximum Working Voltage, peak ${ }^{[4]}$ | VIORM_1inch <br> VIORM_2inch <br> VIORM_3inch <br> VIORM_4inch | $\begin{aligned} & 4.25 \\ & 8.5 \\ & 12.75 \\ & 17.00 \end{aligned}$ |  | kV |
| Maximum Working Voltage, effective ${ }^{\text {[4] }}$ | VIOWM_1inch <br> VIOWM_2inch <br> VIOWM_3inch <br> VIOWM_4inch | $\begin{aligned} & 3 \\ & 6 \\ & 9 \\ & 12 \end{aligned}$ |  | kV |
| Insulation Resistance @ ${\mathrm{Tamb} \text {, max }, \text { min. } 100^{\circ} \mathrm{C}}$ | $\mathrm{R}_{\mathrm{IO}}$ | $10^{11}$ |  | $\Omega$ |
| Insulation Resistance @ $T_{\text {s }}$ | $\mathrm{R}_{\mathrm{IO}}$ | $10^{9}$ |  | $\Omega$ |
| Creepage Distance | 1inch <br> 2inch <br> 3inch <br> 4inch | $\begin{aligned} & 25 \\ & 50.4 \\ & 75.8 \\ & 101.2 \end{aligned}$ |  | mm |
| Clearance Distance | 1inch <br> 2inch <br> 3inch <br> 4inch | $\begin{aligned} & 25 \\ & 50.4 \\ & 75.8 \\ & 101.2 \end{aligned}$ |  | mm |
| Surge Isolation Voltage | $\mathrm{V}_{\text {IOSM }}$ | 12 |  | kV |
| Comparative Tracking Index | CTI | 600 |  |  |
| Pollution degree ${ }^{[5]}$ |  | 2 |  |  |
| Climatic category ${ }^{[6]}$ |  | 40/08 |  |  |
| Maximum ambient Safety temperature | TS | 110 |  | ${ }^{\circ} \mathrm{C}$ |
| Maximum input current | $\mathrm{ISI}^{\prime}$ | 60 |  | mA |
| Maximum output current | Iso | 30 |  | mA |
| Maximum input power dissipation | Psi | 330 |  | mW |
| Maximum output power dissipation | Pso | 165 |  | mW |

## Notes:

1. $V_{\mathrm{pd}(\mathrm{m})}=1.6 \times \mathrm{V}_{\text {IORM }}(=6.8 \mathrm{kV}$ for 1 inch, $=13.6 \mathrm{kV}$ for $2 \mathrm{inch},=20.4 \mathrm{kV}$ for 3 inch,$=27.2 \mathrm{kV}$ for 4 inch$), \mathrm{V}_{\text {ini, }}=\mathrm{V}_{\text {IOTM }}, \mathrm{t}_{\text {ini, }}=60 \mathrm{~s} ; \mathrm{t}_{\mathrm{m}}=10 \mathrm{~s}$
2. $V_{\text {pd }(m)}=1.875 \times V_{\text {IORM }}(=8 \mathrm{kV}$ for 1 inch, $=16 \mathrm{kV}$ for 2 inch, $=24 \mathrm{kV}$ for 3 inch,$=32 \mathrm{kV}$ for 4 inch $), V_{\text {ini,b }}=V_{\text {IOTM }}, t_{\text {ini }, b}=1 \mathrm{~s} ; t_{m}=1 \mathrm{~s}$
3. Altitude up to 2000 m above sea level
4. Pollution degree 2; please note that inhomogeneous field conditions may lead to partial discharge through air for these voltages
5. According IEC-60064-1
6. According IEC-60068-1

Electrical Input Characteristics

| Parameter | Symbol | Min. | Typ. | Max. | Units |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Input Voltage Low | $\mathrm{V}_{\mathrm{IL}}$ |  |  | 0.8 | V |
| Input Voltage High ${ }^{[1]}$ | $\mathrm{V}_{\mathrm{IH}}$ | 2 |  | $\mathrm{~V}_{\mathrm{CCTx}}$ | V |
| Input Capacitance | $\mathrm{C}_{\mathrm{IN}}$ |  | 7 | pF |  |
| Input Resistance | $\mathrm{R}_{\mathrm{IN}}$ | 10 |  |  | $\mathrm{k} \Omega$ |

Notes:

1. Duty Cycle shall be $50 \%$ at 1.5 V

Electrical Output Signal Characteristics

| Parameter | Symbol | Min. | Typ. | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
| High Level Output Voltage | $\mathrm{V}_{\mathrm{OH}}$ | 2.5 | $V_{\text {CCRX }}$ | $\mathrm{V}_{\text {CCRX }}+0.3$ | V |
| Low Level Output Voltage | VOL |  |  | 0.4 | V |
| Output Risetime (10-90\%) [1] | $\mathrm{tr}_{\mathrm{r}}$ |  |  | 5 | ns |
| Output Falltime (90-10\%) ${ }^{[1]}$ | $\mathrm{t}_{\mathrm{f}}$ |  |  | 5 | ns |
| Power Supply Noise Immunity | PSNI | 0.1 | 0.4 |  | Vpp |
| Vcc level to deactivate POR ${ }^{[2]}$ | VPOR_DEACT |  | 2.8 |  | V |
| Vcc level to activate POR [2] | VPOR_ACT |  | 2.6 |  | V |
| POR deactivate delay time ${ }^{[2,3]}$ | tpor-deact_del |  | 10 |  | ms |

Notes:

1. $C_{L}=15 \mathrm{p}, \mathrm{R}_{\mathrm{L}}=50 \mathrm{kOhm} F$
2. A Power-on reset (POR) is both implemented at the Transmitter and the Receiver. It is active below VPOR_DEACT. Once VPOR_DEACT is reached the POR remains active for tpOR-DEACT_DEL. During power down POR starts at VPOR_ACT. During active POR the output signal is low. VPOR_DEACT and VPOR_ACT both apply to Tx and Rx, tPOR-DEACT_DEL POR applies only for the Rx. The delay time of the Tx is typically $\sim 10 \mu \mathrm{~s}$.


Specified Link Performance, $\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{DC}$ to 5 MB Baud, unless otherwise noted.

| Parameter | Symbol | Min. | Typ | Max. | Unit | Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Signaling Rate | $\mathrm{f}_{5}$ | DC |  | 50 | MBd | NRZ |
| Pulse Width Distortion ${ }^{[1]}$ | PWD | -5 |  | +8 | ns | 50MBaud |
| Propagation Delay ${ }^{[2]}$ | $t_{D}$ |  |  | 50 | ns | 50MBaud |
| Skew ${ }^{[3]}$ | ts |  |  | 5 | ns | 50MBaud |
| Supply Current Tx ${ }^{[4]}$ | ICCTx |  | 20 | 31 | mA | 50Mbaud |
| Supply Current Rx ${ }^{[4]}$ | ICCRx |  | 17 | 30 | mA | 50MBaud |

Notes:

1. Provided the following characteristics of the electrical input:
a) no PWD at 1.5 V input level
b) $d U / d t$ between 1 V and 2 V is less than $1 \mathrm{~V} / \mathrm{ns}$
2. Determined from 1.5 V of the rising edge of Data_In to $50 \%$ of the rising edge of Data_Out
3. The $t_{D}$ variation between multiple devices measured for same input conditions and same external signal delay
4. Depends on Supply Voltage and Signal Rate

## Block Diagram - AFBR-3950xxRZ



A low Input signal at Data_in results in a low output signal at Data_out (non-inverted Tx to non-inverted Rx ).
POR remains active during VCC power up, typically until $10 \mu \mathrm{~s}$ for Tx and 10 ms for Rx after 2.8 V is reached. For both Tx and Rx Data_out is low while POR active.

## Recommended chemicals for Cleaning/Degreasing

Alcohols: methyl, isopropyl, isobutyl.
Aliphatics: hexane, heptanes
Other: soap solution, naphtha
Do not use partially halogenated hydrocarbons such as 1.1.1 trichloroethane, ketones such as MEK, acetone, chloroform, ethyl acetate, methylene dichloride, phenol, methylene chloride, or N-methylpyrolldone. Also, Avago does not recommend the use of cleaners that use halogenated hydrocarbons because of their potential environmental harm.

## Recommended Drive Circuit (a) - Top View



Pin Description

| Pin number | Transmitter | Pin number | Receiver |
| :---: | :---: | :---: | :---: |
| 1 | $\mathrm{V}_{\text {CC }} \mathrm{Tx}$ | 5 | No function ${ }^{11]}$ |
| 2 | No function ${ }^{[1]}$ | 6 | $\mathrm{V}_{\text {CCRx }}$ |
| 3 | GND | 7 | GND |
| 4 | Data_in | 8 | Data_out |

Notes:

1. It is recommended to connect this pin to signal ground

## Pinning Schematic

Top View


## Footprint (Top View)

Dimensions in mm
AFBR-395025RZ


AFBR-395050RZ


AFBR-395075RZ


## Footprint (Top View)

Dimensions in mm
AFBR-395000RZ


## Mechanical Dimensions

Dimensions in mm
AFBR-395025RZ


AFBR-395050RZ


## Mechanical Dimensions

Dimensions in mm
AFBR-395075RZ


AFBR-395000RZ


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