imall

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Single Clock Generator AK8113

Features

- Output Frequency Range: 74.17582MHz / 74.25MHz (Selectable)
- Input Frequency: 27MHz
- Low Jitter Performance:
 - **15 ps (Typ.)** Period, 1σ
- Low Current Consumption:
 - 3.8mA (Typ.)
 - Output Load: 15pF (max.)
- Supply Voltage:
 2.7 3.6V
- Operating Temperature Range: -20 to +85°C
- Package:

Block Diagram

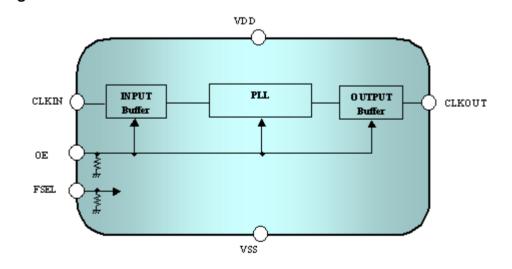
6-pin SON (lead-free) Body Size 2.6mm x 1.6mm

Description

The AK8113 is a single clock generator IC with an integrated PLL. It can generate either a 74.17582MHz or a 74.25MHz clock from a 27MHz master clock input frequency. Through pin control, the output can be enabled or disabled, the frequency can be changed, and the device can be placed in a power-down mode. A high performance PLL locks to the master clock input, generating a low jitter, highly accurate clock output without an external crystal.

Applications

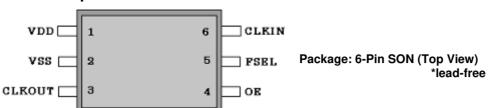
High Definition



AK8113 Single Clock Generator



Pin Descriptions



Pin No.	Pin Name	Pin Type	Description	
1	VDD		Power Supply	
2	VSS		Ground	
3	CLKOUT	OUT	Clock output Output clock frequency is selectable to 74.17582MHz or 74.25MHz by setting the FSEL pin. In power down mode (OE = "L"), this pin is "L".	
4	OE	IN	CLKOUT output enable control "L": CLKOUT="L" and power down. "H": active	(1)
5	FSEL	IN	Clock frequency select "L": 74.25MHz, "H": 74.17582MHz	(1)
6	CLKIN	IN	Clock input (27MHz) Place the AK8113 in power down (OE = "L") mode when an input clock is not supplied. Unstable input to the CLKIN causes the unstable CLKOUT signal. DC input to the CLKIN also causes the unstable CLKOUT signal.	

(1) Internal pull down 100kΩ (Typ.)

Ordering Information

Part Number Marking		Shipping Packaging	Package	Temperature Range	
AK8113L	113(AK8 <u>113</u>)	Tape and Reel	6-pin SON	-20 to 85 °C	



Absolute Maximum Rating

Over operating t	free-air temperature	range unless	otherwise noted (1)
Over operating i	nee-an temperature	range uniess	

Items	Symbol	Ratings	Unit
Supply Voltage	VDD	-0.3 to 4.6	V
Input Voltage	Vin	VSS-0.3 to VDD+0.3	V
Input Current (any pins except supplies)	I _{IN}	± 10	mA
Storage Temperature	Tstg	-55 to 130	°C

Note

(1) Stress beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to absolute-maximum-rating conditions for extended periods may affect device reliability. Electrical parameters are guaranteed only over the recommended operating temperature range.



ESD Sensitive Device

This device is manufactured on a CMOS process, therefore, generically susceptible to damage by excessive static voltage. Failure to observe proper handling and installation procedures can cause damage. AKEMD recommends that this device is handled with appropriate precautions.

Recommended Operation Conditions

Parameter	Symbol	Conditions	Min	Тур	Мах	Unit
Operating Temperature	Та		-20		85	°C
Supply Voltage	VDD		2.7	3.0	3.6	V
Input Clock Frequency	Fin			27		MHz
Input Clock Duty Cycle				50		%
Output Load Capacitance	Cp1	Pin: CLKOUT			15	pF



DC Characteristics

All specifications at VDD: over 2.7 to 3.6V, Ta: -20 to +85°C, Input Frequency: 27MHz, unless otherwise noted

Parameter	Symbol	Conditions	MIN	ТҮР	МАХ	Unit
High Level Input Voltage	V _{IH}	Pin: CLKIN, FSEL, OE	0.8VDD			V
Low Level Input Voltage	VIL	Pin: CLKIN, FSEL, OE			0.2VDD	V
Input Current 1	I∟1	Pin: CLKIN	-10		+10	μA
Input Current 2	I _L 2	Pin: OE, FSEL	-10		+75	μA
High Level Output Voltage	V _{OH}	Pin: CLKOUT I _{OH} =-4mA (VDD=3.0V, Ta=25°C)	0.8VDD			V
Low Level Output Voltage	V _{OL}	Pin: CLKOUT I _{OL} =+4mA (VDD=3.0V, Ta=25°C)			0.2VDD	V
Current Consumption	I _{DD}	No load (VDD=3.0V, Ta=25°C)		3.8		mA
Power down current	I _{pd}	OE="L" FSEL="L" or open		0	10	μA

AC Characteristics

All specifications at VDD: over 2.7 to 3.6V, Ta: -20 to +85°C, Input Frequency: 27MHz, unless otherwise noted

Parameter	Symbol	Conditions	MIN	ТҮР	МАХ	Unit
Output Clock Duty Cycle ^{(2) (3)}			45	50	55	%
Output Clock Rise Time ^{(2) (3)}	t _{rise}	0.2VDD to 0.8VDD		1.8		ns
Output Clock Fall Time ^{(2) (3)}	t _{fall}	0.2VDD to 0.8VDD		1.8		ns
Output Clock Jitter (2) (3)	Jit	Period, 1σ		15		ps
Output Lock Time ⁽¹⁾	t _{lock}	Power-up		1		ms

(1) The time that output reaches the target frequency within accuracy of $\pm 0.1\%$ from the point that the power supply reaches VDD

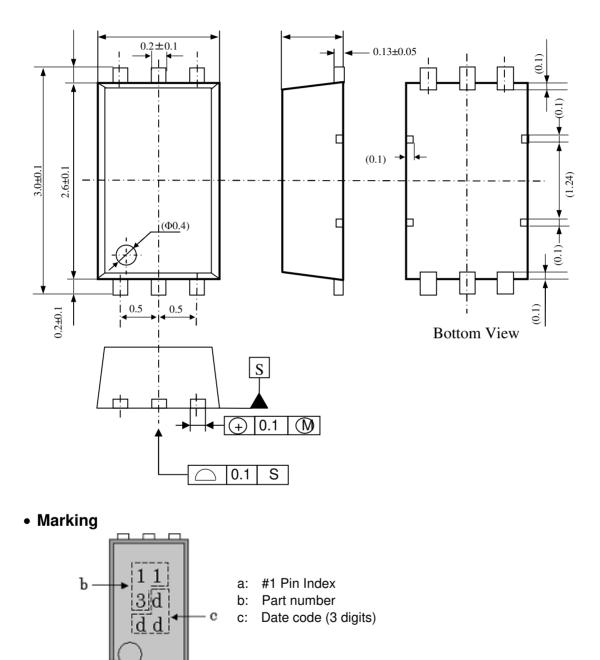
(2) With the load capacitance specified by the recommended operation conditions

(3) Design value



Package Information

• Mechanical data (Units:mm)

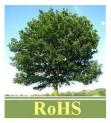


AKM and the logo - *are the brand of AKEMD's IC's and identify that AKEMD continues to offer the best choice for high performance mixed-signal solution under this brand.*

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• RoHS Compliance



All integrated circuits form Asahi Kasei EMD Corporation (AKEMD) assembled in "lead-free" packages* are fully compliant with RoHS.

(*) RoHS compliant products from AKEMD are identified with "Pb free" letter indication on product label posted on the anti-shield bag and boxes.

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 - Note2) A hazard related device or system is one designed or intended for life support or maintenance of safety or for applications in medicine, aerospace, nuclear energy, or other fields, in which its failure to function or perform may reasonably be expected to result in loss of life or in significant injury or damage to person or property.
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