# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



# Contact us

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# 60Amps AEH60 ALH60 (Open Frame) Ultra High Efficiency Half Brick

Total Power: Input Voltages: No. of Outputs: 200 Watts (3.3V @ 60A) 48 V Single

#### **Special Features**

- High efficiency, 3.3V@ 91% (Typical)
- Baseplate (AEH) or Open frame (ALH) design
- · Low output ripple and noise
- · High capacitive load limit on start-up
- Remote sense compensation
- Regulation to zero load
- · Fixed frequency switching
- Industry Std features: Input UVLO, OCP, OVP, OTP; Short ckt protection; ±10% Output Adjust
- Positive or Negative enable logic control option
- Meets Basic Insulation

## Environmental

Operating temperature

-40°C to +100°C Baseplate (AEH60) -40°C to +85°C Ambient (ALH60)

Storage temperature: -55°C to +125°C

Overtemperature protection: 115°C PCB Temperature (Typical) MTBF: >1 million hours

## Safety

UL, cUL 60950 Recognized TUV EN60950 Licensed

## **Electrical Specs**

Input Input range Input Surge Efficiency

Control Enable

### 100V / 100ms 91% @ 3.3V (Typical)

36-75 VDC

TTL compatible (positive or negative enable logic control options)

#### Output Regulation

(Line, Load, Temp) Ripple and Noise<sup>1</sup> Remote Sense Output Voltage Adjust Range Transient Response

Overv Voltage Protection Over Current Protection

Over Temperature Protection

Isolation Voltage

<2%

2% typical (100mVp-p max) Up to 10%Vout

±10% of nominal output

60mV typical output deviation 25% step change 150 μS recovery time

135% nominal output (latching)

115% of full load (latching)

Shutdown - autorecovery mode 1500 Vdc



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ASIA

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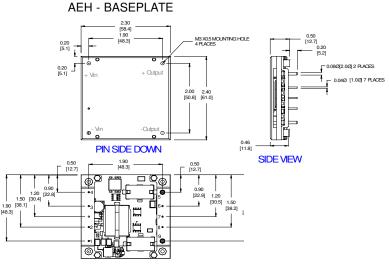


**Ordering Information** 

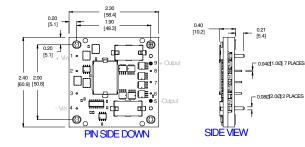
Input Voltage	Output Voltage	Efficiency	Model Number	
48V	3.3V @ 60A	91% (Typ)	AEH60F48(N)	
48V	2.5V @ 60A	90% (Typ)	AEH60G48(N)	
48V	2.5V @ 60A	90% (Typ)	AEH60G48N-6T (see note 3)	
48V	1.8V @ 60A	89% (Typ)	AEH60Y48(N)	
48V	1.2V @ 60A	85% (Typ)	AEH60K48(N)	
48V	3.3V @ 60A	91% (Typ)	ALH60F48(N)	
48V	2.5V @ 60A	90% (Typ)	ALH60G48(N)	
48V	1.8V @ 60A	89% (Typ)	ALH60Y48(N)	
48V	1.2V @ 60A	85% (Typ)	ALH60K48(N)	

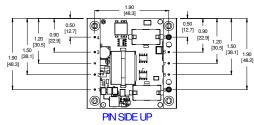
OPTION: suffix "N" = Negative Enable (default is Positive Enable without suffix "N") Standard pin length is 5mm nominal. Consult factory for other options.

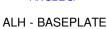
Si	ngle Output
-	
Note	s:
	0 mHz bandwidth. External 10 uF tant. capacitor + 1 uF cer. capacitor placed from +V out to -Vout.
2. R be E C	equires a 2.2 mf, 100V film capacitor connected etween +V in and -V in to meet FCC class A and TS300-386-1 requirements for conducted noise. onsult Factory for filtering information to meet FCC ass B, VDE or EIC specifications.
ve M	uffix -"6" for 3.7mm nominal pin length, "T" for tuned ersion for specific transient response requirement. inimum output capacitance required. Refer to echnical refernce Notes for details.
	Il specifications are typical at nominal line, full load, nd 25°C unless otherwise noted.
	Il specifications subject to change without notice. echanical drawings are for reference only
	echnical Reference Notes should be consulted for etailed information when available
	/arranty: 1yr













**Network Power**