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AM08X5 Real-Time Clock Family

Features

- Ultra-low supply current (all at 3V):
 - 14 nA with RC oscillator
 - 22 nA with RC oscillator and Autocalibration
 - 55 nA with crystal oscillator
- Baseline timekeeping features:
 - 32.768 kHz crystal oscillator with integrated load capacitor/resistor
 - Counters for hundredths, seconds, minutes, hours, date, month, year, century, and week-day
 - Alarm capability on all counters
 - Programmable output clock generation (32.768 kHz to 1 year)
 - Countdown timer with repeat function
 - Automatic leap year calculation
- Advanced timekeeping features:
 - Integrated power optimized RC oscillator
 - Advanced crystal calibration to ± 2 ppm
 - Advanced RC calibration to ± 16 ppm
 - Automatic calibration of RC oscillator to crystal oscillator
 - Watchdog timer with hardware reset
 - 256 bytes of general purpose RAM
- Power management features:
 - Automatic switchover to VBAT
 - External interrupt monitor
 - Programmable low battery detection threshold
 - Programmable analog voltage comparator
- I²C (up to 400 kHz) and 3-wire or 4-wire SPI (up to 2 MHz) serial interfaces available
- Operating voltage 1.5-3.6 V
- Clock and RAM retention voltage 1.5-3.6 V
- Operating temperature -40 to 85 °C
- All inputs include Schmitt Triggers
- 3x3 mm QFN-16 package



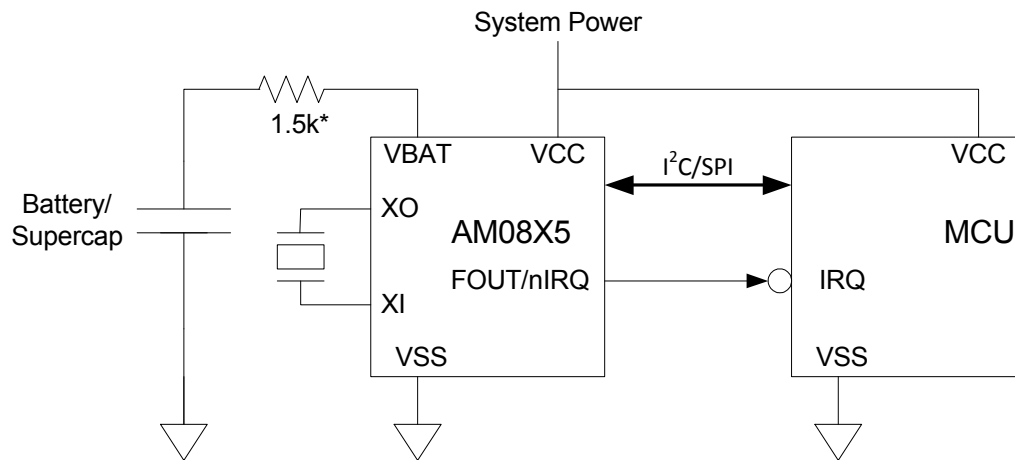
Applications

- Smart cards
- Wireless sensors and tags
- Medical electronics
- Utility meters
- Data loggers
- Appliances
- Handsets
- Consumer electronics
- Communications equipment

Description

The Ambiq Micro AM08X5 Real-Time Clock family provides a groundbreaking combination of ultra-low power coupled with a highly sophisticated feature set. With power requirements significantly lower than any other industry RTC (as low as 14 nA), these are the first semiconductors based on Ambiq Micro's innovative SPOT™ (Subthreshold Power Optimized Technology) CMOS platform. The AM08X5 includes on-chip oscillators to provide minimum power consumption, full RTC functions including battery backup and programmable counters and alarms for timer and watchdog functions, and either an I²C or SPI serial interface for communication with a host controller.

Typical Application Circuit



* Total battery series impedance = 1.5k ohms, which may require an external resistor

Contents

1. Family Summary	10
2. Package Pins	10
2.1. Pin Configuration and Connections	10
2.2. Pin Descriptions	11
3. Digital Architecture Summary	13
4. Electrical Specifications	14
4.1. Absolute Maximum Ratings	14
4.2. Power Supply Parameters	14
4.3. Operating Parameters	16
4.4. Oscillator Parameters	17
4.5. V _{CC} Supply Current	19
4.6. V _{BAT} Supply Current	23
4.7. BREF Electrical Characteristics	26
4.8. I ² C AC Electrical Characteristics	27
4.9. SPI AC Electrical Characteristics	28
4.10. Power On AC Electrical Characteristics	30
5. Functional Description	31
5.1. I ² C Interface	32
5.1.1. Bus Not Busy	32
5.1.2. Start Data Transfer	33
5.1.3. Stop Data Transfer	33
5.1.4. Data Valid	33
5.1.5. Acknowledge	33
5.1.6. Offset Address Transmission	34
5.1.7. Write Operation	34
5.1.8. Read Operation	34
5.2. SPI Interface	35
5.2.1. Write Operation	35
5.2.2. Read Operation	35
5.3. XT Oscillator	36
5.4. RC Oscillator	36
5.5. RTC Counter Access	36
5.6. Hundredths Synchronization	36
5.7. Generating Hundredths of a Second	37
5.8. Watchdog Timer	37
5.9. Digital Calibration	37
5.9.1. XT Oscillator Digital Calibration	37
5.9.2. RC Oscillator Digital Calibration	38
5.10. Autocalibration	39
5.10.1. Autocalibration Operation	39
5.10.2. XT Autocalibration Mode	39
5.10.3. RC Autocalibration Mode	40
5.10.4. Autocalibration Frequency and Control	40
5.10.5. Autocalibration Filter (AF) Pin	40
5.10.6. Autocalibration Fail	40
5.11. Oscillator Failure Detection	41
5.12. Interrupts	41
5.12.1. Interrupt Summary	41
5.12.2. Alarm Interrupt AIRQ	42

5.12.3. Countdown Timer Interrupt TIRQ	42
5.12.4. Watchdog Timer Interrupt WIRQ	42
5.12.5. Battery Low Interrupt BLIRQ	42
5.12.6. External Interrupts X1IRQ and X2IRQ	42
5.12.7. Oscillator Fail Interrupt OFIRQ	43
5.12.8. Autocalibration Fail Interrupt ACIRQ	43
5.12.9. Servicing Interrupts	43
5.13. Power Control and Switching	43
5.13.1. Battery Low Flag and Interrupt	44
5.13.2. Analog Comparator	45
5.13.3. Pin Control and Leakage Management	45
5.13.4. Power Up Timing	45
5.14. Software Reset	46
5.15. Trickle Charger	46
6. Registers	47
6.1. Register Definitions and Memory Map	47
6.2. Time and Date Registers	49
6.2.1. 0x00 - Hundredths	49
6.2.2. 0x01 - Seconds	49
6.2.3. 0x02 - Minutes	50
6.2.4. 0x03 - Hours	50
6.2.5. 0x04 - Date	51
6.2.6. 0x05 - Months	52
6.2.7. 0x06 - Years	52
6.2.8. 0x07 - Weekday	53
6.3. Alarm Registers	53
6.3.1. 0x08 - Hundredths Alarm	53
6.3.2. 0x09 - Seconds Alarm	54
6.3.3. 0x0A - Minutes Alarm	54
6.3.4. 0x0B - Hours Alarm	55
6.3.5. 0x0C - Date Alarm	56
6.3.6. 0x0D - Months Alarm	56
6.3.7. 0x0E - Weekday Alarm	57
6.4. Configuration Registers	57
6.4.1. 0x0F - Status (Read Only)	57
6.4.2. 0x10 - Control1	58
6.4.3. 0x11 - Control2	59
6.4.4. 0x12 - Interrupt Mask	60
6.4.5. 0x13 - SQW	60
6.5. Calibration Registers	62
6.5.1. 0x14 - Calibration XT	62
6.5.2. 0x15 - Calibration RC Upper	62
6.5.3. 0x16 - Calibration RC Lower	63
6.6. Interrupt Polarity Control Register	64
6.6.1. 0x17 - Interrupt Polarity Control	64
6.7. Timer Registers	64
6.7.1. 0x18 - Countdown Timer Control	64
6.7.2. 0x19 - Countdown Timer	66
6.7.3. 0x1A - Timer Initial Value	66
6.7.4. 0x1B - Watchdog Timer	67
6.8. Oscillator Registers	68
6.8.1. 0x1C - Oscillator Control	68
6.8.2. 0x1D - Oscillator Status Register	68

6.9. Miscellaneous Registers	69
6.9.1. 0x1F - Configuration Key	69
6.10. Analog Control Registers	70
6.10.1. 0x20 - Trickle	70
6.10.2. 0x21 - BREF Control	70
6.10.3. 0x26 – AFCTRL	71
6.10.4. 0x27 – Batmode IO Register	72
6.10.5. 0x2F – Analog Status Register (Read Only)	72
6.10.6. 0x30 – Output Control Register	73
6.11. ID Registers	73
6.11.1. 0x28 – ID0 - Part Number Upper Register (Read Only)	73
6.11.2. 0x29 – ID1 - Part Number Lower Register (Read Only)	73
6.11.3. 0x2A – ID2 - Part Revision (Read Only)	74
6.11.4. 0x2B – ID3 – Lot Lower (Read Only)	74
6.11.5. 0x2C – ID4 – ID Upper (Read Only)	74
6.11.6. 0x2D – ID5 – Unique Lower (Read Only)	75
6.11.7. 0x2E – ID6 – Wafer (Read Only)	75
6.12. Ram Registers	76
6.12.1. 0x3F - Extension RAM Address	76
6.12.2. 0x40 - 0x7F – Standard RAM	76
6.12.3. 0x80 - 0xFF – Alternate RAM	76
7. Package Mechanical Information	77
8. Reflow Profile	78
9. Ordering Information	79
10. Document Revision History	79
11. Contact Information	81
12. Legal Information and Disclaimers	81

List of Figures

Figure 1. Pin Configuration Diagram	10
Figure 2. Digital Architecture Summary	13
Figure 3. Power Supply Switchover	14
Figure 4. Calibrated RC Oscillator Typical Frequency Variation vs. Temperature	18
Figure 5. Uncalibrated RC Oscillator Typical Frequency Variation vs. Temperature	18
Figure 6. Typical VCC Current vs. Temperature in XT Mode	20
Figure 7. Typical VCC Current vs. Temperature in RC Mode	20
Figure 8. Typical VCC Current vs. Temperature in RC Autocalibration Mode	21
Figure 9. Typical VCC Current vs. Voltage, Different Modes of Operation	21
Figure 10. Typical VCC Current vs. Voltage, I ² C and SPI Burst Read/Write	22
Figure 11. Typical VBAT Current vs. Temperature in XT Mode	23
Figure 12. Typical VBAT Current vs. Temperature in RC Mode	24
Figure 13. Typical VBAT Current vs. Temperature in RC Autocalibration Mode	24
Figure 14. Typical VBAT Current vs. Voltage, Different Modes of Operation	25
Figure 15. Typical VBAT Current vs. Voltage in VCC Power State	25
Figure 16. I ² C AC Parameter Definitions	27
Figure 17. SPI AC Parameter Definitions – Input	28
Figure 18. SPI AC Parameter Definitions – Output	28
Figure 19. Power On AC Electrical Characteristics	30
Figure 20. Detailed Block Diagram	31
Figure 21. Basic I ² C Conditions	32
Figure 22. I ² C Acknowledge Address Operation	33
Figure 23. I ² C Address Operation	33
Figure 24. I ² C Offset Address Transmission	34
Figure 25. I ² C Write Operation	34
Figure 26. I ² C Read Operation	34
Figure 27. SPI Write Operation	35
Figure 28. SPI Read Operation	36
Figure 29. Power States	44
Figure 30. Power Up Timing	46
Figure 31. Trickle Charger	46
Figure 32. Package Mechanical Diagram	77
Figure 33. Reflow Soldering Diagram	78

List of Tables

Table 1: Family Summary	10
Table 2: Pin Connections	10
Table 3: Pin Descriptions	11
Table 4: Absolute Maximum Ratings	14
Table 5: Power Supply and Switchover Parameters	15
Table 6: Operating Parameters	16
Table 7: Oscillator Parameters	17
Table 8: VCC Supply Current	19
Table 9: VBAT Supply Current	23
Table 10: BREF Parameters	26
Table 11: I ² C AC Electrical Parameters	27
Table 12: SPI AC Electrical Parameters	28
Table 13: Power On AC Electrical Parameters	30
Table 14: Autocalibration Modes	40
Table 15: Interrupt Summary	42
Table 16: Register Definitions (0x00 to 0x0F)	47
Table 17: Register Definitions (0x10 to 0xFF)	48
Table 18: Hundredths Register	49
Table 19: Hundredths Register Bits	49
Table 20: Seconds Register	49
Table 21: Seconds Register Bits	49
Table 22: Minutes Register	50
Table 23: Minutes Register Bits	50
Table 24: Hours Register (12 Hour Mode)	50
Table 25: Hours Register Bits (12 Hour Mode)	50
Table 26: Hours Register (24 Hour Mode)	51
Table 27: Hours Register Bits (24 Hour Mode)	51
Table 28: Date Register	51
Table 29: Date Register Bits	51
Table 30: Months Register	52
Table 31: Months Register Bits	52
Table 32: Years Register	52
Table 33: Years Register Bits	52
Table 34: Weekdays Register	53
Table 35: Weekdays Register Bits	53
Table 36: Hundredths Alarm Register	53
Table 37: Hundredths Alarm Register Bits	53
Table 38: Seconds Alarm Register	54
Table 39: Seconds Alarm Register Bits	54
Table 40: Minutes Alarm Register	54
Table 41: Minutes Alarm Register Bits	54
Table 42: Hours Alarm Register (12 Hour Mode)	55
Table 43: Hours Alarm Register Bits (12 Hour Mode)	55
Table 44: Hours Alarm Register (24 Hour Mode)	55
Table 45: Hours Alarm Register Bits (24 Hour Mode)	55
Table 46: Date Alarm Register	56
Table 47: Date Alarm Register Bits	56
Table 48: Months Alarm Register	56
Table 49: Months Alarm Register Bits	56
Table 50: Weekdays Alarm Register	57
Table 51: Weekdays Alarm Register Bits	57
Table 52: Status Register	57

Table 53: Status Register Bits	57
Table 54: Control1 Register	58
Table 55: Control1 Register Bits	58
Table 56: Control2 Register	59
Table 57: Control2 Register Bits	59
Table 58: nIRQ2 Pin Control	59
Table 59: FOUT/nIRQ Pin Control	59
Table 60: Interrupt Mask Register	60
Table 61: Interrupt Mask Register Bits	60
Table 62: SQW Register	60
Table 63: SQW Register Bits	61
Table 64: Square Wave Function Select	61
Table 65: Calibration XT Register	62
Table 66: Calibration XT Register Bits	62
Table 67: Calibration RC Upper Register	62
Table 68: Calibration RC Upper Register Bits	63
Table 69: CMDR Function	63
Table 70: Calibration RC Lower Register	63
Table 71: Calibration RC Lower Register Bits	63
Table 72: Interrupt Polarity Control Register	64
Table 73: Interrupt Polarity Control Register Bits	64
Table 74: Countdown Timer Control Register	64
Table 75: Countdown Timer Control Register Bits	64
Table 76: Repeat Function	65
Table 77: Countdown Timer Function Select	65
Table 78: Countdown Timer Register	66
Table 79: Countdown Timer Register Bits	66
Table 80: Timer Initial Value Register	66
Table 81: Timer Initial Value Register Bits	67
Table 82: Watchdog Timer Register	67
Table 83: Watchdog Timer Register Bits	67
Table 84: Watchdog Timer Frequency Select	67
Table 85: Oscillator Control Register	68
Table 86: Oscillator Control Register Bits	68
Table 87: Oscillator Status Register	68
Table 88: Oscillator Status Register Bits	69
Table 89: Configuration Key Register	69
Table 90: Configuration Key Register Bits	69
Table 91: Trickle Register	70
Table 92: Trickle Register Bits	70
Table 93: Trickle Charge Output Resistor	70
Table 94: BREF Control Register	70
Table 95: BREF Control Register Bits	71
Table 96: VBAT Reference Voltage	71
Table 97: AFCTRL Register	71
Table 98: AFCTRL Register Bits	71
Table 99: Batmode IO Register	72
Table 100: Batmode IO Register Bits	72
Table 101: Analog Status Register	72
Table 102: Analog Status Register Bits	72
Table 103: Output Control Register	73
Table 104: Output Control Register Bits	73
Table 105: 28 – ID0 – Part Number Upper Register	73
Table 106: 28 – ID1 – Part Number Lower Register	73

Table 107: 2A – ID2 – Part Revision Register	74
Table 108: 2A – ID2 – Part Revision Register Bits	74
Table 109: 2B – ID3 – Lot Lower Register	74
Table 110: 2B – ID3 – Lot Lower Register Bits	74
Table 111: 2C – ID4 – ID Upper Register	74
Table 112: 2C – ID4 – ID Upper Register Bits	75
Table 113: 2D – ID5 – ID Lower Register	75
Table 114: 2D – ID5 – ID Lower Register Bits	75
Table 115: 2E – ID6 – Wafer Register	75
Table 116: 2E – ID6 – Wafer Register Bits	75
Table 117: 3F – Extension RAM Address Register	76
Table 118: 3F – Extension RAM Address Register Bits	76
Table 119: Reflow Soldering Requirements (Pb-free assembly)	78
Table 120: Ordering Information	79
Table 121: Document Revision History	79

1. Family Summary

The AM08X5 family consists of several members (see Table 1). All devices are supplied in a standard 3x3 mm QFN-16 package. Members of the software and pin compatible AM18X5 RTC with Power Management family are also listed.

Table 1: Family Summary

Part #	Baseline Timekeeping		Advanced Timekeeping				Power Management				Interface
	XT Osc	Number of GP Outputs	RC Osc	Calib/ Auto-calib	Watch-dog	RAM (B)	VBAT Switch	Reset Mgmt	Ext Int	Power Switch and Sleep FSM	
AM0805	■	3	■	■	■	256	■		■		I ² C
AM0815	■	2	■	■	■	256	■		■		SPI
Software and Pin Compatible AM18X5 Family Components											
AM1805	■	4	■	■	■	256	■	■	■	■	I ² C
AM1815	■	3	■	■	■	256	■	■	■	■	SPI

2. Package Pins

2.1 Pin Configuration and Connections

Figure 1 and Table 2 show the QFN-16 pin configurations for the AM08X5 parts. Pins labeled NC must be left unconnected. The thermal pad, pin 17, on the QFN-16 packages must be connected to VSS.

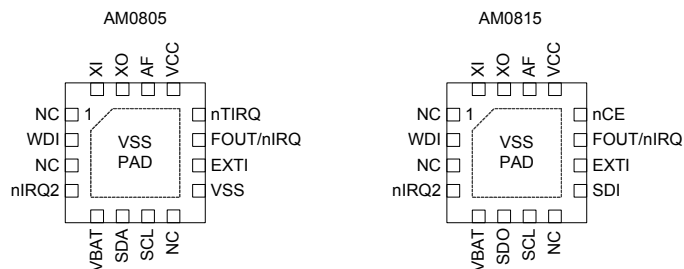


Figure 1. Pin Configuration Diagram

Table 2: Pin Connections

Pin Name	Pin Type	Function	Pin Number	
			AM0805	AM0815
VSS	Power	Ground	9,17	17
VCC	Power	System power supply	13	13
XI	XT	Crystal input	16	16
XO	XT	Crystal output	15	15
AF	Output	Autocalibration filter	14	14

Table 2: Pin Connections

Pin Name	Pin Type	Function	Pin Number	
			AM0805	AM0815
VBAT	Power	Battery power supply	5	5
SCL	Input	I ² C or SPI interface clock	7	7
SDO	Output	SPI data output		6
SDI	Input	SPI data input		9
nCE	Input	SPI chip select		12
SDA	Input	I ² C data input/output	6	
EXTI	Input	External interrupt input	10	10
WDI	Input	Watchdog reset input	2	2
FOUT/nIRQ	Output	Int 1/function output	11	11
nIRQ2	Output	Int 2 output	4	4
nTIRQ	Output	Timer interrupt output	12	

2.2 Pin Descriptions

Table 3 provides a description of the pin connections.

Table 3: Pin Descriptions

Pin Name	Description
VSS	Ground connection. In the QFN-16 packages the ground slug on the bottom of the package must be connected to VSS.
VCC	Primary power connection. If a single power supply is used, it must be connected to VCC.
VBAT	Battery backup power connection. If a backup battery is not present, VBAT must be connected directly to VSS, but it may also be used to provide the analog input to the internal comparator (see Analog Comparator).
XI	Crystal oscillator input connection.
XO	Crystal oscillator output connection.
AF	Autocalibration filter connection. A 47pF ceramic capacitor must be placed between this pin and VSS for improved Autocalibration mode timing accuracy.
SCL	I/O interface clock connection. It provides the SCL input in both I ² C and SPI interface parts. A pull-up resistor is required on this pin.
SDA (only available in I ² C environments)	I/O interface I ² C data connection. A pull-up resistor is required on this pin.
SDO (only available in SPI environments)	I/O interface SPI data output connection.
SDI	I/O interface SPI data input connection.
nCE (only available in SPI environments)	I/O interface SPI chip select input connection. It is an active low signal. A pull-up resistor is recommended to be connected to this pin to ensure it is not floating. A pull-up resistor also prevents inadvertent writes to the RTC during power transitions.

Table 3: Pin Descriptions

Pin Name	Description
EXTI	External interrupt input connection. It may be used to generate an External 1 interrupt with polarity selected by the EX1P bit if enabled by the EX1E bit. The value of the EXTI pin may be read in the EXIN register bit. This pin does not have an internal pull-up or pull-down resistor and so one must be added externally. It must not be left floating or the RTC may consume higher current. Instead, it must be connected directly to either VCC or VSS if not used.
WDI	Watchdog Timer reset input connection. It may also be used to generate an External 2 interrupt with polarity selected by the EX2P bit if enabled by the EX2E bit. The value of the WDI pin may be read in the WDIN register bit. This pin does not have an internal pull-up or pull-down resistor and so one must be added externally. It must not be left floating or the RTC may consume higher current. Instead, it must be connected directly to either VCC or VSS if not used.
FOUT/nIRQ	<p>Primary interrupt output connection. This pin is an open drain output. An external pull-up resistor must be added to this pin. It should be connected to the host device and is used to indicate when the RTC can be accessed via the serial interface. FOUT/nIRQ may be configured to generate several signals as a function of the OUT1S field (see 0x11 - Control2). FOUT/nIRQ is also asserted low on a power up until the AM08X5 has exited the reset state and is accessible via the I/O interface.</p> <ol style="list-style-type: none"> 1. FOUT/nIRQ can drive the value of the OUT bit. 2. FOUT/nIRQ can drive the inverse of the combined interrupt signal IRQ (see Interrupts). 3. FOUT/nIRQ can drive the square wave output (see 0x13 - SQW) if enabled by SQWE. 4. FOUT/nIRQ can drive the inverse of the alarm interrupt signal AIRQ (see Interrupts).
nIRQ2	<ol style="list-style-type: none"> 1. Secondary interrupt output connection. It is an open drain output. This pin can be left floating if not used. nIRQ2 may be configured to generate several signals as a function of the OUT2S field (see 0x11 - Control2). nIRQ2 can drive the value of the OUTB bit. 2. nIRQ2 can drive the square wave output (see 0x13 - SQW) if enabled by SQWE. 3. nIRQ2 can drive the inverse of the combined interrupt signal IRQ (see Interrupts). 4. nIRQ2 can drive the inverse of the alarm interrupt signal AIRQ (see Interrupts). 5. nIRQ2 can drive either sense of the timer interrupt signal TIRQ.
nTIRQ (only available in I ² C environments)	Timer interrupt output connection. It is an open drain output. nTIRQ always drives the active low nTIRQ signal. If this pin is used, an external pull-up resistor must be added to this pin. If the pin is not used, it can be left floating.

3. Digital Architecture Summary

Figure 2 illustrates the overall architecture of the pin inputs and outputs of the AM08X5.

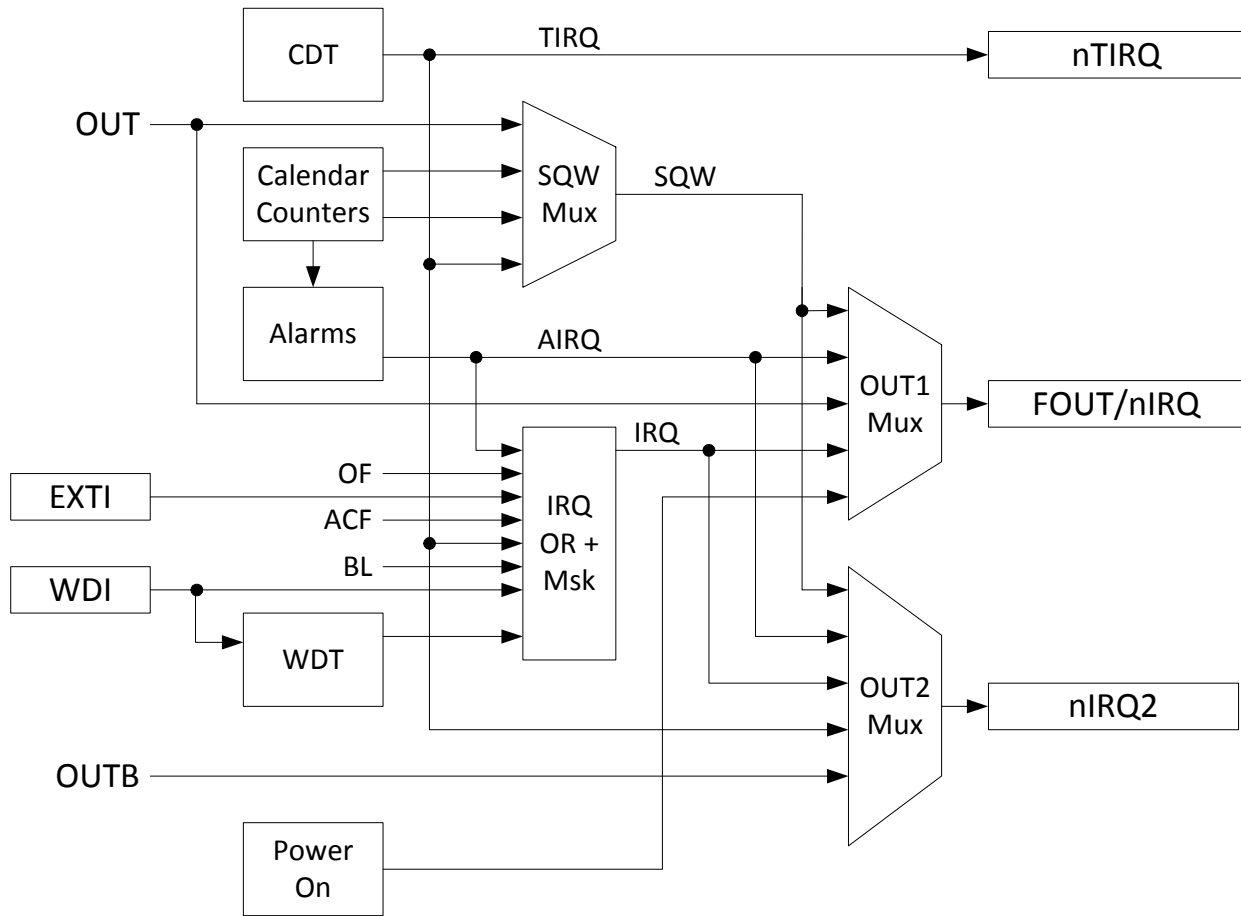


Figure 2. Digital Architecture Summary

4. Electrical Specifications

4.1 Absolute Maximum Ratings

Table 4 lists the absolute maximum ratings.

Table 4: Absolute Maximum Ratings

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V_{CC}	System Power Voltage		-0.3		3.8	V
V_{BAT}	Battery Voltage		-0.3		3.8	V
V_I	Input voltage	VCC Power state	-0.3		$V_{CC} + 0.3$	V
V_I	Input voltage	VBAT Power state	-0.3		$V_{BAT} + 0.3$	V
V_O	Output voltage	VCC Power state	-0.3		$V_{CC} + 0.3$	V
V_O	Output voltage	VBAT Power state	-0.3		$V_{BAT} + 0.3$	V
I_I	Input current		-10		10	mA
I_O	Output current		-20		20	mA
V_{ESD}	ESD Voltage	CDM			± 500	V
		HBM			± 4000	V
I_{LU}	Latch-up Current				100	mA
T_{STG}	Storage Temperature		-55		125	°C
T_{OP}	Operating Temperature		-40		85	°C
T_{SLD}	Lead temperature	Hand soldering for 10 seconds			300	°C
T_{REF}	Reflow soldering temperature	Reflow profile per JEDEC J-STD-020D.1			260	°C

4.2 Power Supply Parameters

Figure 3 and Table 5 describe the power supply and switchover parameters. See Power Control and Switching for a detailed description of the operations.

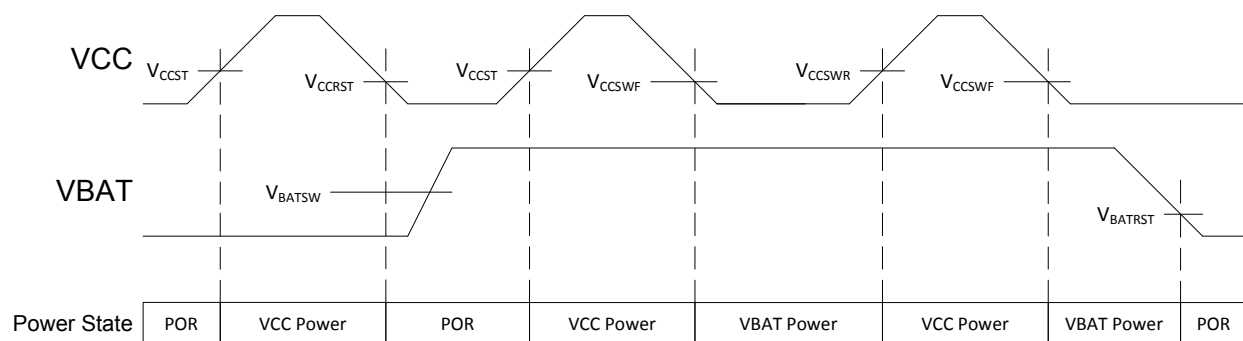


Figure 3. Power Supply Switchover


 For Table 5, $T_A = -40\text{ }^\circ\text{C}$ to $85\text{ }^\circ\text{C}$, TYP values at $25\text{ }^\circ\text{C}$.

Table 5: Power Supply and Switchover Parameters

SYMBOL	PARAMETER	PWR	TYPE	POWER STATE	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V_{CC}	System Power Voltage	VCC	Static	VCC Power	Clocks operating and RAM and registers retained	1.5		3.6	V
V_{CCIO}	VCC I/O Interface Voltage	VCC	Static	VCC Power	I ² C or SPI operation	1.5		3.6	V
V_{CCST}	VCC Start-up Voltage ⁽¹⁾	VCC	Rising	POR → VCC Power		1.6			V
V_{CCRST}	VCC Reset Voltage	VCC	Falling	VCC Power → POR	$V_{BAT} < V_{BAT,MIN}$ or no V_{BAT}		1.3	1.5	V
V_{CCSWR}	VCC Rising Switch-over Threshold Voltage	VCC	Rising	VBAT Power → VCC Power	$V_{BAT} \geq V_{BATRST}$		1.6	1.7	V
V_{CCSWF}	VCC Falling Switch-over Threshold Voltage	VCC	Falling	VCC Power → VBAT Power	$V_{BAT} \geq V_{BATSW,MIN}$	1.2	1.5		V
V_{CCSWH}	VCC Switchover Threshold Hysteresis ⁽²⁾	VCC	Hyst.	VCC Power ↔ VBAT Power			70		mV
V_{CCFS}	VCC Falling Slew Rate to switch to VBAT state ⁽⁴⁾	VCC	Falling	VCC Power → VBAT Power	$V_{CC} < V_{CCSW,MAX}$	0.7	1.4		V/ms
V_{BAT}	Battery Voltage	VBAT	Static	VBAT Power	Clocks operating and RAM and registers retained	1.4		3.6	V
V_{BATSW}	Battery Switchover Voltage Range ⁽⁵⁾	VBAT	Static	VCC Power → VBAT Power		1.6		3.6	V
V_{BATRST}	Falling Battery POR Voltage ⁽⁷⁾	VBAT	Falling	VBAT Power → POR	$V_{CC} < V_{CCSWF}$		1.1	1.4	V
V_{BMRG}	V_{BAT} Margin above V_{CC} ⁽³⁾	VBAT	Static	VBAT Power		200			mV
V_{BATESR}	V_{BAT} supply series resistance ⁽⁶⁾	VBAT	Static	VBAT Power		1.0	1.5		kΩ

⁽¹⁾ V_{CC} must be above V_{CCST} to exit the POR state, independent of the V_{BAT} voltage.
⁽²⁾ Difference between V_{CCSWR} and V_{CCSWF} .
⁽³⁾ V_{BAT} must be higher than V_{CC} by at least this voltage to ensure the AM08X5 remains in the VBAT Power state.
⁽⁴⁾ Maximum VCC falling slew rate to guarantee correct switchover to VBAT Power state. There is no V_{CC} falling slew rate requirement if switching to the VBAT power source is not required.
⁽⁵⁾ V_{BAT} voltage to guarantee correct transition to VBAT Power state when V_{CC} falls.
⁽⁶⁾ Total series resistance of the power source attached to the VBAT pin. The optimal value is 1.5kΩ, which may require an external resistor. VBAT power source ESR + external resistor value = 1.5kΩ.
⁽⁷⁾ V_{BATRST} is also the static voltage required on V_{BAT} for register data retention.

4.3 Operating Parameters

Table 6 lists the operating parameters.



For Table 6, $T_A = -40\text{ }^{\circ}\text{C}$ to $85\text{ }^{\circ}\text{C}$, TYP values at $25\text{ }^{\circ}\text{C}$.

Table 6: Operating Parameters

SYMBOL	PARAMETER	TEST CONDITIONS	V_{CC}	MIN	TYP	MAX	UNIT
V_{T+}	Positive-going Input Threshold Voltage		3.0V		1.5	2.0	V
			1.8V		1.1	1.25	
V_{T-}	Negative-going Input Threshold Voltage		3.0V	0.8	0.9		V
			1.8V	0.5	0.6		
I_{LEAK}	Input leakage current		3.0V		0.02	80	nA
C_1	Input capacitance				3		pF
V_{OH}	High level output voltage on push-pull outputs		1.7V – 3.6V	$0.8 \cdot V_{CC}$			V
V_{OL}	Low level output voltage		1.7V – 3.6V			$0.2 \cdot V_{CC}$	V
I_{OH}	High level output current on push-pull outputs	$V_{OH} = 0.8 \cdot V_{CC}$	1.7V	-2	-3.8		mA
			1.8V	-3	-4.3		
			3.0V	-7	-11		
			3.6V	-8.8	-15		
I_{OL}	Low level output current	$V_{OL} = 0.2 \cdot V_{CC}$	1.7V	3.3	5.9		mA
			1.8V	6.1	6.9		
			3.0V	17	19		
			3.6V	18	20		
I_{OLEAK}	Output leakage current		1.7V – 3.6V		0.02	80	nA

4.4 Oscillator Parameters

Table 7 lists the oscillator parameters.



For Table 7, $T_A = -40\text{ }^\circ\text{C}$ to $85\text{ }^\circ\text{C}$ unless otherwise indicated.
 $V_{CC} = 1.7$ to 3.6V , TYP values at $25\text{ }^\circ\text{C}$ and 3.0V .

Table 7: Oscillator Parameters

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
F_{XT}	XI and XO pin Crystal Frequency			32.768		kHz
F_{OF}	XT Oscillator failure detection frequency			8		kHz
C_{INX}	Internal XI and XO pin capacitance			1		pF
C_{EX}	External XI and XO pin PCB capacitance			1		pF
OA_{XT}	XT Oscillation Allowance	At $25\text{ }^\circ\text{C}$ using a 32.768 kHz crystal	270	320		k Ω
F_{RCC}	Calibrated RC Oscillator Frequency ⁽¹⁾	Factory Calibrated at $25\text{ }^\circ\text{C}$, $V_{CC} = 2.8\text{V}$		128		Hz
F_{RCU}	Uncalibrated RC Oscillator Frequency	Calibration Disabled (OFF-SETR = 0)	89	122	220	Hz
J_{RCCC}	RC Oscillator cycle-to-cycle jitter	Calibration Disabled (OFF-SETR = 0) – 128 Hz		2000		ppm
		Calibration Disabled (OFF-SETR = 0) – 1 Hz		500		
A_{XT}	XT mode digital calibration accuracy ⁽¹⁾	Calibrated at an initial temperature and voltage	-2		2	ppm
A_{AC}	Autocalibration mode timing accuracy, 512 second period, $T_A = -10\text{ }^\circ\text{C}$ to $60\text{ }^\circ\text{C}$ ⁽¹⁾	24 hour run time		35		ppm
		1 week run time		20		
		1 month run time		10		
		1 year run time		3		
T_{AC}	Autocalibration mode operating temperature ⁽²⁾		-10		60	$^\circ\text{C}$
<p>⁽¹⁾ Timing accuracy is specified at $25\text{ }^\circ\text{C}$ after digital calibration of the internal RC oscillator and 32.768 kHz crystal. A typical 32.768 kHz tuning fork crystal has a negative temperature coefficient with a parabolic frequency deviation, which due to the crystal alone can result in a change of up to 150 ppm across the entire operating temperature range of $-40\text{ }^\circ\text{C}$ to $85\text{ }^\circ\text{C}$ in XT mode. Autocalibration mode timing accuracy is specified relative to XT mode timing accuracy from $-10\text{ }^\circ\text{C}$ to $60\text{ }^\circ\text{C}$.</p> <p>⁽²⁾ Outside of this temperature range, the RC oscillator frequency change due to temperature may be outside of the allowable RC digital calibration range ($\pm 12\%$) for autocalibration mode. If this happens, an autocalibration failure will occur and the ACF interrupt flag is set. The AM08X5 should be switched to use the XT oscillator as its clock source. Please see the Autocalibration Fail section for more details.</p>						

Figure 4 shows the typical calibrated RC oscillator frequency variation vs. temperature. RC oscillator calibrated at 2.8V, 25°C.

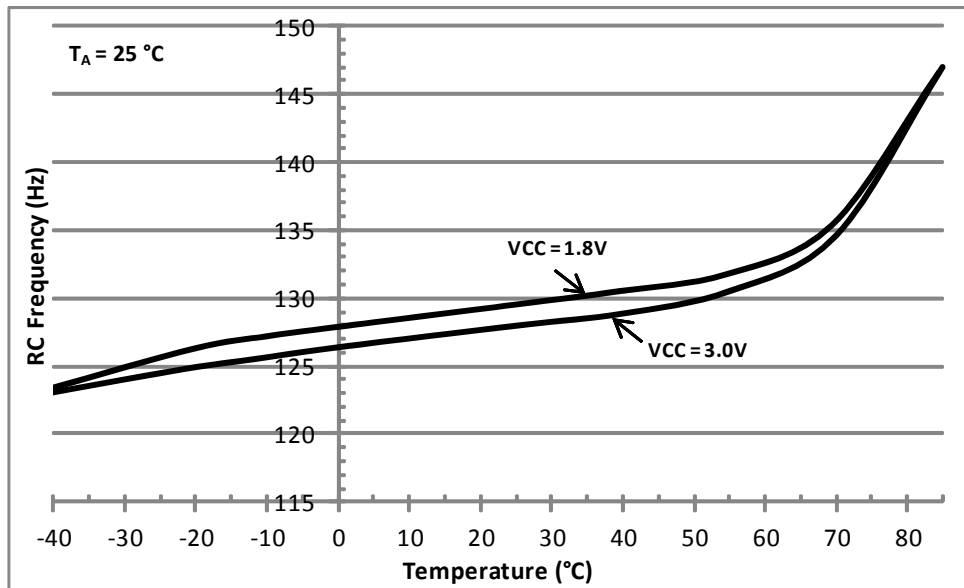


Figure 4. Calibrated RC Oscillator Typical Frequency Variation vs. Temperature

Figure 5 shows the typical uncalibrated RC oscillator frequency variation vs. temperature.

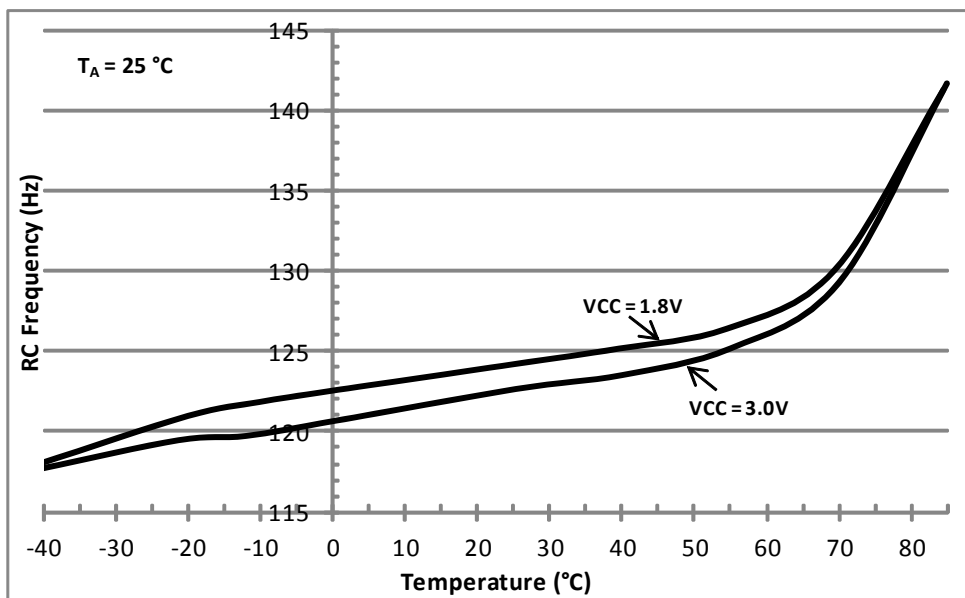


Figure 5. Uncalibrated RC Oscillator Typical Frequency Variation vs. Temperature

4.5 V_{CC} Supply Current

Table 8 lists the current supplied into the VCC power input under various conditions.



For Table 8, T_A = -40 °C to 85 °C, VBAT = 0 V to 3.6 V
 TYP values at 25 °C, MAX values at 85 °C, VCC Power state

Table 8: V_{CC} Supply Current

SYMBOL	PARAMETER	TEST CONDITIONS	VCC	MIN	TYP	MAX	UNIT
I _{VCC:I2C}	V _{CC} supply current during I ² C burst read/write	400kHz bus speed, 2.2k pull-up resistors on SCL/SDA ⁽¹⁾	3.0V		6	10	μA
			1.8V		1.5	3	
I _{VCC:SPIW}	V _{CC} supply current during SPI burst write	2 MHz bus speed ⁽²⁾	3.0V		8	12	μA
			1.8V		4	6	
I _{VCC:SPIR}	V _{CC} supply current during SPI burst read	2 MHz bus speed ⁽²⁾	3.0V		23	37	μA
			1.8V		13	21	
I _{VCC:XT}	V _{CC} supply current in XT oscillator mode	Time keeping mode with XT oscillator running ⁽³⁾	3.0V		55	330	nA
			1.8V		51	290	
I _{VCC:RC}	V _{CC} supply current in RC oscillator mode	Time keeping mode with only the RC oscillator running (XT oscillator is off) ⁽³⁾	3.0V		14	220	nA
			1.8V		11	170	
I _{VCC:ACAL}	Average V _{CC} supply current in Autocalibrated RC oscillator mode	Time keeping mode with only RC oscillator running and Auto-calibration enabled. ACP = 512 seconds ⁽³⁾	3.0V		22	235	nA
			1.8V		18	190	

⁽¹⁾ Excluding external peripherals and pull-up resistor current. All other inputs (besides SDA and SCL) are at 0V or V_{CC}. AM0805 only. Test conditions: Continuous burst read/write, 0x55 data pattern, 25 μs between each data byte, 20 pF load on each bus pin.

⁽²⁾ Excluding external peripheral current. All other inputs (besides SDI, nCE and SCL) are at 0V or V_{CC}. AM0815 only. Test conditions: Continuous burst write, 0x55 data pattern, 25 μs between each data byte, 20 pF load on each bus pin.

⁽³⁾ All inputs and outputs are at 0 V or V_{CC}

Figure 6 shows the typical VCC power state operating current vs. temperature in XT mode.

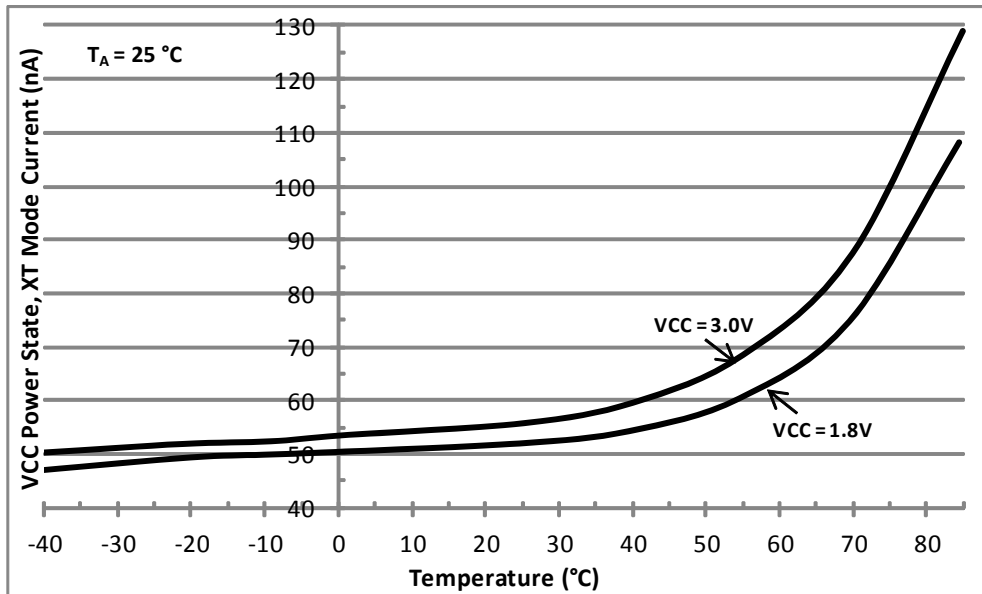


Figure 6. Typical VCC Current vs. Temperature in XT Mode

Figure 7 shows the typical VCC power state operating current vs. temperature in RC mode.

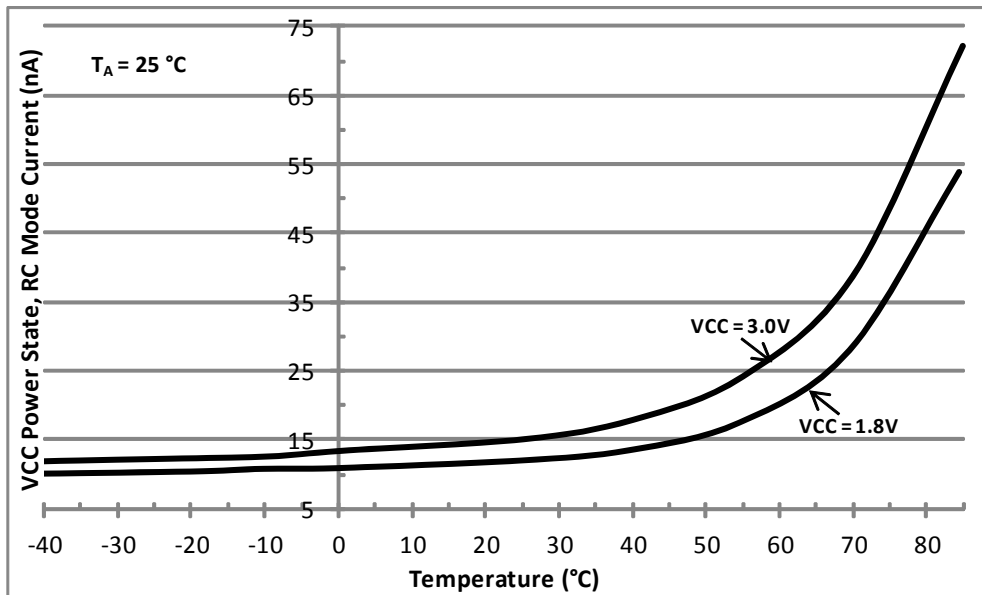


Figure 7. Typical VCC Current vs. Temperature in RC Mode

Figure 8 shows the typical VCC power state operating current vs. temperature in RC Autocalibration mode.

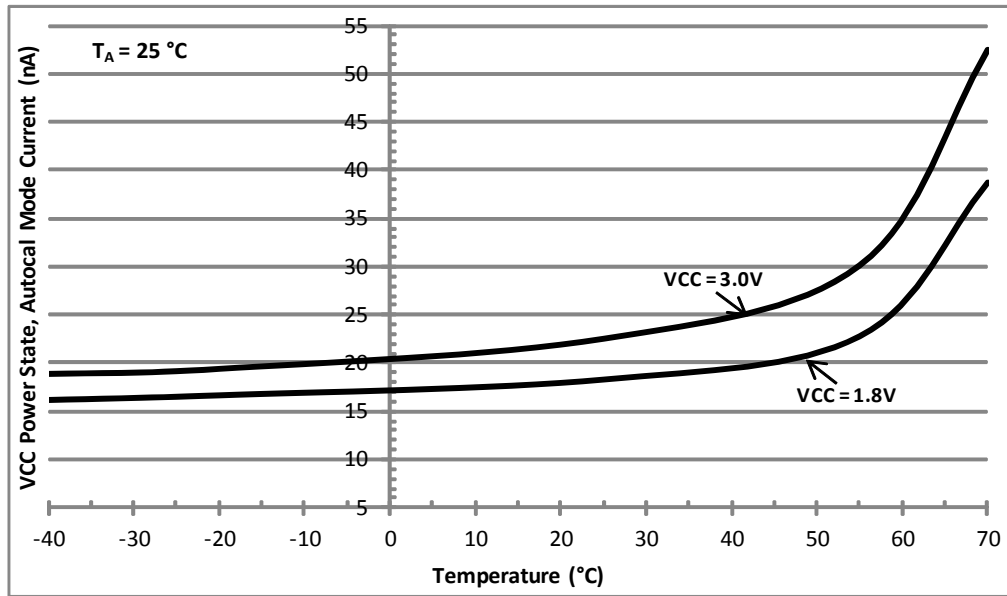


Figure 8. Typical VCC Current vs. Temperature in RC Autocalibration Mode

Figure 9 shows the typical VCC power state operating current vs. voltage for XT Oscillator and RC Oscillator modes and the average current in RC Autocalibrated mode.

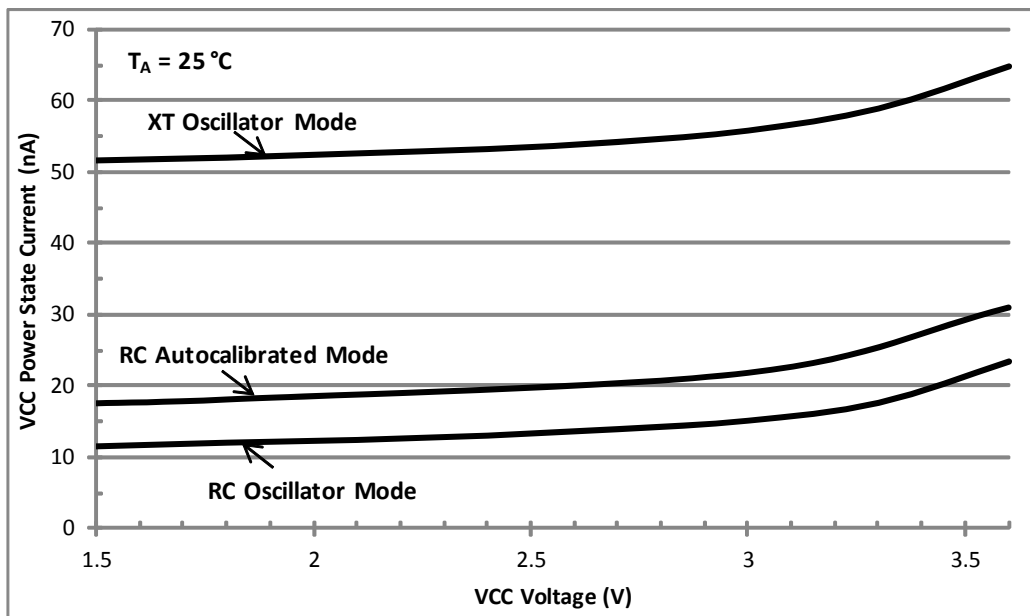


Figure 9. Typical VCC Current vs. Voltage, Different Modes of Operation

Figure 10 shows the typical VCC power state operating current during continuous I²C and SPI burst read and write activity. Test conditions: T_A = 25 °C, 0x55 data pattern, 25 μs between each data byte, 20 pF load on each bus pin, pull-up resistor current not included.

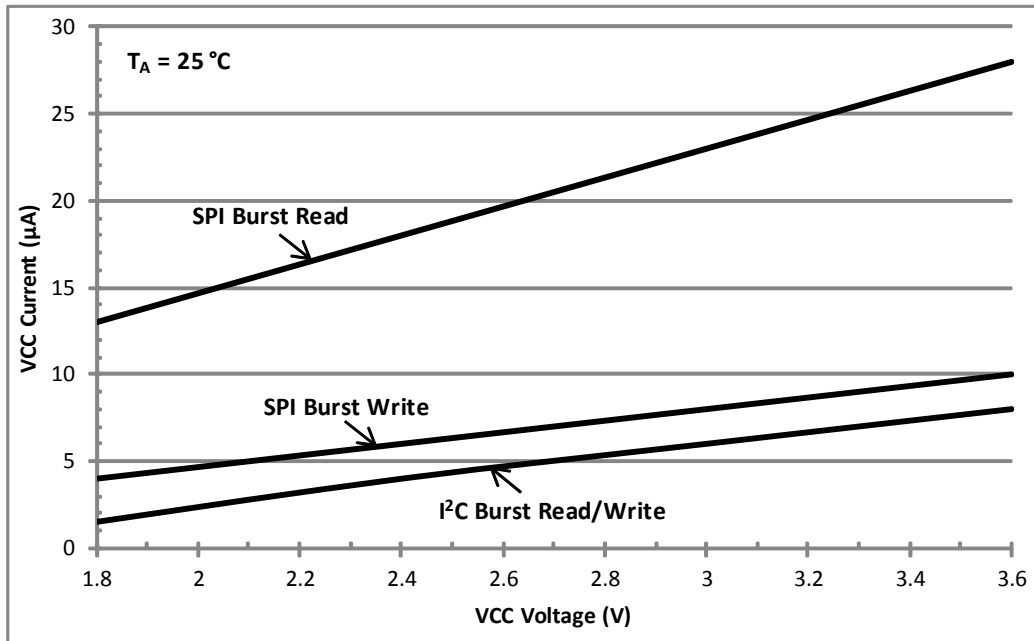


Figure 10. Typical VCC Current vs. Voltage, I²C and SPI Burst Read/Write

4.6 VBAT Supply Current

Table 9 lists the current supplied into the VBAT power input under various conditions.



For Table 9, $T_A = -40\text{ }^{\circ}\text{C}$ to $85\text{ }^{\circ}\text{C}$, TYP values at $25\text{ }^{\circ}\text{C}$, MAX values at $85\text{ }^{\circ}\text{C}$, V_{BAT} Power state.

Table 9: V_{BAT} Supply Current

SYMBOL	PARAMETER	TEST CONDITIONS	V_{CC}	V_{BAT}	MIN	TYP	MAX	UNIT
$I_{VBAT:XT}$	VBAT supply current in XT oscillator mode	Time keeping mode with XT oscillator running ⁽¹⁾	$< V_{CCSWF}$	3.0V		56	330	nA
				1.8V		52	290	
$I_{VBAT:RC}$	VBAT supply current in RC oscillator mode	Time keeping mode with only the RC oscillator running (XT oscillator is off) ⁽¹⁾	$< V_{CCSWF}$	3.0V		16	220	nA
				1.8V		12	170	
$I_{VBAT:ACAL}$	Average VBAT supply current in Autocalibrated RC oscillator mode	Time keeping mode with the RC oscillator running. Autocalibration enabled. ACP = 512 seconds ⁽¹⁾	$< V_{CCSWF}$	3.0V		24	235	nA
				1.8V		20	190	
$I_{VBAT:VCC}$	VBAT supply current in VCC powered mode	V_{CC} powered mode ⁽¹⁾	1.7 - 3.6 V	3.0V	-5	0.6	20	nA
				1.8V	-10	0.5	16	

⁽¹⁾ Test conditions: All inputs and outputs are at 0 V or V_{CC} .

Figure 11 shows the typical VBAT power state operating current vs. temperature in XT mode.

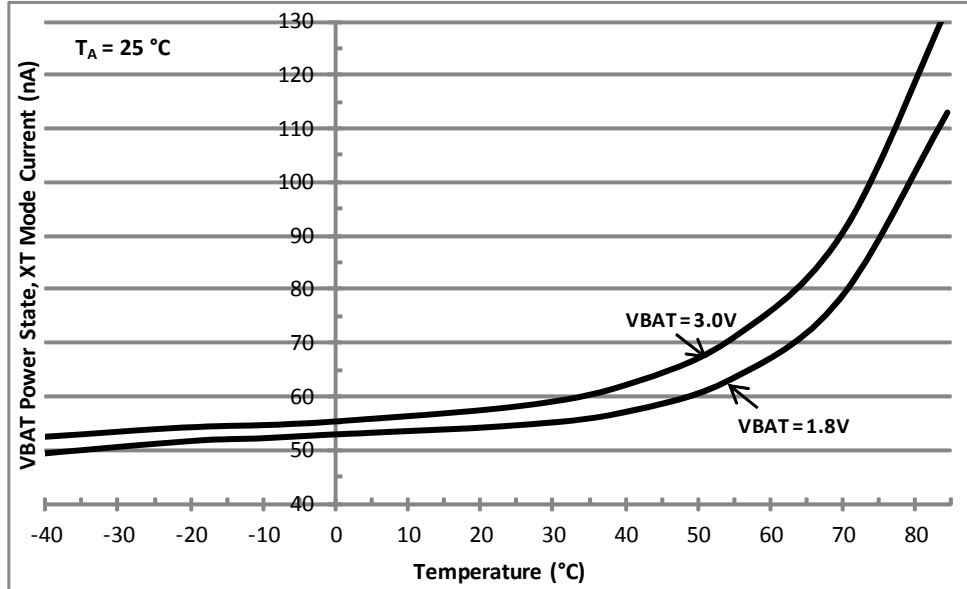


Figure 11. Typical VBAT Current vs. Temperature in XT Mode

Figure 12 shows the typical VBAT power state operating current vs. temperature in RC mode.

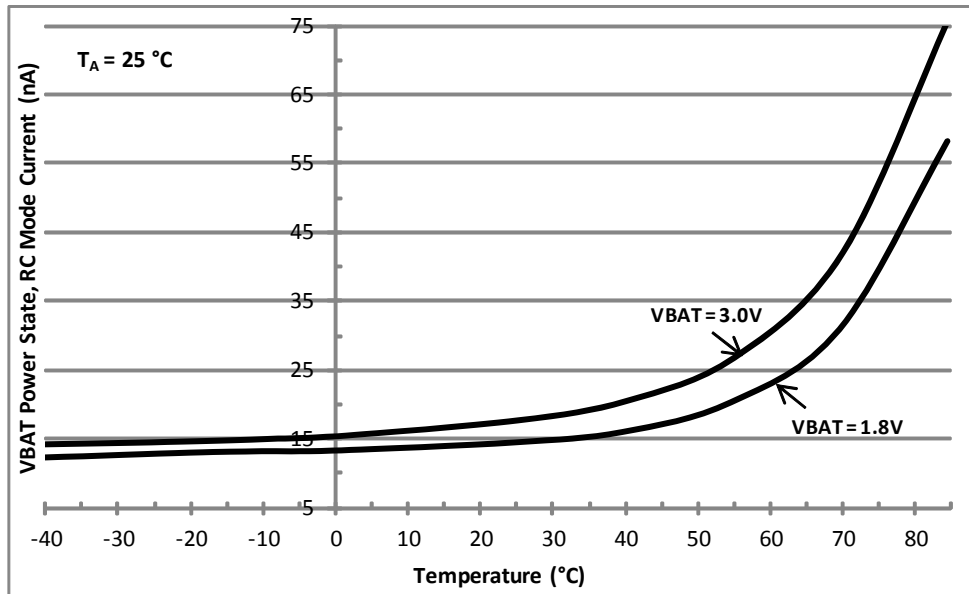


Figure 12. Typical VBAT Current vs. Temperature in RC Mode

Figure 13 shows the typical VBAT power state operating current vs. temperature in RC Autocalibration mode.

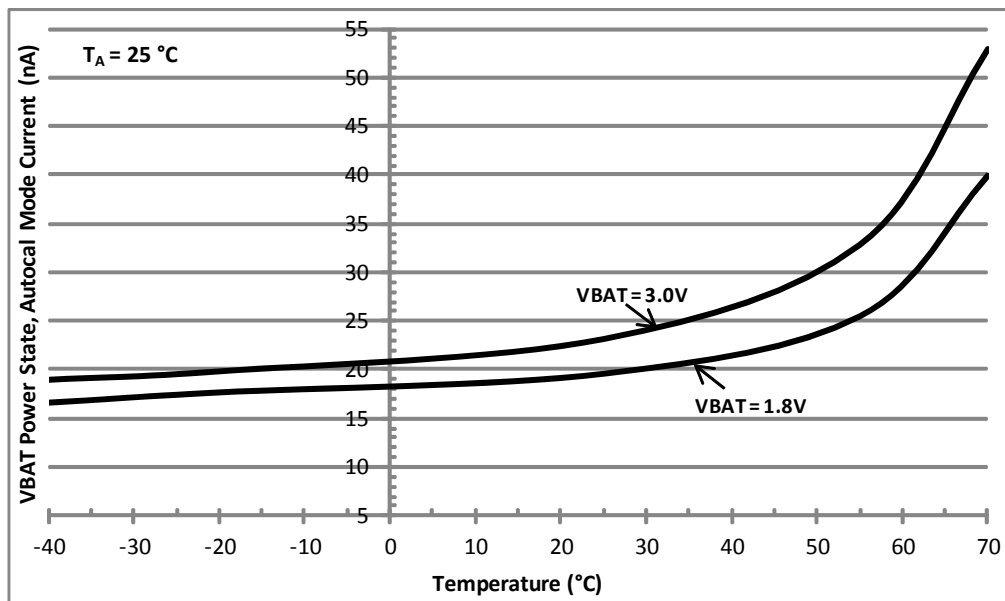


Figure 13. Typical VBAT Current vs. Temperature in RC Autocalibration Mode

Figure 14 shows the typical VBAT power state operating current vs. voltage for XT Oscillator and RC Oscillator modes and the average current in RC Autocalibrated mode, VCC = 0 V.

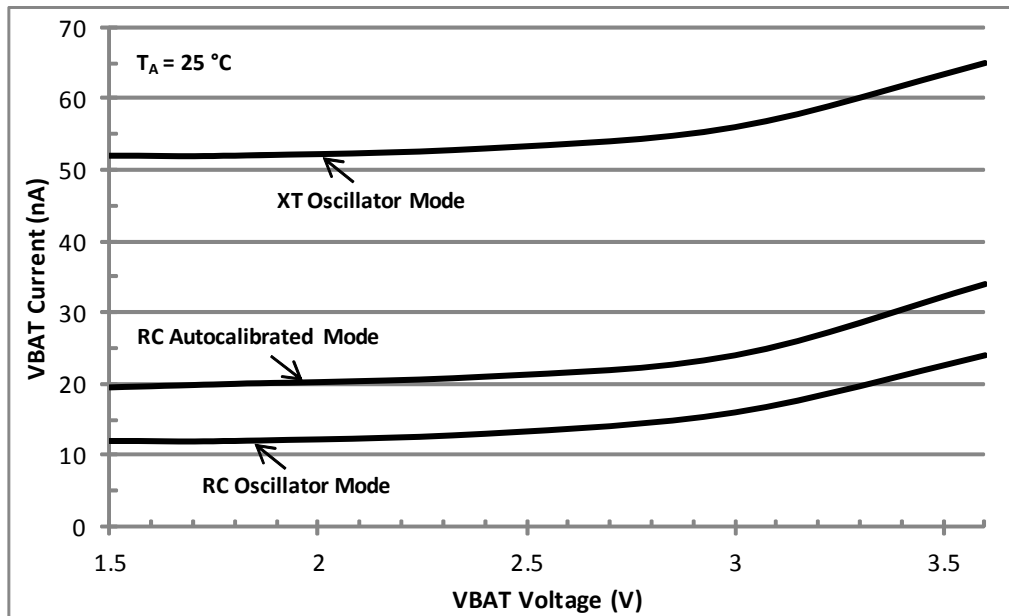


Figure 14. Typical VBAT Current vs. Voltage, Different Modes of Operation

Figure 15 shows the typical VBAT current when operating in the VCC power state, VCC = 1.7 V.

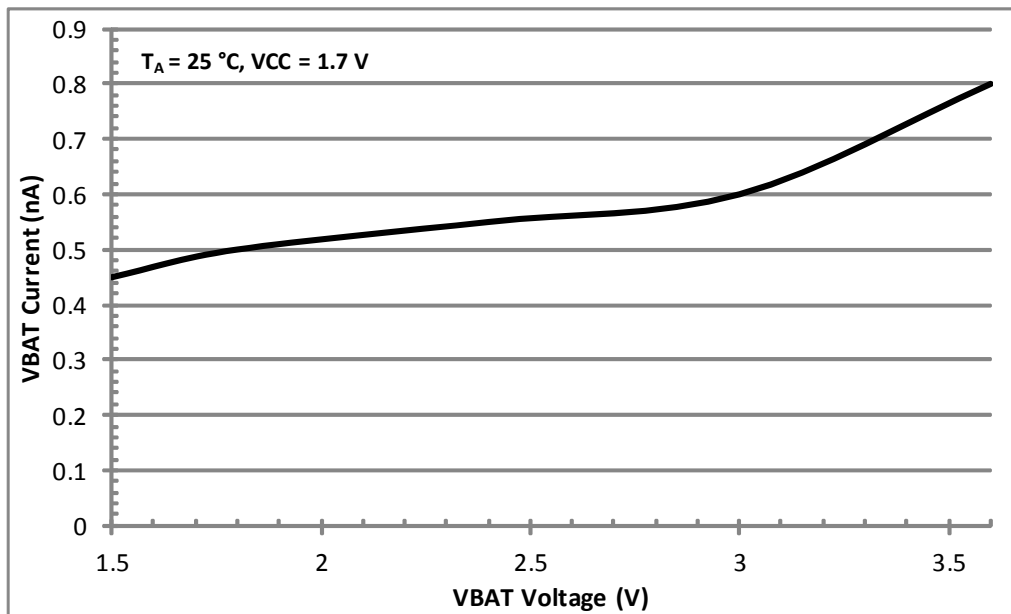


Figure 15. Typical VBAT Current vs. Voltage in VCC Power State