imall

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APL502B2(G) APL502L(G)

500V 58A 0.090Ω

LINEAR MOSFET

Linear Mosfets are optimized for applications operating in the Linear region where concurrent high voltage and high current can occur at near DC conditions (>100 msec).

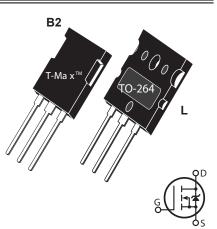
- Higher FBSOA
- Popular T-MAX™ or TO-264 Package
- Higher Power Dissipation
- RoHS Compliant
- MAXIMUM RATINGS

All Ratings: $T_{C} = 25^{\circ}C$ unless otherwise specified.

Symbol	Parameter	APL502B2_L(G)	UNIT	
V _{DSS}	Drain-Source Voltage	500	Volts	
I _D	Continuous Drain Current @ T _C = 25°C	58	Amps	
I _{DM}	Pulsed Drain Current ^①	232		
V _{GS}	Gate-Source Voltage Continuous	±30	Volts	
V _{GSM}	Gate-Source Voltage Transient	±40		
D	Total Power Dissipation @ T_{C} = 25°C	730	Watts	
P _D	Linear Derating Factor	5.84	W/°C	
T _J ,T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C	
Τ _L	Lead Temperature: 0.063" from Case for 10 Sec.	300		
I _{AR}	Avalanche Current $^{\textcircled{1}}$ (Repetitive and Non-Repetitive)	58	Amps	
E _{AR}	Repetitive Avalanche Energy ^①	50	mJ	
E _{AS}	Single Pulse Avalanche Energy ^④	3000		
STATIC E	LECTRICAL CHARACTERISTICS			

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage (V _{GS} = 0V, I _D = 250 μ A)	500			Volts
I _D (ON)	On State Drain Current $^{(2)}(V_{DS} > I_{D}(ON) \times R_{DS}(ON) Max, V_{GS} = 15V)$	58			Amps
R _{DS} (ON)	Drain-Source On-State Resistance ⁽²⁾ (V _{GS} = 15V, 29A)			0.09	Ohms
I _{DSS}	Zero Gate Voltage Drain Current (V_{DS} = 500V, V_{GS} = 0V)			25	μA
	Zero Gate Voltage Drain Current (V_{DS} = 400V, V_{GS} = 0V, T_{C} = 125°C)			250	
I _{GSS}	Gate-Source Leakage Current ($V_{GS} = \pm 30V, V_{DS} = 0V$)			±100	nA
V _{GS} (TH)	Gate Threshold Voltage $(V_{DS} = V_{GS}, I_{D} = 2.5mA)$	2		4	Volts

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.



DYNAMIC CHARACTERISTICS

Symbol	Characteristic	Test Conditions	MIN	ТҮР	MAX	UNIT
C _{iss}	Input Capacitance	V _{GS} = 0V		7485	9000	
C _{oss}	Output Capacitance	V _{DS} = 25V		1290	1810	pF
C _{rss}	Reverse Transfer Capacitance	f = 1 MHz		617	930	
t _d (on)	Turn-on Delay Time	V _{GS} = 15V		13	26	
t _r	Rise Time	V _{DD} = 250V		27	54	ns
t _d (off)	Turn-off Delay Time	I _D = 29A @ 25°C		56	84	
t _f	Fall Time	$R_{g} = 0.6\Omega$		16	20	

THERMAL CHARACTERISTICS

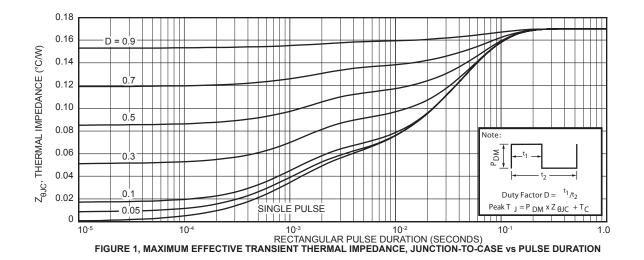
Symbol	Characteristic	MIN	ТҮР	MAX	UNIT
R _{θJC}	Junction to Case			.17	°C/W
W _T	Package Weight		0.22		οz
			5.9		g

 $^{(1)}$ Repetitive Rating: Pulse width limited by maximum junction temperature.

 $\overset{(3)}{=}$ See MIL-STD-750 Method 3471 $\overset{(4)}{=}$ Starting T_i = +25°C, L = 1.78mH, R_G = 25\Omega, Peak I_L = 58A

(2) Pulse Test: Pulse width < 380 µS, Duty Cycle < 2%

Microsemi reserves the right to change, without notice, the specifications and information contained herein.



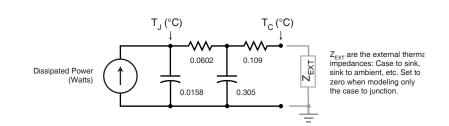
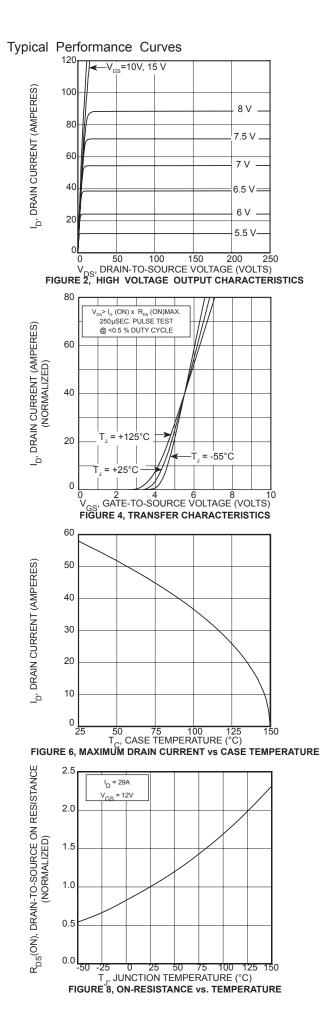
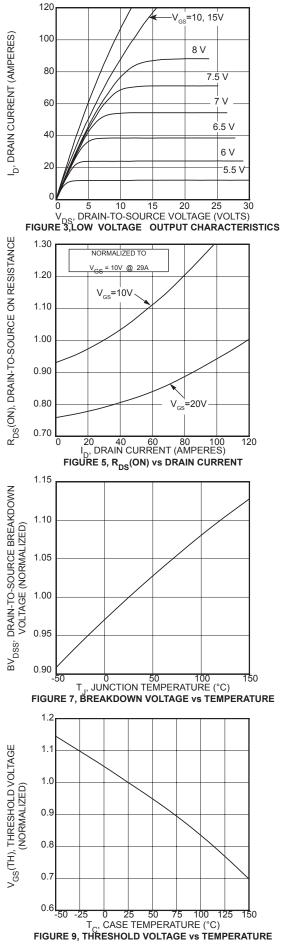
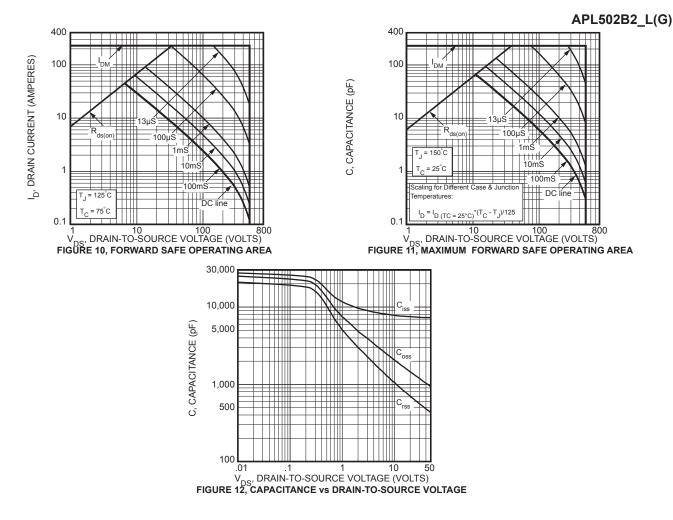
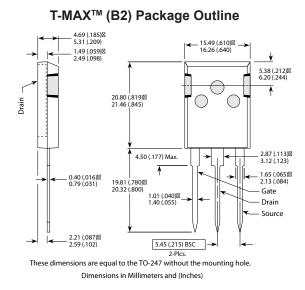


FIGURE 1a, TRANSIENT THERMAL IMPEDANCE MODEL

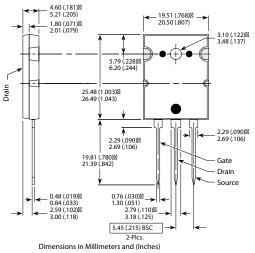








TO-264 (L) Package Outline



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