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APT10035JLL



1000V 25A 0.350Ω

POWER MOS 7® MOSFET

Power MOS 7° is a new generation of low loss, high voltage, N-Channel enhancement mode power MOSFETS. Both conduction and switching losses are addressed with Power MOS 7° by significantly lowering R_{DS(ON)} and Q_g. Power MOS 7° combines lower conduction and switching losses along with exceptionally fast switching speeds inherent with APT's patented metal gate structure.



Lower Input Capacitance

Increased Power Dissipation

Lower Miller Capacitance

Easier To Drive

· Lower Gate Charge, Qg

Popular SOT-227 Package



MAXIMUM RATINGS

All Ratings: $T_C = 25$ °C unless otherwise specified.

Symbol	Parameter	APT10035JLL	UNIT	
V _{DSS}	Drain-Source Voltage	1000	Volts	
I _D	Continuous Drain Current @ T _C = 25°C	25	Amna	
I _{DM}	Pulsed Drain Current ①	100	Amps	
V _{GS}	Gate-Source Voltage Continuous	±30	\/-lt-	
V _{GSM}	Gate-Source Voltage Transient	±40	Volts	
P_D	Total Power Dissipation @ T _C = 25°C	520	Watts	
, D	Linear Derating Factor	4.16	W/°C	
T_J , T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	- °C	
T _L	Lead Temperature: 0.063" from Case for 10 Sec.	300]	
I _{AR}	Avalanche Current (Repetitive and Non-Repetitive)	25	Amps	
E _{AR}	Repetitive Avalanche Energy ①	50	- mJ	
E _{AS}	Single Pulse Avalanche Energy ^④	3000	1113	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage $(V_{GS} = 0V, I_D = 250\mu\text{A})$	1000			Volts
I _{D(on)}	On State Drain Current ② $(V_{DS} > I_{D(on)} \times R_{DS(on)} Max, V_{GS} = 10V)$	25			Amps
R _{DS(on)}	Drain-Source On-State Resistance ② (V _{GS} = 10V, 14A)			0.350	Ohms
I _{DSS}	Zero Gate Voltage Drain Current $(V_{DS} = 1000V, V_{GS} = 0V)$			100	μА
	Zero Gate Voltage Drain Current (V _{DS} = 800V, V _{GS} = 0V, T _C = 125°C)			500	
I _{GSS}	Gate-Source Leakage Current $(V_{GS} = \pm 30V, V_{DS} = 0V)$			±100	nA
V _{GS(th)}	Gate Threshold Voltage $(V_{DS} = V_{GS}, I_{D} = 2.5 \text{mA})$	3		5	Volts

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

DYNAMIC CHARACTERISTICS

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Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C _{iss}	Input Capacitance	V _{GS} = 0V		5185		
C _{oss}	Output Capacitance	$V_{DS} = 25V$		881		pF
C _{rss}	Reverse Transfer Capacitance	f = 1 MHz		160		μ.
Q_g	Total Gate Charge ^③	V _{GS} = 10V		186		
Q_{gs}	Gate-Source Charge	V _{DD} = 500V		24		nC
Q_{gd}	Gate-Drain ("Miller") Charge	I _D = 28A @ 25°C		122		
t _{d(on)}	Turn-on Delay Time	RESISTIVE SWITCHING V _{GS} = 15V		12		
t _r	Rise Time	$V_{GS} = 13V$ $V_{DD} = 500V$		10		
t _{d(off)}	Turn-off Delay Time	I _D = 28A @ 25°C		36		ns
t _f	Fall Time	$R_G = 1.6\Omega$		9		
E _{on}	Turn-on Switching Energy [©]	INDUCTIVE SWITCHING @ 25°C V _{DD} = 670V, V _{GS} = 15V		900		
E _{off}	Turn-off Switching Energy	$I_{D} = 28A, R_{G} = 5\Omega$		623		
E _{on}	Turn-on Switching Energy [®]	INDUCTIVE SWITCHING @ 125°C V _{DD} = 670V V _{GS} = 15V		1423		μJ
E _{off}	Turn-off Switching Energy	$I_D = 28A, R_G = 5\Omega$		779		

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
I _s	Continuous Source Current (Body Diode)			25	Amps
I _{SM}	Pulsed Source Current ① (Body Diode)			100	Allips
V _{SD}	Diode Forward Voltage ② $(V_{GS} = 0V, I_S = -I_D 25A)$			1.3	Volts
t _{rr}	Reverse Recovery Time $(I_S = -I_D 25A, dI_S / dt = 100A / \mu s)$		1170		ns
Q _{rr}	Reverse Recovery Charge $(I_S = -I_D^2 25A, dI_S/dt = 100A/\mu s)$		16.28		μC
dv/ _{dt}	Peak Diode Recovery dv/dt (5)			10	V/ns

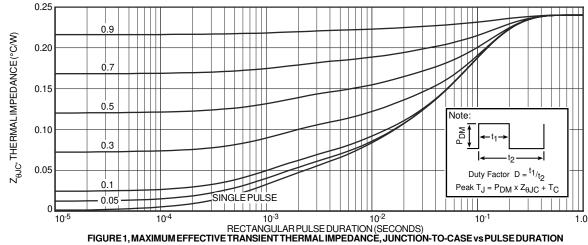
THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.24	°C/W
$R_{\theta JA}$	Junction to Ambient			40	C/VV

- 1 Repetitive Rating: Pulse width limited by maximum junction temperature
- 2 Pulse Test: Pulse width < 380 μ s, Duty Cycle < 2%
- ③ See MIL-STD-750 Method 3471

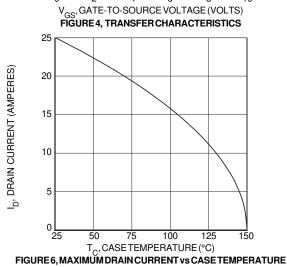
- (4) Starting T_j = +25°C, L = 9.60mH, R_G = 25 Ω , Peak I_L = 25A
- $\textcircled{5}\ \text{dv/}_{\text{dt}}$ numbers reflect the limitations of the test circuit rather than the device itself. $I_S \le -I_D 25A \frac{di}{dt} \le 700A/\mu s \quad V_R \le V_{DSS} \quad T_J \le 150 ^{\circ}C$
- 6 Eon includes diode reverse recovery. See figures 18, 20.

APT Reserves the right to change, without notice, the specifications and information contained herein.



Typical Performance Curves RC MODEL Junction temp. ("C) _{ID}, DRAIN CURRENT (AMPERES) 0.0528 0.0203F Power 0.0651 0.173F (Watts) 0.123 0.490F Case temperature FIGURE 2, TRANSIENT THERMAL IMPEDANCE MODEL RESISTANCE V_{DS}> I_D (ON) x R_{DS} (ON)MAX 250µSEC. PULSE TEST 70 DRAIN CURRENT (AMPERES) @ <0.5 % DUTY CYCLE 60 50 40 T_J = +125°C 30

= -55°C

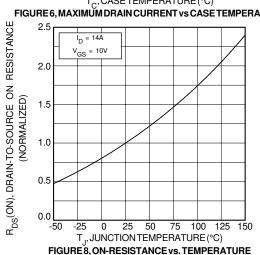


= +25°C

20

10

0



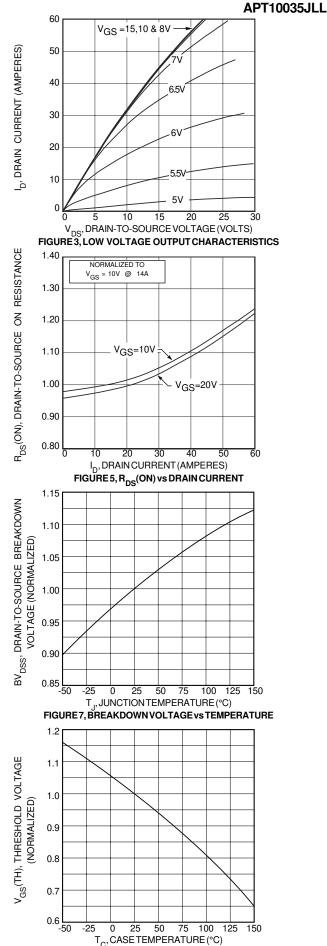


FIGURE 9, THRESHOLD VOLTAGE vs TEMPERATURE

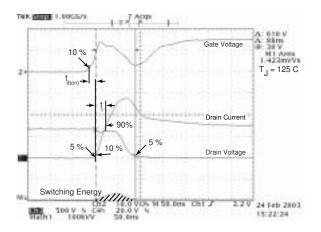


Figure 18, Turn-on Switching Waveforms and Definitions

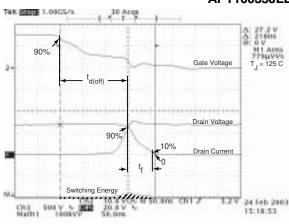


Figure 19, Turn-off Switching Waveforms and Definitions

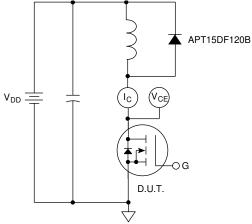


Figure 20, Inductive Switching Test Circuit

SOT-227 (ISOTOP®) Package Outline

