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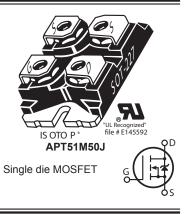


APT51M50J

500V, 51A, 0.075Ω Max

N-Channel MOSFET

Power MOS 8TM is a high speed, high voltage N-channel switch-mode power MOSFET. A proprietary planar stripe design yields excellent reliability and manufacturability. Low switching loss is achieved with low input capacitance and ultra low C_{rss} "Miller" capacitance. The intrinsic gate resistance and capacitance of the poly-silicon gate structure help control slew rates during switching, resulting in low EMI and reliable paralleling, even when switching at very high frequency. Reliability in flyback, boost, forward, and other circuits is enhanced by the high avalanche energy capability.



FEATURES

- Fast switching with low EMI/RFI
- Low R_{DS(on)}
- + Ultra low $\mathbf{C}_{\mathrm{rss}}$ for improved noise immunity
- Low gate charge
- Avalanche energy rated
- RoHS compliant

TYPICAL APPLICATIONS

- PFC and other boost converter
- Buck converter
- Two switch forward (asymmetrical bridge)
- Single switch forward
- Flyback
- Inverters

Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
I _D	Continuous Drain Current @ T _C = 25°C	51	
	Continuous Drain Current @ T _C = 100°C	32	А
I _{DM}	Pulsed Drain Current ^①	230	
V _{GS}	Gate-Source Voltage	±30	V
E _{AS}	Single Pulse Avalanche Energy	1580	mJ
I _{AR}	Avalanche Current, Repetitive or Non-Repetitive	37	А

Thermal and Mechanical Characteristics

Symbol	Characteristic	Min	Тур	Мах	Unit	
P _D	Total Power Dissipation @ $T_{C} = 25^{\circ}C$			480	W	
$R_{_{ extsf{ heta}JC}}$	Junction to Case Thermal Resistance			0.26		
R _{ecs}	Case to Sink Thermal Resistance, Flat, Greased Surface		0.15		°C/W	
T_,T _{STG}	Operating and Storage Junction Temperature Range	-55		150	°C	
V _{Isolation}	RMS Voltage (50-60hHz Sinusoidal Waveform from Terminals to Mounting Base for 1 Min.)	2500			V	
W _T	Package Weight		1.03		oz	
			29.2		g	
Torque	Transische and Maustine Oracus			10	in∙lbf	
	Terminals and Mounting Screws.			1.1	N∙m	

Static Characteristics

T_J = 25°C unless otherwise specified

APT51M50J

Symbol	Parameter	Test Conditions		Min	Тур	Мах	Unit
V _{BR(DSS)}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_{D} = 250\mu A$		500			V
$\Delta V_{BR(DSS)} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	Reference to 25°C, $I_D = 250 \mu A$			0.60		V/°C
R _{DS(on)}	Drain-Source On Resistance ^③	V _{GS} = 10V, I _D = 37A			0.064	0.075	Ω
V _{GS(th)}	Gate-Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 2.5 \text{mA}$		3	4	5	V
$\Delta V_{GS(th)} / \Delta T_J$	Threshold Voltage Temperature Coefficient				-10		mV/°C
	Zero Gate Voltage Drain Current	V _{DS} = 500V	T _J = 25°C			100	μA
DSS		$V_{GS} = 0V$	T _J = 125°C			500	
I _{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 30V$				±100	nA

Dynamic Characteristics

T_J = 25°C unless otherwise specified

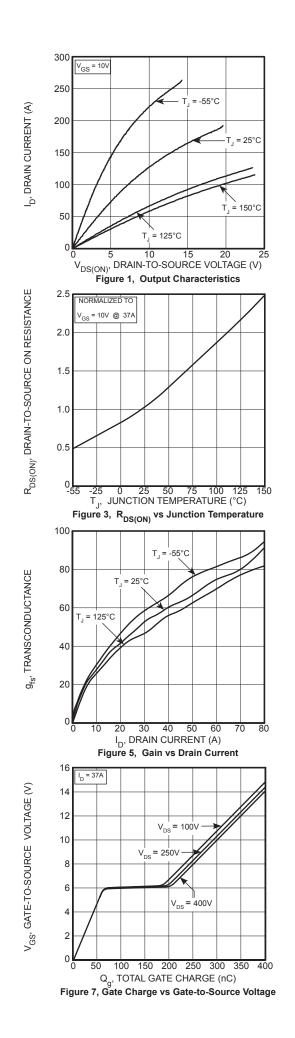
Symbol	Parameter	Test Conditions Min Typ		Мах	Unit		
9 _{fs}	Forward Transconductance	V _{DS} = 50V, I _D = 37A		55		S	
C _{iss}	Input Capacitance			11600			
C _{rss}	Reverse Transfer Capacitance	V _{GS} = 0V, V _{DS} = 25V f = 1MHz		160			
C _{oss}	Output Capacitance			1250			
C _{o(cr)} ④	Effective Output Capacitance, Charge Related	y' = 0y' y' = 0y' to 222y'		725		pF	
C _{o(er)} (5)	Effective Output Capacitance, Energy Related	V_{GS} = 0V, V_{DS} = 0V to 333V		365			
Q _g	Total Gate Charge	$y_{1} = 0 + 10 y_{1} + 0.20$		290			
Q _{gs}	Gate-Source Charge	$V_{GS} = 0 \text{ to } 10V, I_{D} = 37A,$ $V_{DS} = 250V$		65		nC	
Q _{gd}	Gate-Drain Charge	v _{DS} - 250V		130			
t _{d(on)}	Turn-On Delay Time	Resistive Switching		45			
t _r	Current Rise Time	V _{DD} = 333V, I _D = 37A		55		ne	
t _{d(off)}	Turn-Off Delay Time	R _G = 2.2Ω [®] , V _{GG} = 15V		120		ns	
t _f	Current Fall Time			39			

Source-Drain Diode Characteristics

Symbol	Parameter	Test Conditions	Min	Тур	Мах	Unit
۱ _s	Continuous Source Current (Body Diode)	MOSFET symbol showing the			51	A
I _{SM}	Pulsed Source Current (Body Diode) ^①	integral reverse p-n junction diode (body diode)			230	
V _{SD}	Diode Forward Voltage	$I_{SD} = 37A, T_{J} = 25^{\circ}C, V_{GS} = 0V$			1	V
t _{rr}	Reverse Recovery Time	I _{SD} = 37A ^③		695		ns
Q _{rr}	Reverse Recovery Charge	di _{SD} /dt = 100A/µs, T _J = 25°C		17		μC
dv/dt	Peak Recovery dv/dt	$I_{SD} \le 37A$, di/dt $\le 1000A/\mu s$, $V_{DD} = 333V$, $T_J = 125^{\circ}C$			8	V/ns

- (1) Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature.
- (2) Starting at $T_J = 25^{\circ}C$, L = 2.31mH, $R_G = 25\Omega$, $I_{AS} = 37A$.
- (3) Pulse test: Pulse Width < 380μ s, duty cycle < 2%.
- (4) $C_{o(cr)}$ is defined as a fixed capacitance with the same stored charge as C_{OSS} with $V_{DS} = 67\%$ of $V_{(BR)DSS}$. (5) $C_{o(er)}$ is defined as a fixed capacitance with the same stored energy as C_{OSS} with $V_{DS} = 67\%$ of $V_{(BR)DSS}$. To calculate $C_{o(er)}$ for any value of V_{DS} less than $V_{(BR)DSS}$, use this equation: $C_{o(er)} = -1.65E-7/V_{DS}^{2} + 5.51E-8/V_{DS} + 2.03E-10$.
- 6 R_G is external gate resistance, not including internal gate resistance or gate driver impedance. (MIC4452)

Microsemi reserves the right to change, without notice, the specifications and information contained herein.



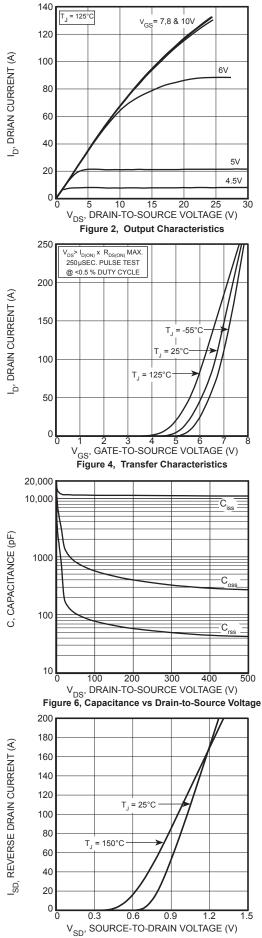
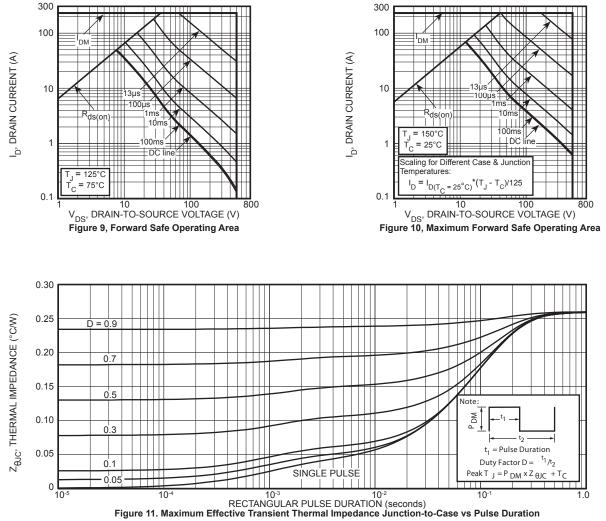
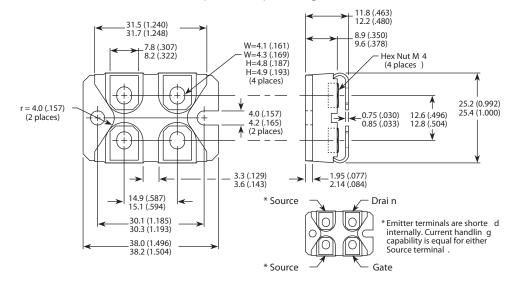


Figure 8, Reverse Drain Current vs Source-to-Drain Voltage



SOT-227 (ISOTOP®) Package Outline



Dimensions in Millimeters and (Inches)