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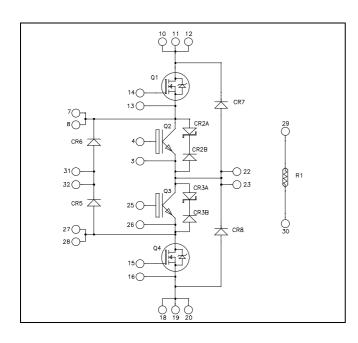
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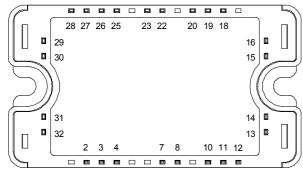
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Three level inverter CoolMOS & Trench + Field Stop IGBT3 Power Module





All multiple inputs and outputs must be shorted together Example: 10/11/12 ; 7/8 ...

APTCV60TLM45T3G

Trench & Field Stop IGBT3 Q2, Q3:

 $V_{CES} = 600V$; $I_C = 75A$ @ $Tc = 80^{\circ}C$

CoolMOS™ Q1, Q4:

$V_{DSS} = 600V$; $I_D = 38A$ @ Tc = 80°C

Application

- Solar converter
- Uninterruptible Power Supplies

Features

- Q2, Q3 Trench + Field Stop IGBT3 Technology
 - Low voltage dropLow tail current
 - Switching frequency up to 20 kHz
 - Switching frequency up to 20 kf
 Soft recovery parallel diodes
- Low diode VF
- Low leakage current
- RBSOA and SCSOA rated

• *Q1, Q4 CoolMOS*TM

- Ultra low R_{DSon}
- Low Miller capacitance
- Ultra low gate charge
- Avalanche energy rated
- Very rugged
- Kelvin emitter for easy drive
- Very low stray inductance
- High level of integration
- Internal thermistor for temperature monitoring

Benefits

- Stable temperature behavior
- Very rugged
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Easy paralleling due to positive TC of VCEsat
- Low profile
- RoHS Compliant

All ratings (a) $T_i = 25^{\circ}C$ unless otherwise specified

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



Q1 & Q4 Absolute maximum ratings (per CoolMOSTM)

Symbol	Parameter		Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage		600	V
т	Continuous Drain Current	$T_c = 25^{\circ}C$	49	Α
I _D	Continuous Drain Current	$T_c = 80^{\circ}C$	38	Α
I _{DM}	Pulsed Drain current		130	
V _{GS}	Gate - Source Voltage		± 20	V
R _{DSon}	Drain - Source ON Resistance		45	mΩ
P _D	Maximum Power Dissipation	$T_c = 25^{\circ}C$	250	W
I _{AR}	Avalanche current (repetitive and non repetitive)		15	А
E _{AR}	Repetitive Avalanche Energy		3	mI
E _{AS}	Single Pulse Avalanche Energy		1900	mJ

Q1 & Q4 Electrical Characteristics (per CoolMOSTM)

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I _{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$ $T_j =$	= 25°C		250	۸
	Zero Gate voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$ $T_j =$	= 125°C		500	μA
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 24.5A$		40	45	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 3mA$	2.1	3	3.9	V
I _{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 20 V, V_{DS} = 0V$			100	nA

Q1 & Q4 Dynamic Characteristics (per CoolMOSTM)

-	Characteristic	Test Conditions	Min	Тур	Max	Unit
C _{iss}	Input Capacitance	$V_{GS} = 0V$; $V_{DS} = 25V$		7.2		nF
Coss	Output Capacitance	f = 1MHz		8.5		m
Qg	Total gate Charge	$V_{GS} = 10V$		150		
Q _{gs}	Gate – Source Charge	$V_{Bus} = 300V$ $I_D = 49A$		34		nC
Q_{gd}	Gate – Drain Charge			51		
T _{d(on)}	Turn-on Delay Time	Inductive Switching (125°C)		21		
Tr	Rise Time	$V_{GS} = 10V$		30		ns
$T_{d(off)}$	Turn-off Delay Time	$V_{Bus} = 400V$ $I_D = 49A$		100		
$T_{\rm f}$	Fall Time	$R_G = 5\Omega$		45		
Eon	Turn-on Switching Energy	Inductive switching @ $25^{\circ}C$		675		1
E _{off}	Turn-off Switching Energy	$V_{GS} = 10V$; $V_{Bus} = 400V$ $I_D = 49A$; $R_G = 5\Omega$		520		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		1100		T
$\mathrm{E}_{\mathrm{off}}$	Turn-off Switching Energy	$V_{GS} = 10V ; V_{Bus} = 400V$ $I_D = 49A ; R_G = 5\Omega$		635		μJ
R _{thJC}	Junction to Case Thermal Resistance				0.5	°C/W



Q2 & Q3 Absolute maximum ratings (per IGBT)

Symbol	Parameter		Max ratings	Unit
V _{CES}	Collector - Emitter Breakdown Voltage		600	V
т	Continuous Collector Current	$T_C = 25^{\circ}C$	100	
I _C	Continuous Conector Current	$T_C = 80^{\circ}C$	75	А
I _{CM}	Pulsed Collector Current	$T_C = 25^{\circ}C$	140	
V _{GE}	Gate – Emitter Voltage		± 20	V
PD	Maximum Power Dissipation	$T_C = 25^{\circ}C$	250	W
RBSOA	Reverse Bias Safe Operating Area	$T_{\rm J} = 150^{\circ}{\rm C}$	150A @ 550V	

Q2 & Q3 Electrical Characteristics (per IGBT)

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
I _{CES}	Zero Gate Voltage Collector Current	$V_{GE} = 0V, V_{CE} = 600V$				250	μA
V _{CE(sat)}	Collector Emitter Saturation Voltage	$V_{GE} = 15V$	$T_j = 25^{\circ}C$		1.5	1.9	V
V CE(sat)	Collector Emitter Saturation Voltage	$I_C = 75A$	$T_{j} = 150^{\circ}C$		1.7		v
V _{GE(th)}	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 600 \mu A$		5.0	5.8	6.5	V
I _{GES}	Gate – Emitter Leakage Current	$V_{GE} = 20V, V_{CE}$	= 0V			600	nA

Q2 & Q3 Dynamic Characteristics (per IGBT)

	Characteristic	Test Conditions		Min	Тур	Max	Unit
Cies	Input Capacitance	$V_{GE} = 0V$			4620		
C _{oes}	Output Capacitance	$V_{CE} = 25V$			300		pF
Cres	Reverse Transfer Capacitance	f = 1 MHz			140		
Q_{G}	Gate charge	V _{GE} =±15V, I _C =' V _{CE} =300V	75A		0.8		μC
T _{d(on)}	Turn-on Delay Time	Inductive Switch	hing (25°C)		110		
T _r	Rise Time	$V_{GE} = \pm 15V$			45		
T _{d(off)}	Turn-off Delay Time	$V_{Bus} = 300V$ $I_C = 75A$			200		ns
T _f	Fall Time	$R_{\rm G} = 4.7\Omega$			40		
T _{d(on)}	Turn-on Delay Time	Inductive Switching (150°C)			120		
Tr	Rise Time	$V_{GE} = \pm 15V$ $V_{Bus} = 300V$			50		ns
T _{d(off)}	Turn-off Delay Time	$I_C = 75A$			250		
T _f	Fall Time	$R_G = 4.7\Omega$			60		
Eon	Turn-on Switching Energy	$V_{GE} = \pm 15V$	$T_j = 25^{\circ}C$		0.35		mJ
Lon	Turn-on Switching Energy	$V_{Bus} = 300V$	$T_{j} = 150^{\circ}C$		0.6		1115
E _{off}	Turn-off Switching Energy	$I_c = 75A$	$T_j = 25^{\circ}C$		2.2		mJ
Loff	Turn-on Switching Energy	$R_G = 4.7\Omega$	$T_{j} = 150^{\circ}C$		2.6		1115
I _{sc}	Short Circuit data	$V_{GE} \le 15V; V_{Bu}, t_p \le 6\mu s; T_i = 15$			380		А
R _{thJC}	Junction to Case Thermal Resistance					0.60	°C/W



CR2 & CR3 diode ratings and characteristics (per device)

_	Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
	$V_{\rm F}$	Diode + tranzorb Forward Voltage	$I_F = 10A$		10		V
	R _{thJC}	Junction to Case Thermal Resistance				8	°C/W

CR5 & CR6 diode ratings and characteristics (per diode)

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
V _{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V
I _{RM}	Maximum Reverse Leakage Current	V _R =600V				25	μA
I _F	DC Forward Current		$Tc = 80^{\circ}C$		30		Α
		$I_F = 30A$			1.8	2.2	
$V_{\rm F}$	Diode Forward Voltage	$I_F = 60A$			2.2		V
		$I_F = 30A$	$T_{i} = 125^{\circ}C$		1.5		v
+	Reverse Recovery Time		$T_j = 25^{\circ}C$		25		ns
t _{rr}	Reverse Recovery Time	$I_{\rm F} = 30 A$ $V_{\rm R} = 400 V$	$T_{j} = 125^{\circ}C$		160		115
0	Reverse Recovery Charge	$v_R = 400 v$ di/dt = 200 A/µs	$T_j = 25^{\circ}C$		35		nC
Q _{rr}	Reverse Recovery Charge		$T_{j} = 125^{\circ}C$		480		шС
E _{rr}	Reverse Recovery Energy	$I_F = 30A$ $V_R = 400V$ $di/dt = 1000A/\mu s$	$T_j = 125^{\circ}C$		0.6		mJ
R _{thJC}	Junction to Case Thermal Resistance					1.2	°C/W

CR7 & CR8 diode ratings and characteristics (per diode)

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
V _{RRM}	Maximum Peak Repetitive Reverse Voltage			1200			V
I _{RM}	Maximum Reverse Leakage Current	V _R =1200V				100	μA
I_F	DC Forward Current		$Tc = 80^{\circ}C$		30		Α
		$I_F = 30A$			2.6	3.1	
\mathbf{V}_{F}	Diode Forward Voltage	$I_F = 60A$			3.2		V
		$I_{\rm F} = 30 {\rm A}$	$T_{i} = 125^{\circ}C$		1.8		v
ŧ	Pavarsa Pacavary Tima		$T_j = 25^{\circ}C$		300		100
$\iota_{\rm rr}$		$I_F = 30A$	$T_{j} = 125^{\circ}C$		380		ns
Q _{rr}	Reverse Recovery Charge	$V_R = 800V$ di/dt = 200A/µs	$T_j = 25^{\circ}C$		360		nC
Qn	Reverse Recovery Charge		$T_{j} = 125^{\circ}C$		1700		ш
Err	Reverse Recovery Energy	$I_{\rm F} = 30A$ $V_{\rm R} = 800V$ $di/dt = 1000A/\mu s$	$T_j = 125^{\circ}C$		1.6		mJ
R_{thJC}	Junction to Case Thermal Resistance					1.2	°C/W

Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information).

Symbol	Characteristic		Min	Тур	Max	Unit
R ₂₅	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
B _{25/85}	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta B/B$		T _C =100°C		4		%
	$R_{T} = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$ T: Thermistor temp R _T : Thermistor value					

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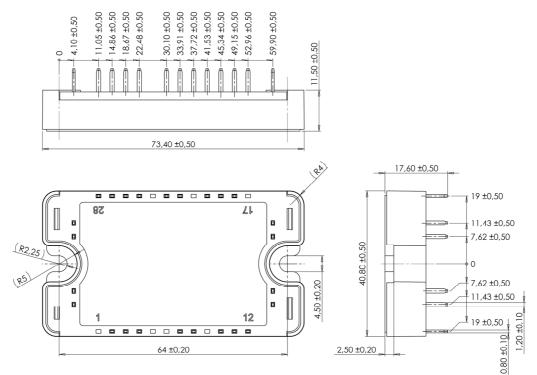


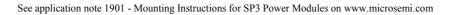
Thermal and package characteristics

Symbol	Characteristic			Min	Тур	Max	Unit
V _{ISOL}	RMS Isolation Voltage, any terminal to case t =1	MS Isolation Voltage, any terminal to case t =1 min, 50/60Hz					V
TJ	Operating junction temperature range			-40		175*	
T _{STG}	Storage Temperature Range			-40		125	°C
T _C	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					110	g

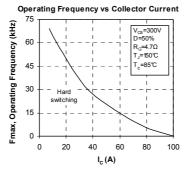
* Tjmax = 150°C for Q1 & Q4

SP3 Package outline (dimensions in mm)



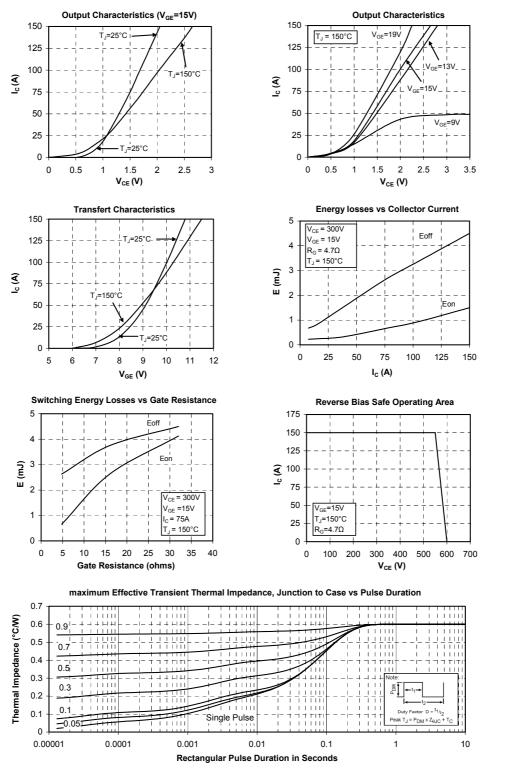


Q2 & Q3 Typical performance curve



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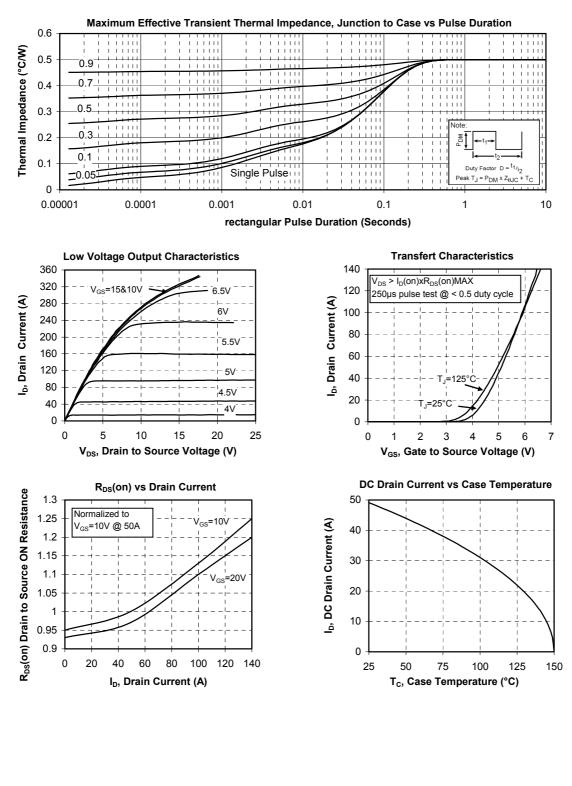




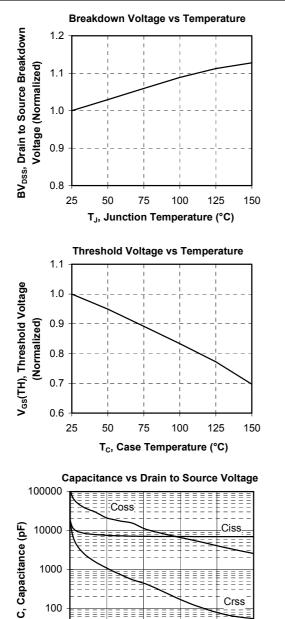
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Q1 & Q4 Typical performance curve







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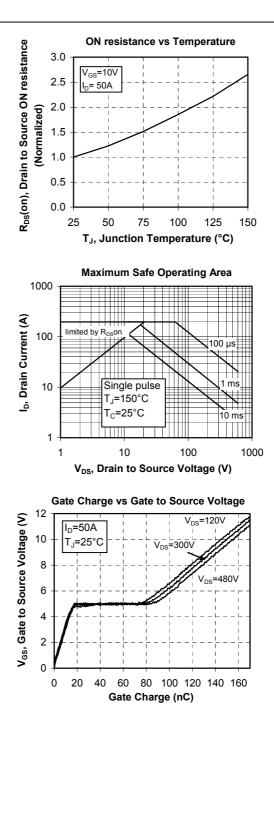
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V_{DS}, Drain to Source Voltage (V)

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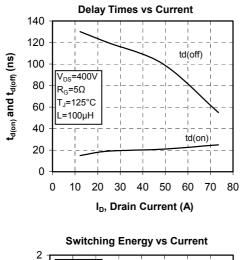
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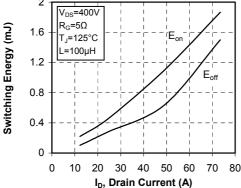
40

Crss

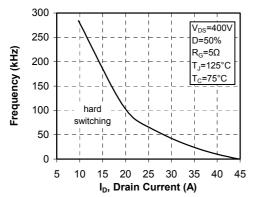
50

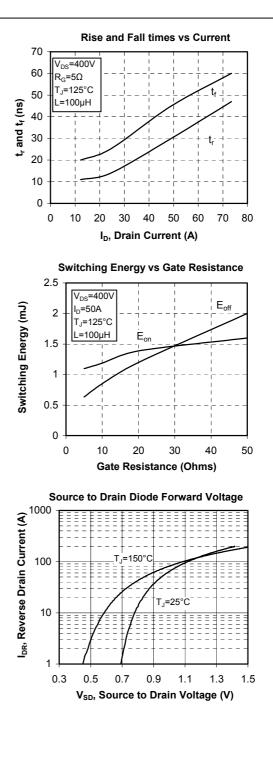






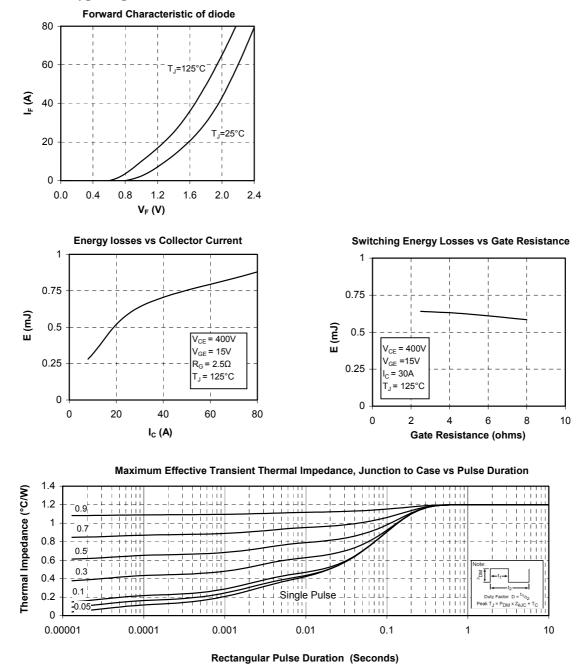






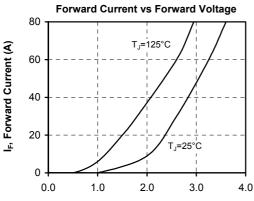


CR5 & CR6 Typical performance curve

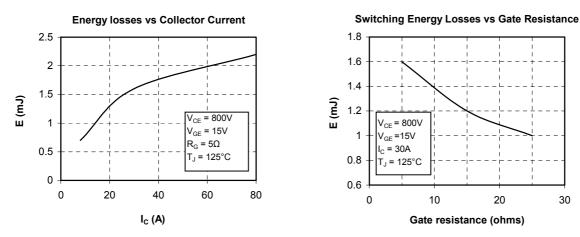


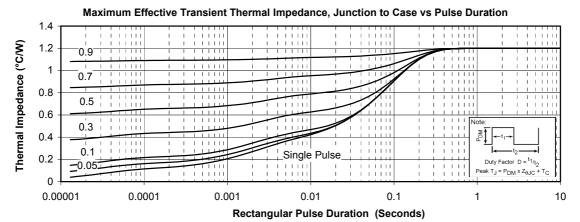


CR7 & CR8 Typical performance curve



V_F, Anode to Cathode Voltage (V)





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