Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

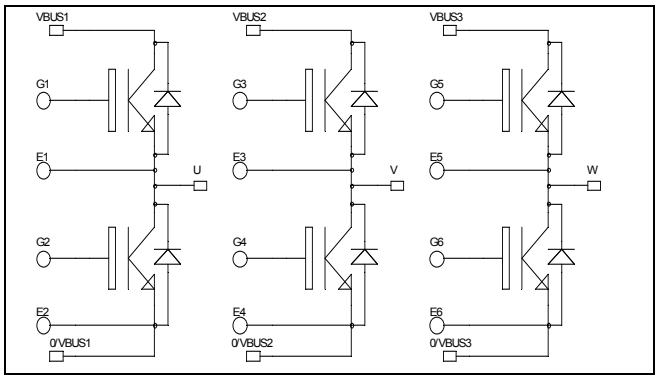
Contact us

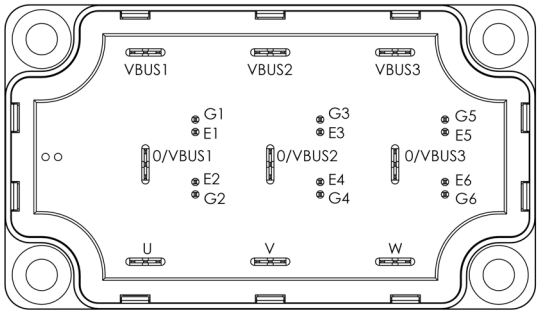
Tel: +86-755-8981 8866 Fax: +86-755-8427 6832
Email & Skype: info@chipsmall.com Web: www.chipsmall.com
Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

APTGF50TA120PG

Triple phase leg NPT IGBT Power Module

V_{CES} = 1200V
I_C = 50A @ Tc = 80°C





Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

- Non Punch Through (NPT) FAST IGBT
  - Low voltage drop
  - Low tail current
  - Switching frequency up to 50 kHz
  - Soft recovery parallel diodes
  - Low diode VF
  - Low leakage current
  - RBSOA and SCSOA rated
- Kelvin emitter for easy drive
- Very low stray inductance
  - Symmetrical design
  - Lead frames for power connections
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Very low (12mm) profile
- Easy paralleling due to positive TC of VCEsat
- Each leg can be easily paralleled to achieve a phase leg of three times the current capability
- Module can be configured as a three phase bridge
- Module can be configured as a boost followed by a full bridge
- RoHS compliant

Absolute maximum ratings

Symbol	*Parameter*		*Max ratings*	*Unit*
V_{CES}	Collector - Emitter Breakdown Voltage		1200	V
I_C	Continuous Collector Current	T_c = 25°C	75	A
		T_c = 80°C	50	
I_{CM}	Pulsed Collector Current	T_c = 25°C	150	
V_{GE}	Gate – Emitter Voltage		±20	V
P_D	Maximum Power Dissipation	T_c = 25°C	312	W
RBSOA	Reverse Bias Safe Operating Area	T_j = 150°C	100A @ 1200V	

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

All ratings @ T_j = 25°C unless otherwise specified

Electrical Characteristics

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
I_{CES}	Zero Gate Voltage Collector Current	V_{GE} = 0V, V_{CE} = 1200V	T_j = 25°C			250	µA
			T_j = 125°C			500	
$V_{CE(sat)}$	Collector Emitter saturation Voltage	V_{GE} =15V, I_C = 50A	T_j = 25°C		3.2	3.7	V
			T_j = 125°C		4.0		
$V_{GE(th)}$	Gate Threshold Voltage	V_{GE} = V_{CE}, I_C = 1 mA		4.5		6.5	V
I_{GES}	Gate – Emitter Leakage Current	V_{GE} = 20 V, V_{CE} = 0V				100	nA

Dynamic Characteristics

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
C_{ies}	Input Capacitance	V_{GE} = 0V V_{CE} = 25V f = 1MHz			3450		pF
C_{oes}	Output Capacitance				330		
C_{res}	Reverse Transfer Capacitance				220		
Q_g	Total gate Charge	V_{GS} = 15V V_{Bus} = 600V I_C = 50A			330		nC
Q_{ge}	Gate – Emitter Charge				35		
Q_{gc}	Gate – Collector Charge				200		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (25°C) V_{GE} = 15V V_{Bus} = 600V I_C = 50A R_G = 5 Ω			35		ns
T_r	Rise Time				65		
$T_{d(off)}$	Turn-off Delay Time				320		
T_f	Fall Time				30		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (125°C) V_{GE} = ±15V V_{Bus} = 600V I_C = 50A R_G = 5 Ω			35		ns
T_r	Rise Time				65		
$T_{d(off)}$	Turn-off Delay Time				360		
T_f	Fall Time				40		
E_{on}	Turn-on Switching Energy	V_{GE} = ±15V V_{Bus} = 600V I_C = 50A R_G = 5 Ω	T_j = 125°C		6.9		mJ
E_{off}	Turn-off Switching Energy		T_j = 125°C		3.05		

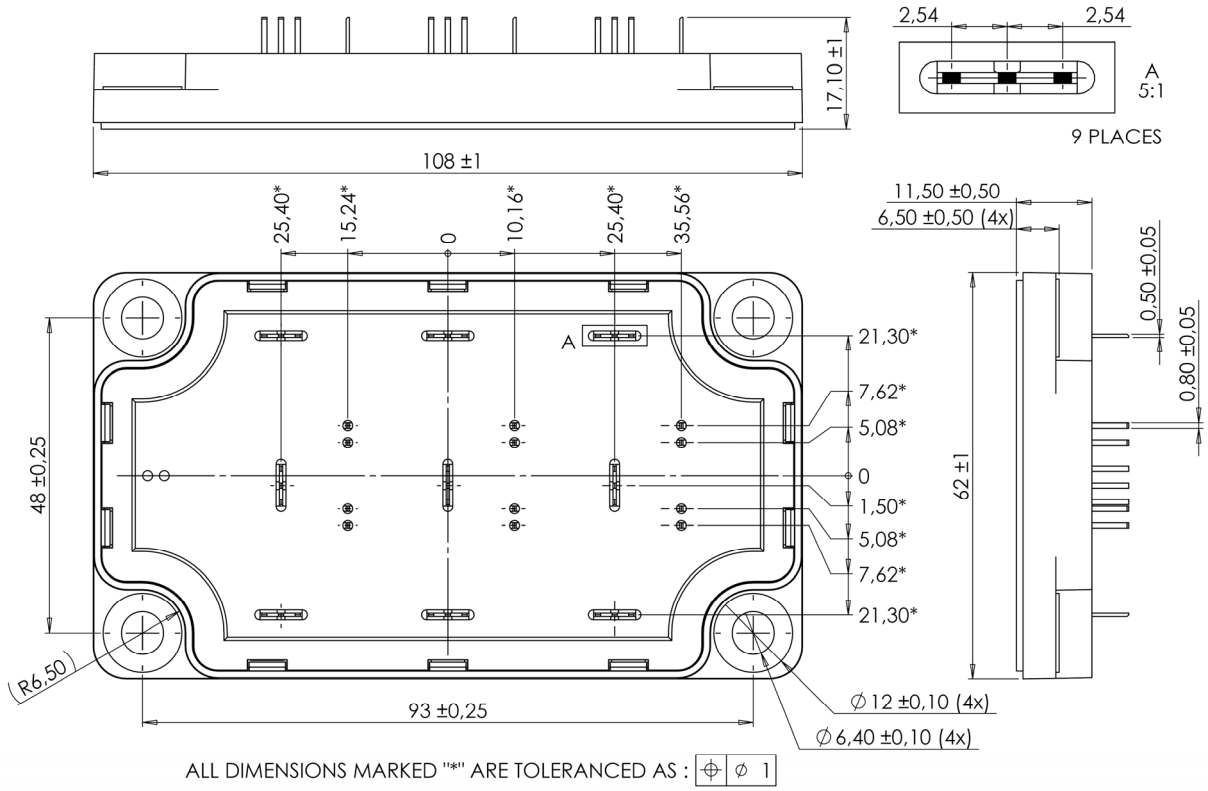
Chopper diode ratings and characteristics

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			1200			V
I_{RM}	Maximum Reverse Leakage Current	V_R=1200V	T_j = 25°C			250	µA
			T_j = 125°C			500	
I_F	DC Forward Current		Tc = 70°C		60		A
V_F	Diode Forward Voltage	I_F = 60A			2.0	2.5	V
		I_F = 120A			2.3		
		I_F = 60A	T_j = 125°C		1.8		
t_{rr}	Reverse Recovery Time	I_F = 60A V_R = 800V di/dt =200A/µs	T_j = 25°C		400		ns
			T_j = 125°C		470		
Q_{rr}	Reverse Recovery Charge		T_j = 25°C		1200		nC
			T_j = 125°C		4000		

Thermal and package characteristics

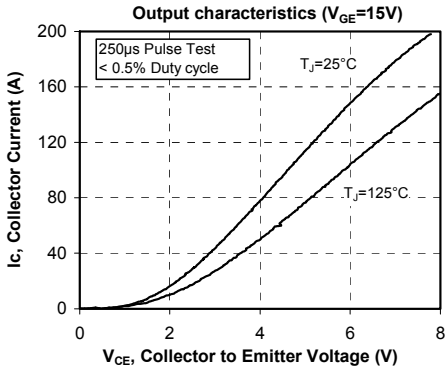
Symbol	*Characteristic*			*Min*	*Typ*	*Max*	*Unit*
R_{thJC}	Junction to Case Thermal Resistance		IGBT			0.4	°C/W
			Diode			0.9	
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T_J	Operating junction temperature range			-40		150	°C
T_{STG}	Storage Temperature Range			-40		125	
T_C	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M6	3		5	N.m
Wt	Package Weight					250	g

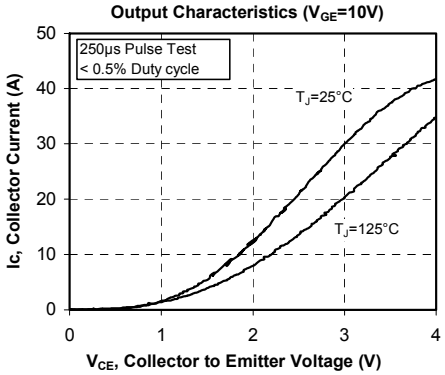
SP6-P Package outline (dimensions in mm)

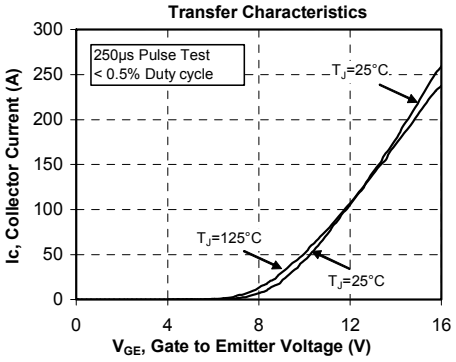


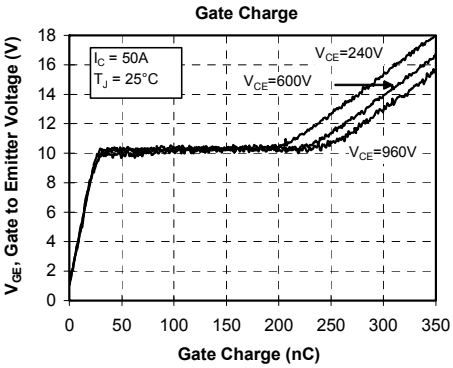
See application note 1902 - Mounting Instructions for SP6-P (12mm) Power Modules on www.microsemi.com

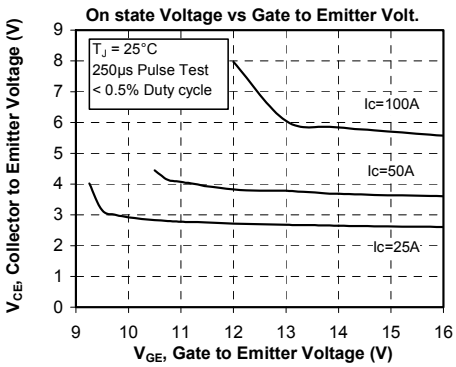
Typical Performance Curve

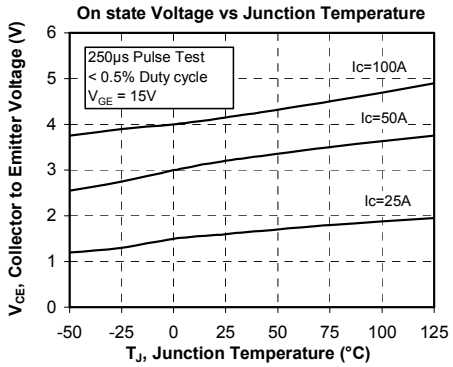


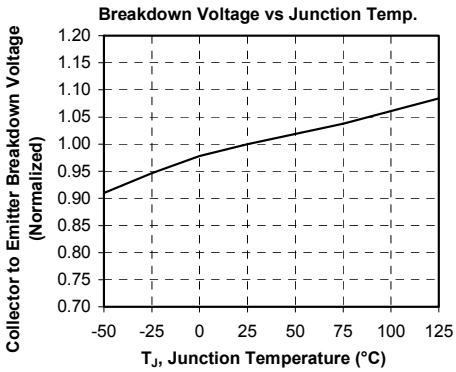


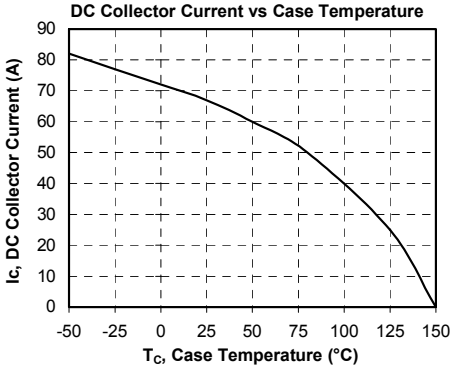




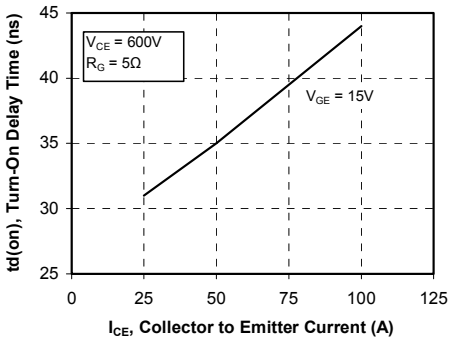




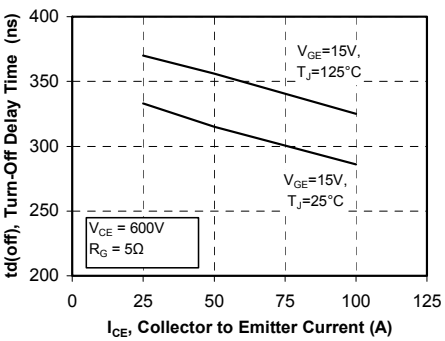




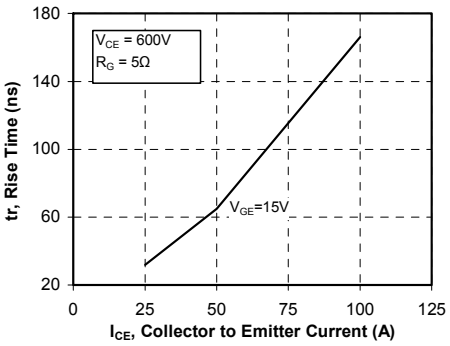
Turn-On Delay Time vs Collector Current



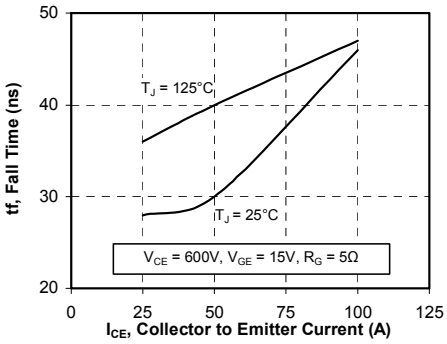
Turn-Off Delay Time vs Collector Current



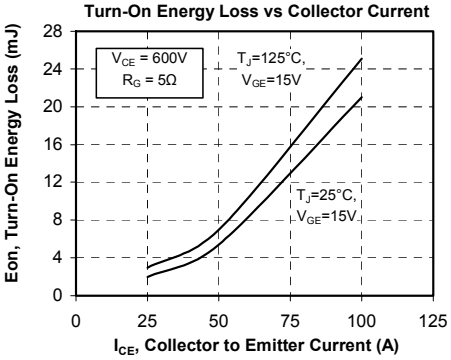
Current Rise Time vs Collector Current



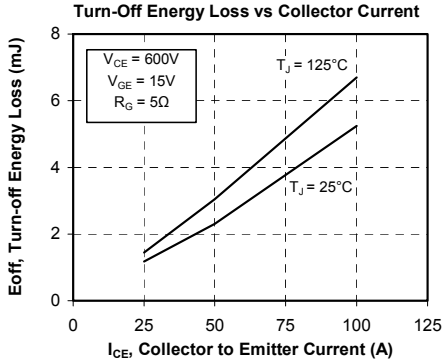
Current Fall Time vs Collector Current



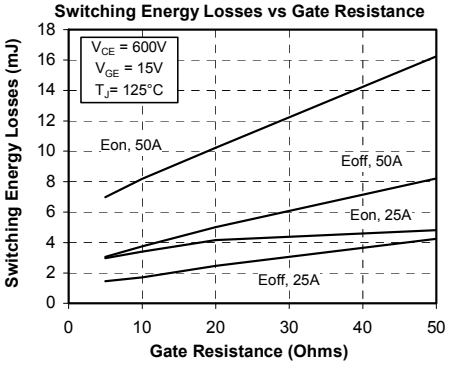
Turn-On Energy Loss vs Collector Current



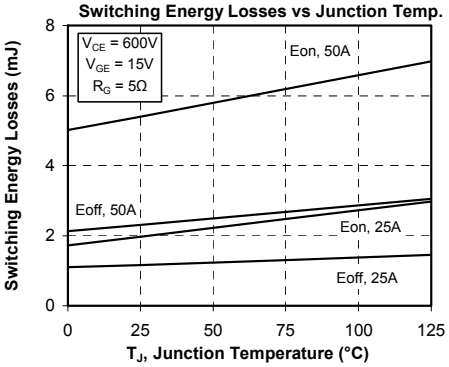
Turn-Off Energy Loss vs Collector Current



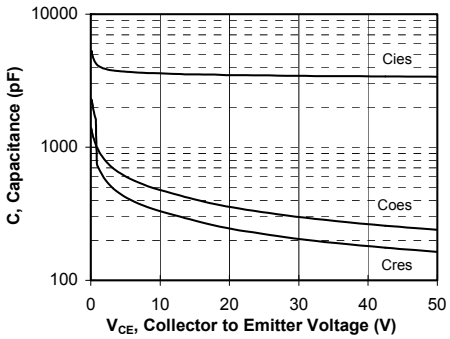
Switching Energy Losses vs Gate Resistance



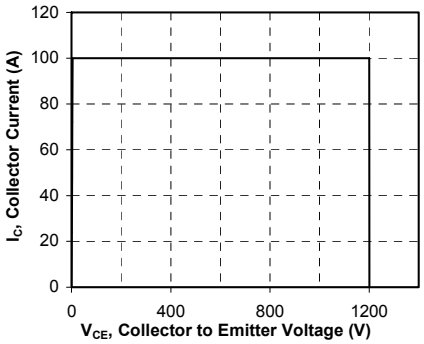
Switching Energy Losses vs Junction Temp.



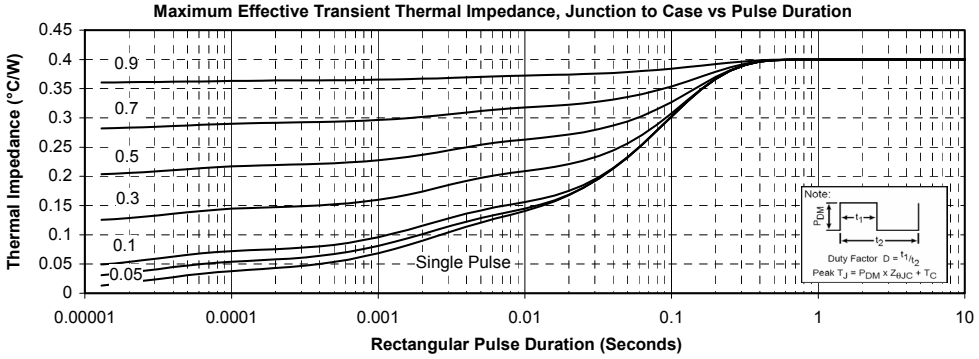
Capacitance vs Collector to Emitter Voltage



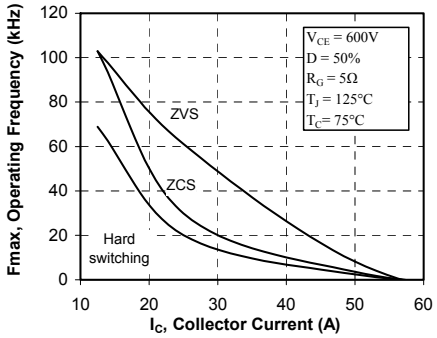
Reverse Bias Safe Operating Area



Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration



Operating Frequency vs Collector Current



DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with life-support or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at http://www.microsemi.com/legal/tnc.asp

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.