imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



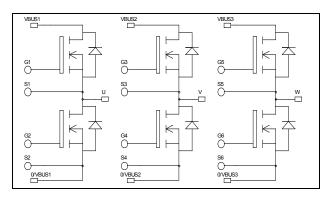
Contact us

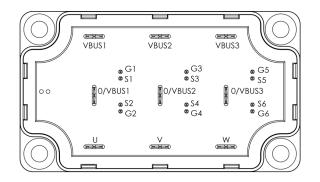
Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





Triple phase leg MOSFET Power Module





Absolute maximum ratings

Symbol Parameter Max ratings Unit Drain - Source Breakdown Voltage 75 V V_{DSS} $T_c = 25^{\circ}C$ 120 Continuous Drain Current I_D $T_c = 80^{\circ}C$ 90 Α I<u>DM</u> Pulsed Drain current 250 Gate - Source Voltage ± 30 V V_{GS} **R**_{DSon} Drain - Source ON Resistance 4.5 mΩ Maximum Power Dissipation $T_c = 25^{\circ}C$ $P_{\rm D}$ 138 W Avalanche current (repetitive and non repetitive) 75 I_{AR} А 50 Repetitive Avalanche Energy EAR mJ Single Pulse Avalanche Energy EAS 1500

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

$V_{DSS} = 75V$ $R_{DSon} = 4.2m\Omega \max @ Tj = 25^{\circ}C$ $I_D = 120A @ Tc = 25^{\circ}C$

Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

- Power MOSFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Fast intrinsic diode
 - Avalanche energy rated
 - Very rugged
 - Kelvin source for easy drive
 - Very low stray inductance
 - Symmetrical design
 - Lead frames for power connections
 - High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Very low (12mm) profile
- Each leg can be easily paralleled to achieve a phase leg of three times the current capability
- Module can be configured as a three phase bridge
- Module can be configured as a boost followed by a full bridge
- RoHS Compliant

1 - 7



All ratings (a) $T_j = 25^{\circ}C$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I _{DSS}	Zana Cata Malta as Duain Comment	$V_{GS} = 0V, V_{DS} = 75V$ $T_j = 25^{\circ}C$			100	
	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 60V$ $T_j = 125^{\circ}C$			250	μA
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 60A$		4.2	4.5	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 1 \text{mA}$	2		4	V
I _{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C _{iss}	Input Capacitance	$V_{GS} = 0V$		4530		
C _{oss}	Output Capacitance	$V_{\rm DS} = 25 V$		1080		pF
C _{rss}	Reverse Transfer Capacitance	f = 1MHz		450		
Qg	Total gate Charge	$V_{GS} = 10V$		153		
Q _{gs}	Gate – Source Charge	$V_{Bus} = 60V$		25		nC
Q_{gd}	Gate – Drain Charge	$I_D = 120A$		82		
T _{d(on)}	Turn-on Delay Time	Inductive switching @ 125°C $V_{GS} = 15V$ $V_{Bus} = 40V$ $I_D = 120A$ $R_G = 5\Omega$		35		ns
Tr	Rise Time			60		
$T_{d(off)}$	Turn-off Delay Time			100		
$T_{\rm f}$	Fall Time			65		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C $V_{GS} = 15V, V_{Bus} = 40V$ $I_D = 120A, R_G = 5\Omega$		290		μJ
E _{off}	Turn-off Switching Energy			317		
Eon	Turn-on Switching Energy	Inductive switching @ 125°C $V_{GS} = 15V, V_{Bus} = 40V$ $I_D = 120A, R_G = 5\Omega$		319		T
$\mathrm{E}_{\mathrm{off}}$	Turn-off Switching Energy			336		μJ

Source - Drain diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
т	Continuous Source current		$Tc = 25^{\circ}C$			120	•
Is	(Body diode)		$Tc = 80^{\circ}C$			90	A
V _{SD}	Diode Forward Voltage	$V_{GS} = 0V, I_S = -120A$				1.3	V
dv/dt	Peak Diode Recovery 1					6	V/ns
t _{rr}	Reverse Recovery Time	$I_{\rm S} = -120A$	$T_j = 25^{\circ}C$		100	200	ns
Q _{rr}	Reverse Recovery Charge	$V_R = 40V$ $di_S/dt = 100A/\mu s$	$T_j = 25^{\circ}C$		300		nC

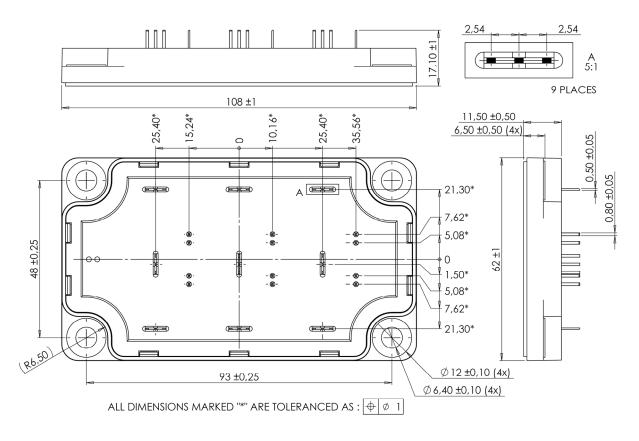
• dv/dt numbers reflect the limitations of the circuit rather than the device itself. $I_S \leq -120A$ di/dt $\leq 700A/\mu s$ $V_R \leq V_{DSS}$ $T_j \leq 150^{\circ}C$



Thermal and package characteristics

Symbol	Characteristic		Min	Тур	Max	Unit	
R _{thJC}	Junction to Case Thermal Resistance					0.9	°C/W
V _{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T _J	Operating junction temperature range			-40		150	
T _{STG}	Storage Temperature Range			-40		125	°C
T _C	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M6	3		5	N.m
Wt	Package Weight					250	g

SP6-P Package outline (dimensions in mm)

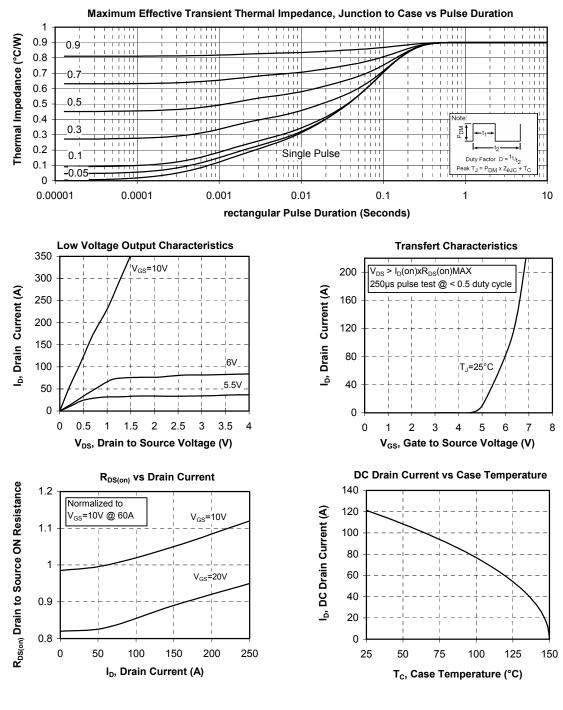


See application note 1902 - Mounting Instructions for SP6-P (12mm) Power Modules on www.microsemi.com

www.microsemi.com



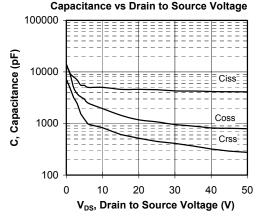
Typical Performance Curve



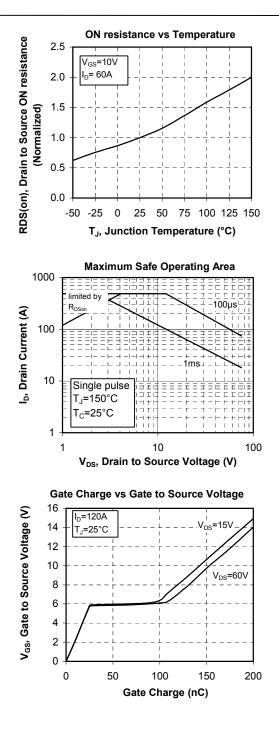
www.microsemi.com



Breakdown Voltage vs Temperature 1.15 **BV_{DSS}, Drain to Source Breakdown** 1.10 Voltage (Normalized) 1.05 1.00 0.95 0.90 25 50 75 100 125 150 -50 -25 0 T_J, Junction Temperature (°C) **Threshold Voltage vs Temperature** 1.2 V_{GS}(TH), Threshold Voltage 1.1 1.0 (Normalized) 0.9 0.8 0.7 0.6 -50 -25 0 25 50 75 100 125 150 T_c, Case Temperature (°C)



APTM08TAM04PG



APTM08TAM04P G- Rev 2 October, 2012

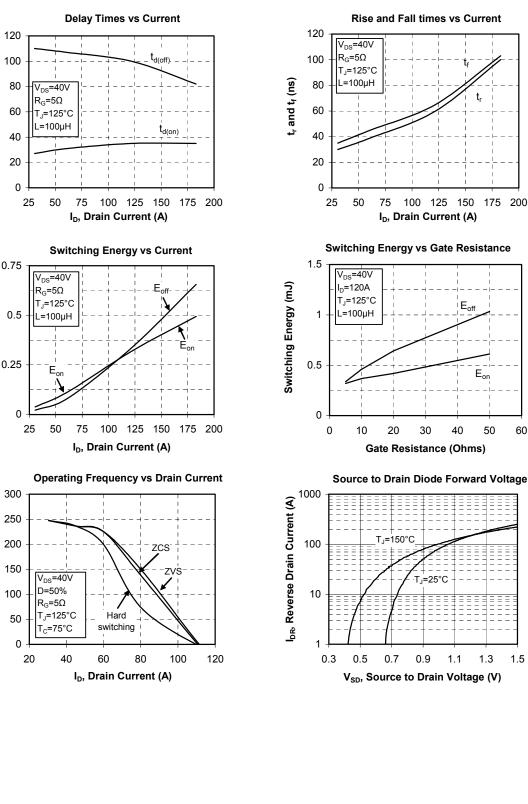


t_{d(on)} and t_{d(off)} (ns)

Eon and Eoff (mJ)

Frequency (kHz)

APTM08TAM04PG





www.microsemi.com

6 – 7



DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with lifesupport or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at http://www.microsemi.com/legal/tnc.asp

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.