# imall

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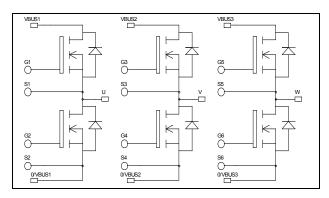
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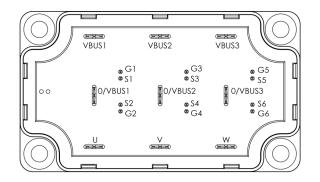
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Triple phase leg MOSFET Power Module





#### Absolute maximum ratings

#### Symbol Parameter Max ratings Unit Drain - Source Breakdown Voltage 75 V V<sub>DSS</sub> $T_c = 25^{\circ}C$ 120 Continuous Drain Current $I_D$ $T_c = 80^{\circ}C$ 90 Α I<u>DM</u> Pulsed Drain current 250 Gate - Source Voltage $\pm 30$ V V<sub>GS</sub> **R**<sub>DSon</sub> Drain - Source ON Resistance 4.5 mΩ Maximum Power Dissipation $T_c = 25^{\circ}C$ $P_{\rm D}$ 138 W Avalanche current (repetitive and non repetitive) 75 I<sub>AR</sub> А 50 Repetitive Avalanche Energy EAR mJ Single Pulse Avalanche Energy EAS 1500

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

### $V_{DSS} = 75V$ $R_{DSon} = 4.2m\Omega \max @ Tj = 25^{\circ}C$ $I_D = 120A @ Tc = 25^{\circ}C$

#### Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

#### Features

- Power MOSFETs
  - Low R<sub>DSon</sub>
    - Low input and Miller capacitance
    - Low gate charge
    - Fast intrinsic diode
    - Avalanche energy rated
    - Very rugged
  - Kelvin source for easy drive
  - Very low stray inductance
    - Symmetrical design
    - Lead frames for power connections
    - High level of integration

#### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Very low (12mm) profile
- Each leg can be easily paralleled to achieve a phase leg of three times the current capability
- Module can be configured as a three phase bridge
- Module can be configured as a boost followed by a full bridge
- RoHS Compliant

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#### All ratings (a) $T_j = 25^{\circ}C$ unless otherwise specified

#### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I <sub>DSS</sub>	Zana Cata Malta as Duain Comment	$V_{GS} = 0V, V_{DS} = 75V$ $T_j = 25^{\circ}C$			100	
	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 60V$ $T_j = 125^{\circ}C$			250	μA
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 60A$		4.2	4.5	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 1 \text{mA}$	2		4	V
I <sub>GSS</sub>	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

#### **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C <sub>iss</sub>	Input Capacitance	$V_{GS} = 0V$		4530		
C <sub>oss</sub>	Output Capacitance	$V_{\rm DS} = 25 V$		1080		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz		450		
Qg	Total gate Charge	$V_{GS} = 10V$		153		
Q <sub>gs</sub>	Gate – Source Charge	$V_{Bus} = 60V$		25		nC
$Q_{gd}$	Gate – Drain Charge	$I_D = 120A$		82		
T <sub>d(on)</sub>	Turn-on Delay Time	Inductive switching @ 125°C $V_{GS} = 15V$ $V_{Bus} = 40V$ $I_D = 120A$ $R_G = 5\Omega$		35		ns
Tr	Rise Time			60		
$T_{d(off)}$	Turn-off Delay Time			100		
$T_{\rm f}$	Fall Time			65		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C $V_{GS} = 15V, V_{Bus} = 40V$ $I_D = 120A, R_G = 5\Omega$		290		μJ
E <sub>off</sub>	Turn-off Switching Energy			317		
Eon	Turn-on Switching Energy	Inductive switching @ 125°C $V_{GS} = 15V, V_{Bus} = 40V$ $I_D = 120A, R_G = 5\Omega$		319		T
$\mathrm{E}_{\mathrm{off}}$	Turn-off Switching Energy			336		μJ

#### Source - Drain diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
т	Continuous Source current		$Tc = 25^{\circ}C$			120	•
Is	(Body diode)		$Tc = 80^{\circ}C$			90	A
V <sub>SD</sub>	Diode Forward Voltage	$V_{GS} = 0V, I_S = -120A$				1.3	V
dv/dt	Peak Diode Recovery <b>1</b>					6	V/ns
t <sub>rr</sub>	Reverse Recovery Time	$I_{\rm S} = -120A$	$T_j = 25^{\circ}C$		100	200	ns
Q <sub>rr</sub>	Reverse Recovery Charge	$V_R = 40V$ $di_S/dt = 100A/\mu s$	$T_j = 25^{\circ}C$		300		nC

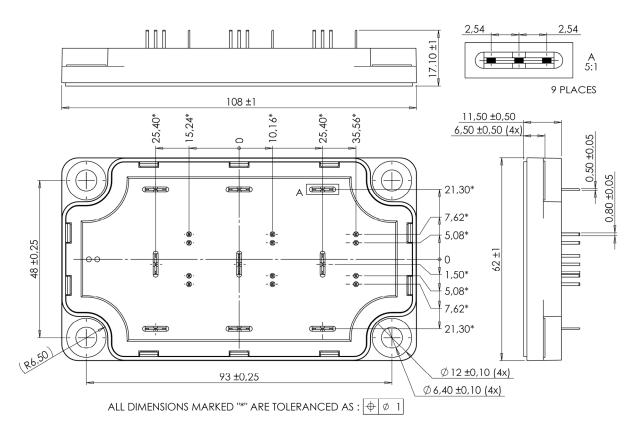
• dv/dt numbers reflect the limitations of the circuit rather than the device itself.  $I_S \leq -120A$  di/dt  $\leq 700A/\mu s$   $V_R \leq V_{DSS}$   $T_j \leq 150^{\circ}C$ 



#### Thermal and package characteristics

Symbol	Characteristic		Min	Тур	Max	Unit	
R <sub>thJC</sub>	Junction to Case Thermal Resistance					0.9	°C/W
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T <sub>J</sub>	Operating junction temperature range			-40		150	
T <sub>STG</sub>	Storage Temperature Range			-40		125	°C
T <sub>C</sub>	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M6	3		5	N.m
Wt	Package Weight					250	g

SP6-P Package outline (dimensions in mm)

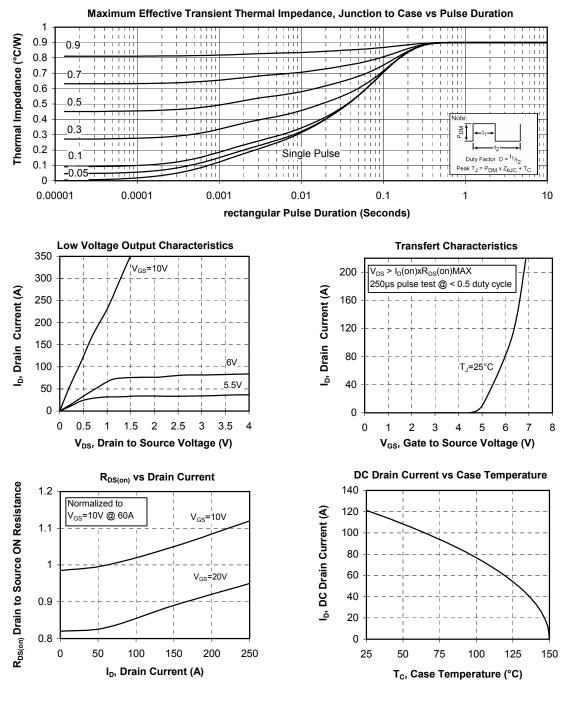


See application note 1902 - Mounting Instructions for SP6-P (12mm) Power Modules on www.microsemi.com

www.microsemi.com



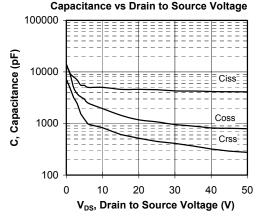
#### **Typical Performance Curve**



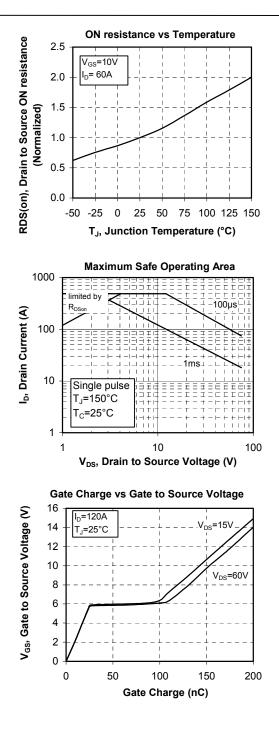
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#### Breakdown Voltage vs Temperature 1.15 **BV<sub>DSS</sub>, Drain to Source Breakdown** 1.10 Voltage (Normalized) 1.05 1.00 0.95 0.90 25 50 75 100 125 150 -50 -25 0 T<sub>J</sub>, Junction Temperature (°C) **Threshold Voltage vs Temperature** 1.2 V<sub>GS</sub>(TH), Threshold Voltage 1.1 1.0 (Normalized) 0.9 0.8 0.7 0.6 -50 -25 0 25 50 75 100 125 150 T<sub>c</sub>, Case Temperature (°C)



## APTM08TAM04PG



APTM08TAM04P G- Rev 2 October, 2012

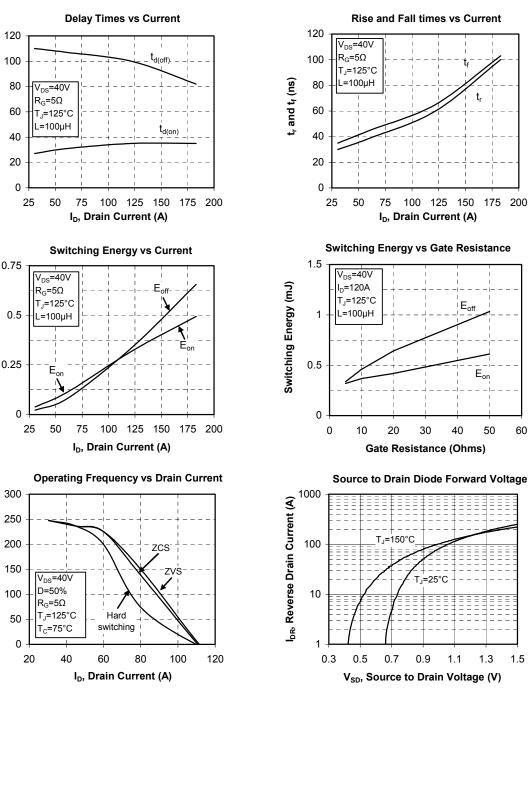


t<sub>d(on)</sub> and t<sub>d(off)</sub> (ns)

Eon and Eoff (mJ)

Frequency (kHz)

### APTM08TAM04PG





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