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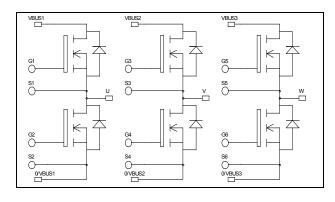








Triple phase leg MOSFET Power Module



$$\begin{split} V_{DSS} &= 1000 V \\ R_{DSon} &= 350 m \Omega \text{ typ @ Tj} = 25^{\circ} C \\ I_D &= 22 A \text{ @ Tc} = 25^{\circ} C \end{split}$$

Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

- Power MOS 7[®] FREDFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Fast intrinsic reverse diode
 - Avalanche energy rated
 - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
 - Symmetrical design
 - Lead frames for power connections
- High level of integration



- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Very low (12mm) profile
- Each leg can be easily paralleled to achieve a phase leg of three times the current capability
- Module can be configured as a three phase bridge
- Module can be configured as a boost followed by a full bridge
- RoHS Compliant

GI G3 G5 S1 S3 S5 OO PO/VBUS1 PO/VBUS2 PO/VBUS3 S2 S4 S6 G2 G4 G6	VBUS1	VBUS2	VBUS3
● G2 ● G4 ● G6 ■ U V W		∭0/VBUS2	
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Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		1000	V
т	Continuous Drain Current $T_c = 25^{\circ}C$		22	
I_D	$T_c = 80^{\circ}C$		17	A
I_{DM}	Pulsed Drain current		88	
V_{GS}	Gate - Source Voltage		±30	V
R_{DSon}	Drain - Source ON Resistance		420	mΩ
P_{D}	Maximum Power Dissipation $T_c = 25^{\circ}C$		390	W
I_{AR}	Avalanche current (repetitive and non repetitive)		25	A
E _{AR}	Repetitive Avalanche Energy		50	ma I
E_{AS}	Single Pulse Avalanche Energy		3000	mJ

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



All ratings @ $T_j = 25$ °C unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
T	Zara Cata Waltaga Drain Current	$V_{GS} = 0V, V_{DS} = 1000V$	$T_j = 25^{\circ}C$			100	4
$I_{ m DSS}$	Zero Gate Voltage Drain Current $ V_{GS} = 0V, V_{DS} = 800V $ $ T_j = 125^{\circ}C $				500	μΑ	
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 11A$			350	420	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 2.5 \text{mA}$		3		5	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$				±100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		5.2		
C_{oss}	Output Capacitance	$V_{DS} = 25V$		0.88		nF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		0.16		
Q_{g}	Total gate Charge	$V_{GS} = 10V$		186		
Q_{gs}	Gate – Source Charge	$V_{\text{Bus}} = 500\text{V}$		24		nC
Q_{gd}	Gate – Drain Charge	$I_D = 22A$		122		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @ 125°C		18		
$T_{\rm r}$	Rise Time	$V_{GS} = 15V$		12		
$T_{d(off)}$	Turn-off Delay Time	$V_{\text{Bus}} = 670V$ $I_{\text{D}} = 22A$		155		ns
T_{f}	Fall Time	$R_G = 5\Omega$		40		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		900		
E _{off}	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 670V$ $I_D = 22A, R_G = 5\Omega$		623		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		1423		
E _{off}	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 670V$ $I_D = 22A, R_G = 5\Omega$		779		μJ

Source - Drain diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
I_S	Continuous Source current		$Tc = 25^{\circ}C$			22	Δ
	(Body diode)		$Tc = 80^{\circ}C$			17	Α
V_{SD}	Diode Forward Voltage	$V_{GS} = 0V, I_{S} = -22A$	L			1.3	V
dv/dt	Peak Diode Recovery •					18	V/ns
+	Davarsa Dagayary Tima		$T_j = 25^{\circ}C$			320	nc
t_{rr}	Reverse Recovery Time	$I_S = -22A$ $V_R = 670V$	$T_j = 125$ °C			650	ns
	Davarga Dagayary Charga	1: /1/ 100 4 /	$T_j = 25$ °C		3.6		C
Qrr	Reverse Recovery Charge		$T_{\rm j} = 125^{\circ}{\rm C}$		9.72		μС

• dv/dt numbers reflect the limitations of the circuit rather than the device itself.

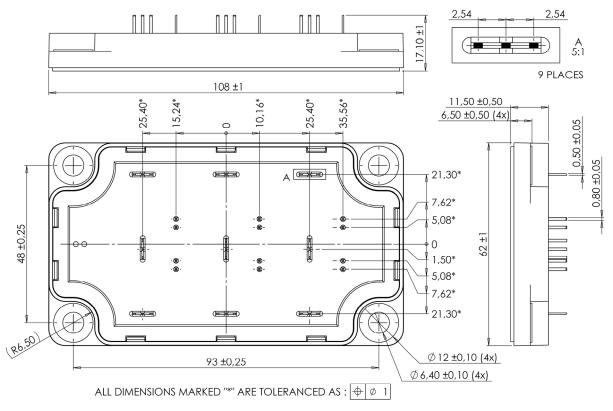
 $I_S \le$ - 22A $di/dt \le 700 A/\mu s$ $V_R \le V_{DSS}$ $T_j \le 150 ^{\circ} C$



Thermal and package characteristics

Symbol	Characteristic			Min	Тур	Max	Unit
R_{thJC}	Junction to Case Thermal Resistance					0.32	°C/W
V_{ISOL}	RMS Isolation Voltage, any terminal to case $t = 1 \text{ min}$, $50/60\text{Hz}$			4000			V
T_{J}	Operating junction temperature range			-40		150	
T_{STG}	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M6	3		5	N.m
Wt	Package Weight					250	g

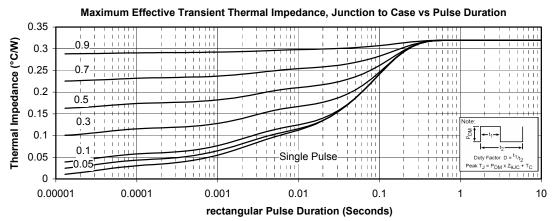
SP6-P Package outline (dimensions in mm)

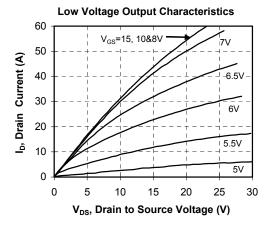


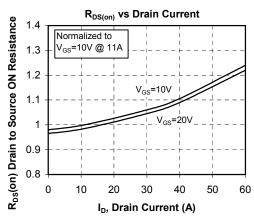
See application note 1902 - Mounting Instructions for SP6-P (12mm) Power Modules on www.microsemi.com

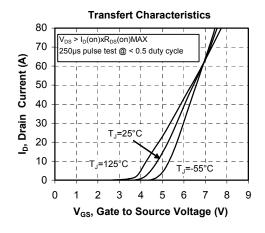


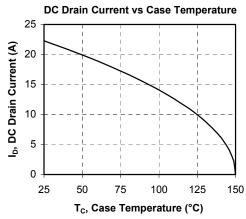
Typical Performance Curve



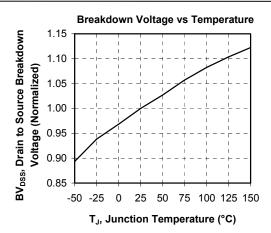


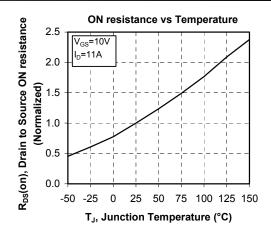


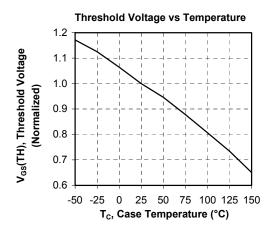


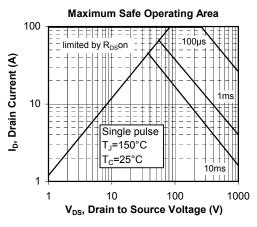


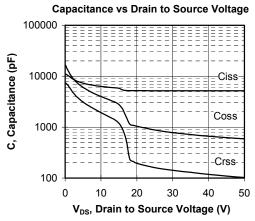


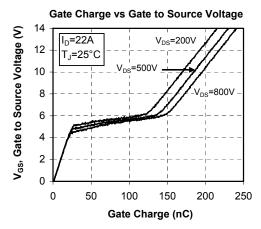




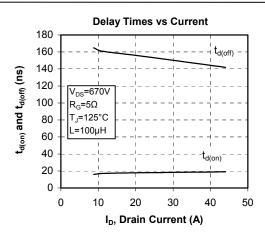


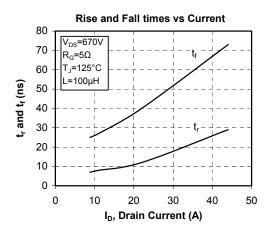


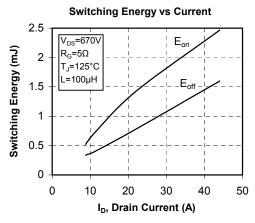


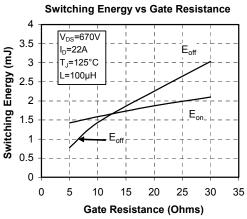


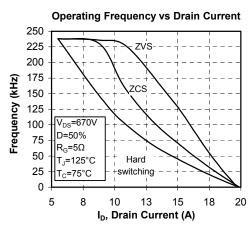


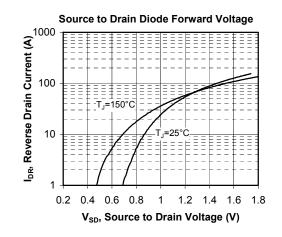














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