# imall

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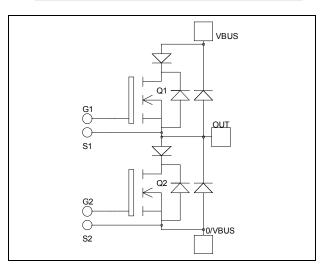
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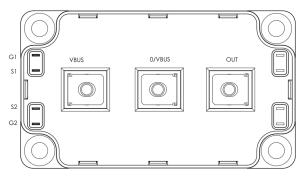
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Phase leg Series & parallel diodes MOSFET Power Module





# APTM50AM24SG

### $V_{DSS} = 500V$ $R_{DSon} = 24m\Omega \text{ typ } @ \text{ Tj} = 25^{\circ}C$ $I_D = 150A @ \text{ Tc} = 25^{\circ}C$

#### Application

- Motor control
  - Switched Mode Power Supplies
- Uninterruptible Power Supplies

#### Features

- Power MOS 7<sup>®</sup> MOSFETs
  - Low R<sub>DSon</sub>
    - Low input and Miller capacitance
  - Low gate charge
  - Fast intrinsic reverse diode
  - Avalanche energy rated
  - Very rugged
- Kelvin source for easy drive
  - Very low stray inductance
    - Symmetrical design
  - M5 power connectors
- High level of integration

#### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS Compliant

### All ratings (a) $T_i = 25^{\circ}C$ unless otherwise specified

### Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
V <sub>DSS</sub>	Drain - Source Breakdown Voltage		500	V
т	Continuous Drain Comment	$T_c = 25^{\circ}C$	150	
I <sub>D</sub>	Continuous Drain Current	$T_c = 80^{\circ}C$	110	А
I <sub>DM</sub>	Pulsed Drain current		600	
V <sub>GS</sub>	Gate - Source Voltage		±30	V
R <sub>DSon</sub>	Drain - Source ON Resistance		28	mΩ
P <sub>D</sub>	Maximum Power Dissipation $T_c = 25^{\circ}C$		1250	W
I <sub>AR</sub>	Avalanche current (repetitive and non repetitive)		24	А
E <sub>AR</sub>	Repetitive Avalanche Energy		30	
E <sub>AS</sub>	Single Pulse Avalanche Energy		1300	mJ

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 500V$			500	μΑ
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 75A$		24	28	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 6mA$	3		5	V
I <sub>GSS</sub>	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			$\pm 600$	nA

### **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C <sub>iss</sub>	Input Capacitance	$V_{GS} = 0V$		19.6		
C <sub>oss</sub>	Output Capacitance	$V_{\rm DS} = 25 V$		4.2		nF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1 MHz		0.3		
Qg	Total gate Charge	$V_{GS} = 10V$		434		
Q <sub>gs</sub>	Gate – Source Charge	$V_{Bus} = 250V$		120		nC
$Q_{\text{gd}}$	Gate – Drain Charge	$I_{\rm D} = 150 {\rm A}$		216		
T <sub>d(on)</sub>	Turn-on Delay Time	Inductive switching @ 125°C		10		
Tr	Rise Time	$V_{GS} = 15V$		17		ns
T <sub>d(off)</sub>	Turn-off Delay Time	$V_{Bus} = 333V$ $I_D = 150A$		50		
$T_{\mathrm{f}}$	Fall Time	$R_G = 0.8\Omega$		41		
Eon	Turn-on Switching Energy	Inductive switching @ $25^{\circ}C$		1.9		mI
E <sub>off</sub>	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 150A, R_G = 0.8\Omega$		1.5		mJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		3.3		I and
E <sub>off</sub>	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 150A, R_G = 0.8\Omega$		1.7		mJ
R <sub>thJC</sub>	Junction to Case Thermal Resistance				0.1	°C/W

### Series diode ratings and characteristics

Symbol	Characteristic	Characteristic Test Conditions		Min	Тур	Max	Unit
V <sub>RRM</sub>	Maximum Peak Repetitive Reverse Volt	age		600			V
I <sub>RM</sub>	Maximum Reverse Leakage Current	$V_{R} = 600V$				150	μA
I <sub>F</sub>	DC Forward Current		$Tc = 80^{\circ}C$		200		Α
V	Diode Forward Voltage	$I_{\rm F} = 200 {\rm A}$	$T_i = 25^{\circ}C$		1.6	2	V
$V_{\rm F}$		$V_{GE} = 0V$	$T_{i} = 150^{\circ}C$		1.5		v
+	Deverse Deservery Time		$T_j = 25^{\circ}C$		125		
t <sub>rr</sub>	Reverse Recovery Time		$T_{j} = 150^{\circ}C$		220		ns
0	Pawaraa Paaawary Charga	$V_{P} = 300V$	$T_j = 25^{\circ}C$		9.4		чC
Q <sub>rr</sub>	Reverse Recovery Charge		$T_{j} = 150^{\circ}C$		19.8		μC
Б			$T_j = 25^{\circ}C$		2.2		mI
Er	Reverse Recovery Energy		$T_{j} = 150^{\circ}C$		4.8		mJ
R <sub>thJC</sub>	Junction to Case Thermal Resistance					0.39	°C/W



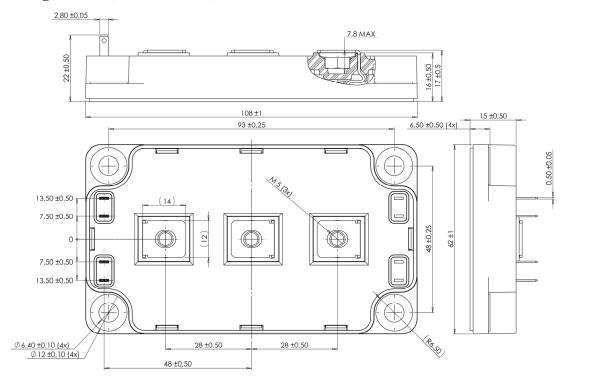
#### Parallel diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
V <sub>RRM</sub>	Maximum Repetitive Reverse Voltage	e		600			V
I <sub>RM</sub>	Maximum Reverse Leakage Current	$V_{R} = 600V$				350	μA
I <sub>F</sub>	DC Forward Current		$T_c = 70^{\circ}C$		120		Α
		$I_{\rm F} = 120 {\rm A}$			1.6	1.8	
V <sub>F</sub>	Diode Forward Voltage	$I_{\rm F} = 240 {\rm A}$		1.9		V	
		$I_{\rm F} = 120 {\rm A}$	$T_{j} = 125^{\circ}C$		1.4		
t	Powerza Pasawary Tima		$T_j = 25^{\circ}C$		130		20
t <sub>rr</sub>	Reverse Recovery Time	$I_{\rm F} = 120 {\rm A}$	$T_i = 125^{\circ}C$		170		ns
0	Deserve Deservers Channel	$V_{R} = 400V$ di/dt = 400A/µs	$T_i = 25^{\circ}C$		440		
Q <sub>rr</sub>	Reverse Recovery Charge	αι/αι +00/1/μs	$T_{j} = 125^{\circ}C$		1840		nC
R <sub>thJC</sub>	Junction to Case Thermal Resistance					0.46	°C/W

### Thermal and package characteristics

Symbol	Characteristic				Max	Unit
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000		V
T <sub>J</sub>	Operating junction temperature range			-40	150	
T <sub>JOP</sub>	Recommended junction temperature under switching conditions				T <sub>J</sub> max -25	°C
T <sub>STG</sub>	Storage Temperature Range		-40	125	C	
T <sub>C</sub>	Operating Case Temperature	-40	100			
Torque	Mounting torque	To heatsink	M6	3	5	N.m
Torque	Mounting torque For terminals M5			2	3.5	19.111
Wt	Package Weight				300	g

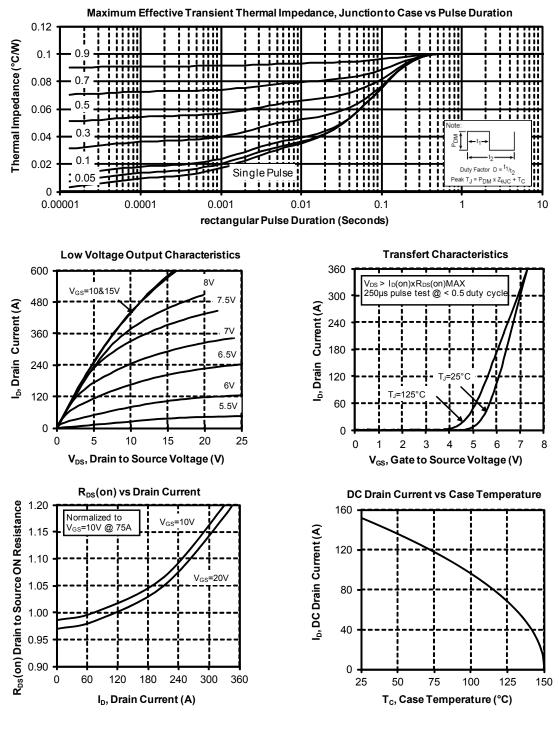
#### SP6 Package outline (dimensions in mm)



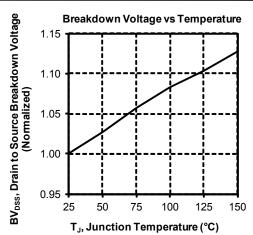
See application note APT0601 - Mounting Instructions for SP6 Power Modules on www.microsemi.com

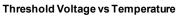


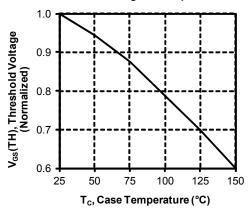
#### **Typical Performance Curve**

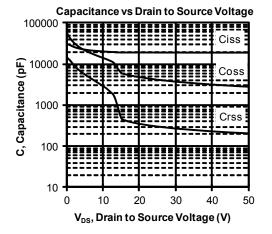


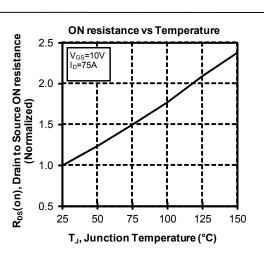




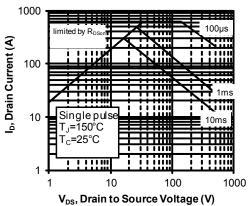


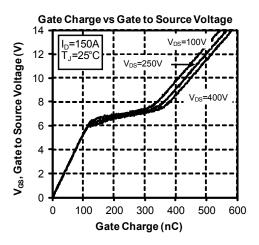






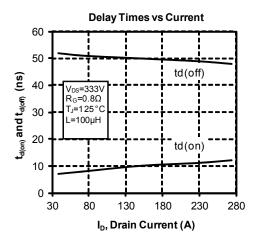
Maximum Safe Operating Area



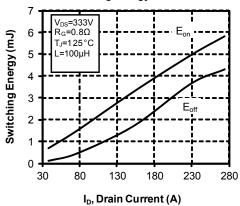


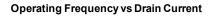
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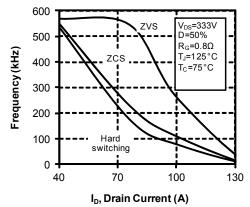


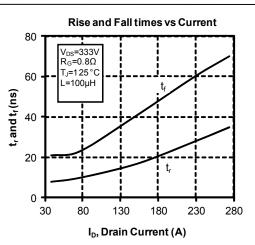


Switching Energy vs Current

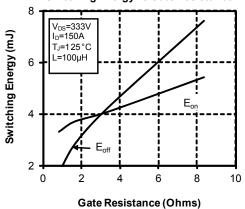








Switching Energy vs Gate Resistance



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