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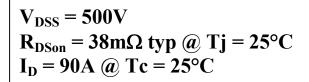


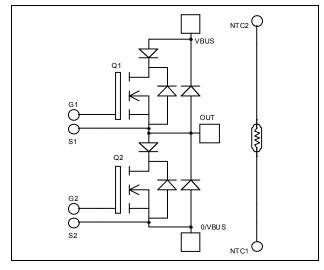






Phase leg Series & SiC parallel diodes MOSFET Power Module





0/VBUS

\$2

G2

OUT

NTC2

NTC1

Application

- Motor control
 - Switched Mode Power Supplies
- Uninterruptible Power Supplies

Features

- Power MOS 7® MOSFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Avalanche energy rated

• Parallel SiC Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Kelvin source for easy drive
- Very low stray inductance
 - Symmetrical design
 - Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

All ratings @ $T_i = 25^{\circ}C$ unless otherwise specified

Absolute maximum ratings

VBUS

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		500	V
Ţ	Continuous Drain Current	$T_c = 25^{\circ}C$	90	
I_{D}		$T_c = 80$ °C	67	A
I_{DM}	Pulsed Drain current		360	
V_{GS}	Gate - Source Voltage		±30	V
R_{DSon}	Drain - Source ON Resistance		45	mΩ
P_D	Maximum Power Dissipation	$T_c = 25^{\circ}C$	694	W
I_{AR}	Avalanche current (repetitive and non repetitive)		46	A
E _{AR}	Repetitive Avalanche Energy		50	mJ
E_{AS}	Single Pulse Avalanche Energy		2500	IIIJ

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 500V$ $T_j = 25^{\circ}C$,		200	4
		$V_{GS} = 0V, V_{DS} = 400V$ $T_j = 125^{\circ}C$	2		1000	μΑ
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 45A$		38	45	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 5mA$	3		5	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±150	nA

Dynamic Characteristics

•	Characteristic	Test Conditions	Min	Тур	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		11.2		
C_{oss}	Output Capacitance	$V_{DS} = 25V$		2.36		nF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		0.18		
Q_{g}	Total gate Charge	$V_{GS} = 10V$		246		
Q_{gs}	Gate – Source Charge	$V_{\text{Bus}} = 250V$		66		nC
Q_{gd}	Gate – Drain Charge	$I_D = 90A$		130		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @ 125°C		18		
$T_{\rm r}$	Rise Time	$V_{GS} = 15V$ $V_{Bus} = 333V$ $I_D = 90A$		35		ns
$T_{d(off)}$	Turn-off Delay Time			87		
T_{f}	Fall Time	$R_G = 2\Omega$		77		
E_{on}	Turn-on Switching Energy	Inductive switching @ 25°C		906		_
E _{off}	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 90A, R_G = 2\Omega$		1452		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C $V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 90A, R_G = 2\Omega$		1490		
E _{off}	Turn-off Switching Energy			1692	-	μJ
R_{thJC}	Junction to Case Thermal Resistance				0.18	°C/W

Series diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Vol	tage		600			V
I_{RM}	Maximum Reverse Leakage Current	$V_{R} = 600V$				250	μA
I_F	DC Forward Current		$T_c = 85^{\circ}C$		90		A
	Diode Forward Voltage	$I_F = 90A$			1.6	1.8	
$V_{\rm F}$		$I_{\rm F} = 180A$			1.9		V
		$I_F = 90A$	$T_j = 125$ °C		1.4		
t _{rr}	Reverse Recovery Time		$T_j = 25$ °C		85		ng
		$I_F = 90A$ $V_R = 400V$	$T_j = 125^{\circ}C$		160		ns
Q_{rr}	Reverse Recovery Charge	$di/dt = 600A/\mu s$	$T_j = 25^{\circ}C$		390		пC
Qrr			$T_{j} = 125^{\circ}C$		2100		iic
R_{thJC}	Junction to Case Thermal Resistance					0.45	°C/W



Parallel diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V
I_{RM}	Maximum Reverse Leakage Current	V _R =600V	$T_j = 25^{\circ}C$ $T_i = 175^{\circ}C$		250 500	1000 5000	μΑ
I_{F}	DC Forward Current		Tc = 125°C		50		A
V_{F}	Diode Forward Voltage	$I_F = 50A$	$T_i = 25^{\circ}C$ $T_j = 175^{\circ}C$		1.6	1.8	V
Qc	Total Capacitive Charge	$I_F = 50A, V_R = 300V$ di/dt = 1400A/ μ s			70		nC
	T + 1 C · ·	$f = 1MHz, V_R =$	$= 1 MHz, V_R = 200 V$		325		E
Q	Total Capacitance	$f = 1 MHz, V_R = 400 V$			250		pF
R_{thJC}	Junction to Case Thermal Resistance				0.5	°C/W	

Thermal and package characteristics

Symbol	Characteristic			Min	Max	Unit
V_{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, 50/60Hz			4000		V
T_{J}	Operating junction temperature range			-40	150	
T_{JOP}	Recommended junction temperature under switching conditions			-40	T _J max -25	°C
T_{STG}	Storage Temperature Range			-40	125	
$T_{\rm C}$	Operating Case Temperature	-40	100			
Torque	Mounting torque	To heatsink	M5	2.5	4.7	N.m
Wt	Package Weight				160	g

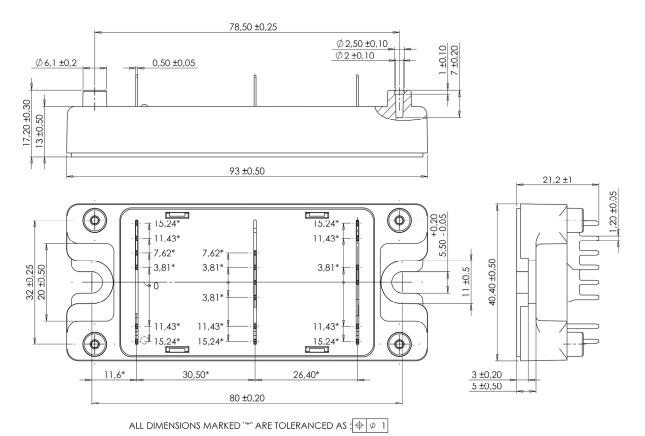
Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information).

1 chiperature sensor 1 1 e (see application note Ar 10400 on www.inicroscini.com for more information).						
Symbol	Characteristic		Min	Typ	Max	Unit
R ₂₅	Resistance @ 25°C	5°C		50		kΩ
$\Delta R_{25}/R_{25}$				5		%
$B_{25/85}$	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta B/B$		$T_C=100$ °C		4		%

$$R_{T} = \frac{R_{25}}{\exp \left[B_{25/85} \left(\frac{1}{T_{25}} - \frac{1}{T} \right) \right]} \quad \text{T: Thermistor temperature}$$

$$R_{T}: \text{ Thermistor value at T}$$

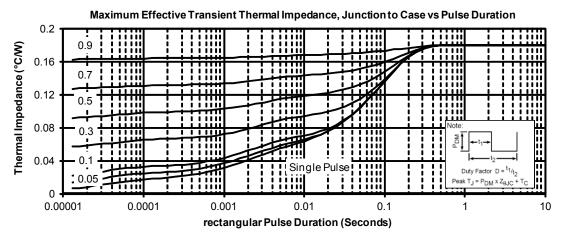
SP4 Package outline (dimensions in mm)

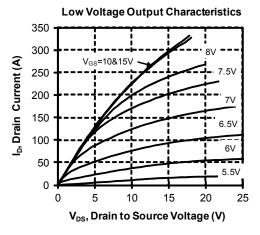


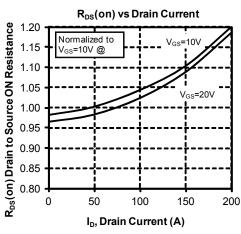
See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

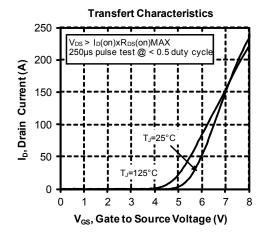


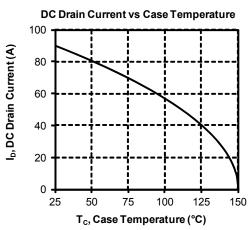
Typical MOSFET Performance Curve





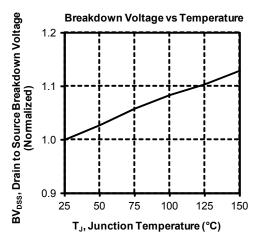


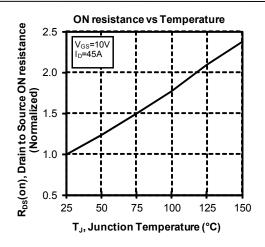


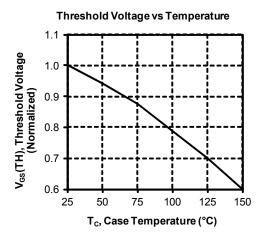


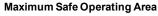
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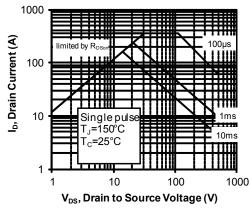


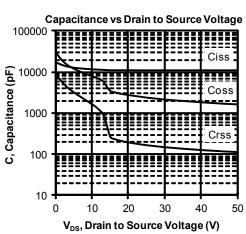


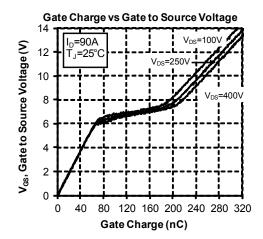




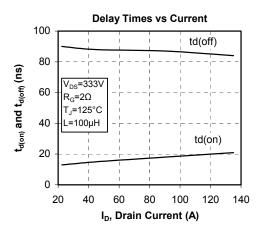


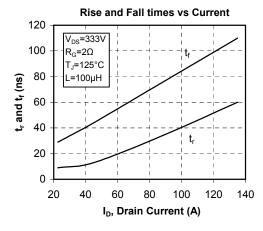


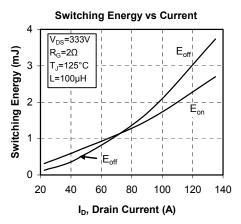


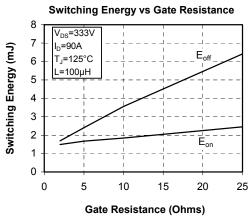


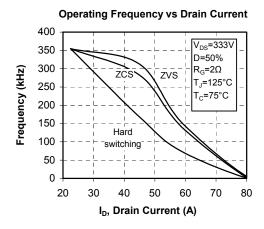


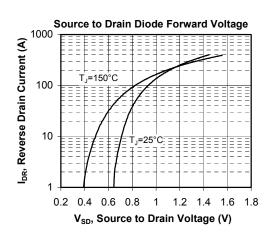






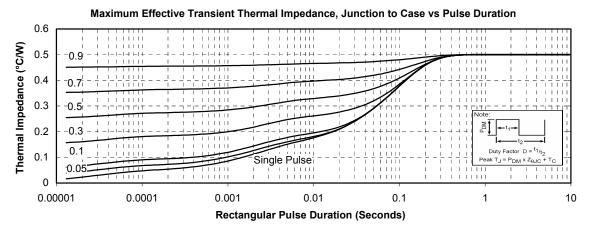


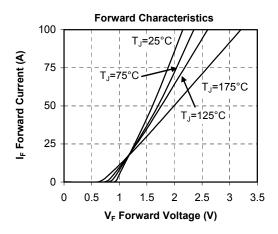


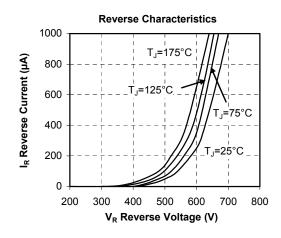


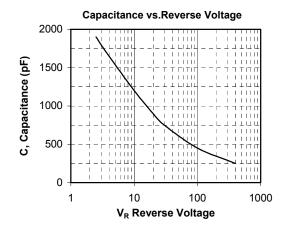


Typical SiC Diode Performance Curve









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