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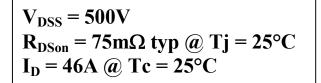


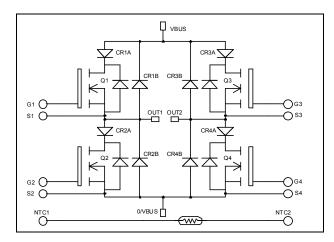






# Full bridge Series & parallel diodes MOSFET Power Module





G4 🛍

S4 🗓

OUT1

NTC2 🛭

NTC1 🛭

O/VBUS

#### Application

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

#### **Features**

- Power MOS 7<sup>®</sup> MOSFETs
  - Low R<sub>DSon</sub>
  - Low input and Miller capacitance
  - Low gate charge
  - Avalanche energy rated
  - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration

#### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS compliant

## All ratings @ $T_j = 25$ °C unless otherwise specified

#### Absolute maximum ratings

**0** G3

**8** S3

VBUS

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		500	V
т	Continuous Durin Comment	$T_c = 25^{\circ}C$	46	
$I_D$	Continuous Drain Current	$T_c = 80$ °C	34	A
$I_{DM}$	Pulsed Drain current	184		
$V_{GS}$	Gate - Source Voltage		±30	V
R <sub>DSon</sub>	Drain - Source ON Resistance		90	mΩ
$P_{D}$	Maximum Power Dissipation	$T_c = 25^{\circ}C$	357	W
$I_{AR}$	Avalanche current (repetitive and non repetitive)		46	A
$E_{AR}$	Repetitive Avalanche Energy		50	m I
$E_{AS}$	Single Pulse Avalanche Energy	Avalanche Energy		mJ

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



#### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 500V$ $T_j =$	25°C		100	
		$V_{GS} = 0V, V_{DS} = 400V$ $T_{j} =$	125°C		500	μΑ
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 23A$		75	90	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 2.5 \text{mA}$	3		5	V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

**Dynamic Characteristics** 

	Characteristic	Test Conditions	Min	Тур	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		5600		
$C_{oss}$	Output Capacitance	$V_{DS} = 25V$		1200		pF
$C_{rss}$	Reverse Transfer Capacitance	f = 1MHz		90		
$Q_{\rm g}$	Total gate Charge	$V_{GS} = 10V$		123		
$Q_{\mathrm{gs}}$	Gate – Source Charge	$V_{Bus} = 250V$		33		nC
$Q_{\mathrm{gd}}$	Gate – Drain Charge	$I_D = 46A$		65		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @ 125°C		18		
$T_{\rm r}$	Rise Time	$V_{GS} = 15V$ $V_{Bus} = 333V$ $I_D = 46A$		35		
$T_{d(off)}$	Turn-off Delay Time			87		ns
$T_{\mathrm{f}}$	Fall Time	$R_G = 5\Omega$		77		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		755		T
$\mathrm{E}_{\mathrm{off}}$	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 46A, R_G = 5\Omega$		726		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C $V_{GS} = 15V$ , $V_{Bus} = 333V$ $I_D = 46A$ , $R_G = 5\Omega$		1241		Ţ
E <sub>off</sub>	Turn-off Switching Energy			846		μJ
$R_{thJC}$	Junction to Case Thermal Resistance				0.35	°C/W

## Series diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage		600			V	
$I_{RM}$	Maximum Reverse Leakage Current	V <sub>R</sub> =600V				250	μA
$I_F$	DC Forward Current		$T_c = 70$ °C		30		A
	Diode Forward Voltage	$I_F = 30A$			1.6	1.8	
$V_{\mathrm{F}}$		$I_F = 60A$			1.9		V
		$I_F = 30A$	$T_j = 125$ °C		1.4		
	Reverse Recovery Time	$T_j = 25$ °C	$T_j = 25$ °C		85		
$t_{\mathrm{rr}}$		$I_F = 30A$ $V_R = 400V$	$T_j = 125$ °C		160		ns
Qrr	Reverse Recovery Charge	$di/dt = 200A/\mu s$	$T_j = 25$ °C		130		nC
		·	$T_{j} = 125^{\circ}C$		700		IIC
$R_{thJC}$	Junction to Case Thermal Resistance					1.2	°C/W

www.microsemi.com



## Parallel diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Vol	ltage		600			V
$I_{RM}$	Maximum Reverse Leakage Current	$V_{R} = 600V$				250	μA
$I_F$	DC Forward Current		$T_c = 70$ °C		30		Α
		$I_F = 30A$			1.6	1.8	
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 60A$			1.9		V
•		$I_F = 30A$	$T_j = 125$ °C		1.4		
	Reverse Recovery Time		$T_j = 25$ °C		85		
$t_{rr}$		$I_F = 30A$ $V_R = 400V$	$T_j = 125$ °C		160		ns
Qrr	Reverse Recovery Charge	$\frac{V_R - 400V}{\text{di/dt} = 200\text{A/}\mu\text{s}}$	$T_j = 25$ °C		130		пC
Vп			$T_{j} = 125^{\circ}C$		700		iiC
$R_{thJC}$	Junction to Case Thermal Resistance					1.2	°C/W

## Thermal and package characteristics

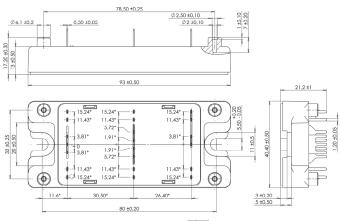
Symbol	Characteristic			Min	Max	Unit	
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000		V	
$T_{J}$	Operating junction temperature range			-40	150		
$T_{JOP}$	Recommended junction temperature under switching conditions			-40	T <sub>J</sub> max -25	°C	
$T_{STG}$	Storage Temperature Range			-40	125		
$T_{C}$	Operating Case Temperature			-40	100		
Torque	Mounting torque	To heatsink	M5	2.5	4.7	N.m	
Wt	Package Weight				160	g	

#### Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic		Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
$B_{25/85}$	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta \mathrm{B/B}$		T <sub>C</sub> =100°C		4		%

$$R_{T} = \frac{R_{25}}{\exp \left[ B_{25/85} \left( \frac{1}{T_{25}} - \frac{1}{T} \right) \right]} \quad \text{T: Thermistor temperature} \\ R_{T}: \text{ Thermistor value at T}$$

## SP4 Package outline (dimensions in mm)

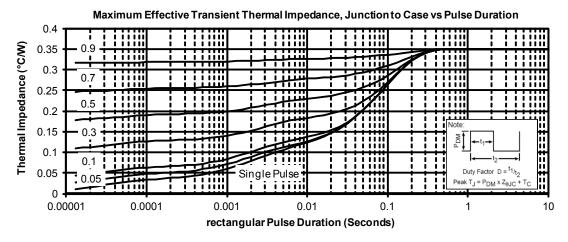


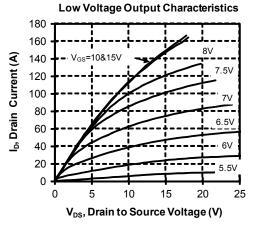
ALL DIMENSIONS MARKED "\*" ARE TOLERANCED AS :  $\boxed{\Phi} \hspace{0.1cm} \emptyset \hspace{0.1cm} 1$ 

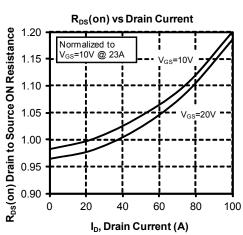
See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

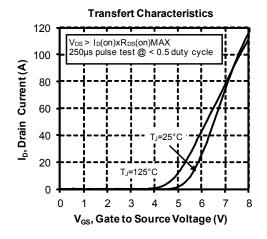


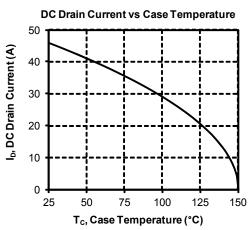
#### **Typical Performance Curve**



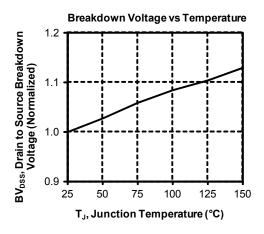


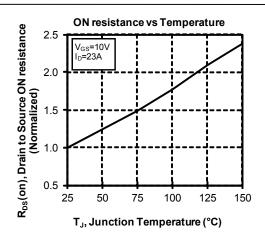


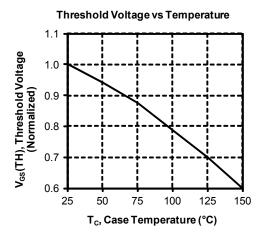


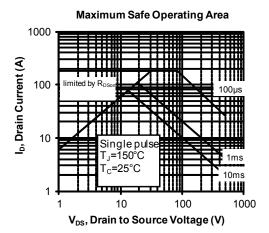


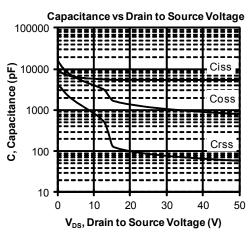


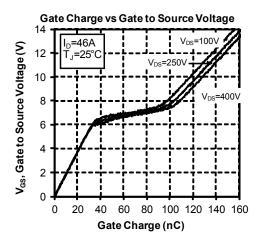




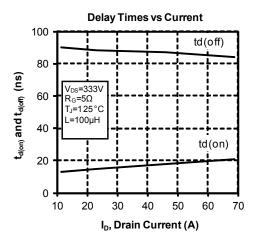


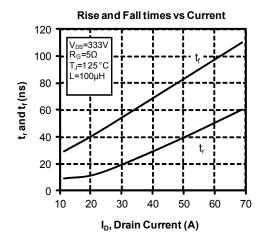


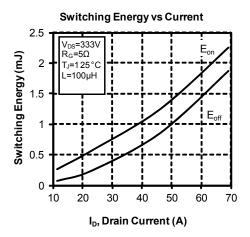


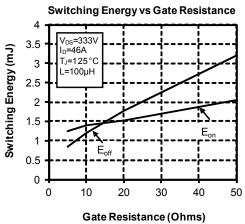


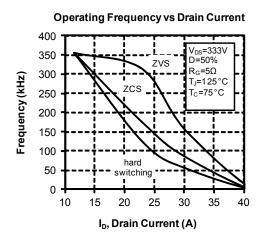


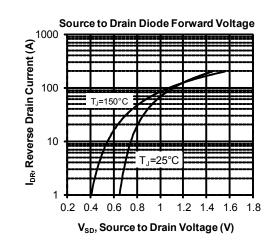












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