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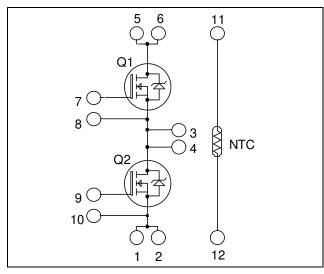


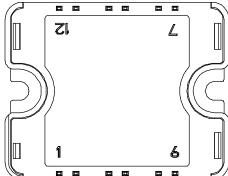




# Phase leg MOSFET Power Module

$$\begin{split} V_{DSS} &= 600 V \\ R_{DSon} &= 230 m \Omega \ typ \ @ \ Tj = 25^{\circ} C \\ I_D &= 20 A \ @ \ Tc = 25^{\circ} C \end{split}$$





Pins 1/2; 3/4; 5/6 must be shorted together

### Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

#### **Features**

- Power MOS 8<sup>TM</sup> Ultrafast FREDFETs
  - Low R<sub>DSon</sub>
  - Low input and Miller capacitance
  - Low gate charge
  - Ultrafast intrinsic reverse diode
  - Avalanche energy rated
  - Very rugged
- Very low stray inductance
  - Symmetrical design
- Internal thermistor for temperature monitoring
- High level of integration

### **Benefits**

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

### **Absolute maximum ratings**

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		600	V
т	Continuous Drain Current	$T_c = 25^{\circ}C$	20	
$I_{D}$	Continuous Drain Current	$T_c = 80^{\circ}C$	15	A
$I_{DM}$	Pulsed Drain current	rent		
$V_{GS}$	Gate - Source Voltage		±30	V
R <sub>DSon</sub>	Drain - Source ON Resistance		276	mΩ
$P_{D}$	Maximum Power Dissipation	$T_c = 25^{\circ}C$	208	W
$I_{AR}$	Avalanche current (repetitive and non repetitive)		17	A

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



## All ratings @ $T_i = 25^{\circ}C$ unless otherwise specified

### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
ī	Zero Gate Voltage Drain Current	$V_{\rm DS} = 600 \rm V$	$T_j = 25^{\circ}C$			250	^
$I_{ m DSS}$		$V_{GS} = 0V$	$T_j = 125^{\circ}C$			1000	μA
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 17A$			230	276	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 1 \text{mA}$		3	4	5	V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}$				±100	nA

**Dynamic Characteristics** 

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		5316		
$C_{oss}$	Output Capacitance	$V_{DS} = 25V$		610		pF
$C_{rss}$	Reverse Transfer Capacitance	f = 1MHz		56		
$Q_{\mathrm{g}}$	Total gate Charge	$V_{GS} = 10V$		165		
$Q_{gs}$	Gate – Source Charge	$V_{Bus} = 300V$		36		nC
$Q_{\mathrm{gd}}$	Gate – Drain Charge	$I_D = 17A$		70		
$T_{d(on)}$	Turn-on Delay Time	Resistive switching @ 25°C		37		
$T_{\rm r}$	Rise Time	$V_{GS} = 15V$ $V_{Bus} = 400V$		43		<b>m</b> .c
$T_{d(off)}$	Turn-off Delay Time	$I_{\text{Bus}} = 400 \text{ V}$ $I_{\text{D}} = 17 \text{ A}$		115		ns
$T_{\mathrm{f}}$	Fall Time	$R_G = 4.7\Omega$		34		

**Source - Drain diode ratings and characteristics** 

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$I_S$	Continuous Source current		$Tc = 25^{\circ}C$			20	A
	(Body diode)		$Tc = 80^{\circ}C$			15	Λ
$V_{\mathrm{SD}}$	Diode Forward Voltage	$V_{GS} = 0V, I_S = -17A$	<u>.</u>			1	V
dv/dt	Peak Diode Recovery					30	V/ns
t <sub>rr</sub>	Reverse Recovery Time	17.4	$T_j = 25^{\circ}C$			200	ns
·rr	Reverse Recovery Time	$I_S = -17A$ $V_R = 100V$	$T_j = 125$ °C			370	113
$Q_{rr}$	Reverse Recovery Charge	$di_{S}/dt = 100 \text{ A}/\mu\text{s}$	$T_j = 25^{\circ}C$		0.76		μC
Qrr	Reverse Recovery Charge		$T_j = 125^{\circ}C$		1.91		μС

• dv/dt numbers reflect the limitations of the circuit rather than the device itself.

 $I_S \le$  - 17A  $di/dt \le 1000 A/\mu s$   $V_{DD} \le 400 V$   $T_i \le 125 ^{\circ} C$ 



## Thermal and package characteristics

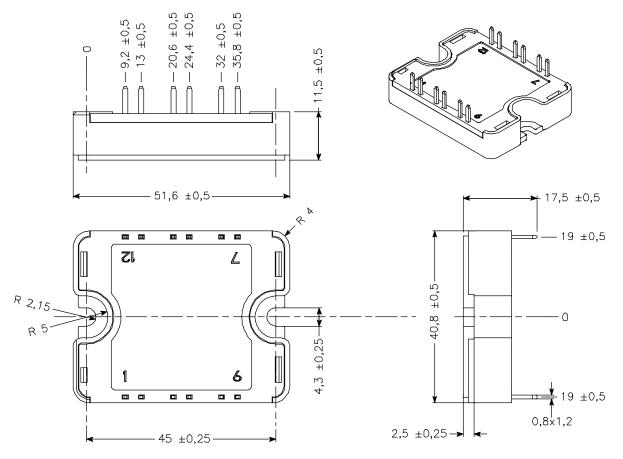
Symbol	Characteristic		Min	Typ	Max	Unit	
$R_{thJC}$	Junction to Case Thermal Resistance					0.6	°C/W
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t =1 min, I isol<1mA, 50/60Hz			2500			V
$T_{J}$	Operating junction temperature range		-40		150		
$T_{STG}$	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M4	2.5		4.7	N.m
Wt	Package Weight					80	g

### Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information).

Symbol	Characteristic	Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C		50		kΩ
B 25/85	$T_{25} = 298.15 \text{ K}$		3952		K

$$R_T = \frac{R_{25}}{\exp \left[ B_{25/85} \left( \frac{1}{T_{25}} - \frac{1}{T} \right) \right]} \quad \text{T: Thermistor temperature}$$
 
$$R_T: \text{ Thermistor value at T}$$

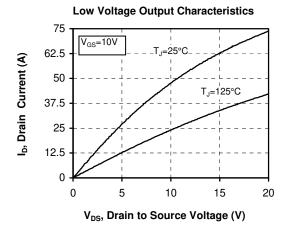
## SP1 Package outline (dimensions in mm)

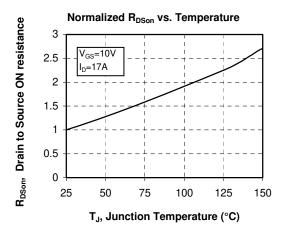


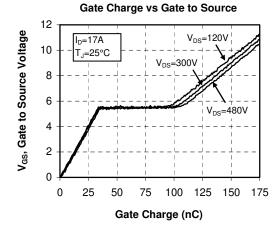
See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

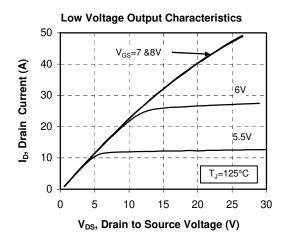


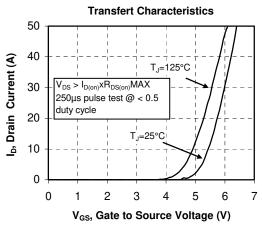
## **Typical Performance Curve**

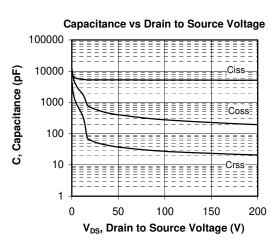






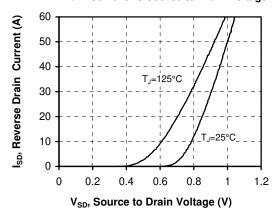




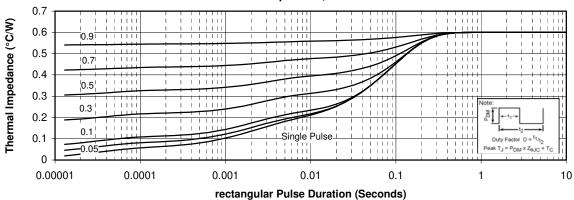




### **Drain Current vs Source to Drain Voltage**



### Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration



### Microsemi reserves the right to change, without notice, the specifications and information contained herein

Microsemi's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.