

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



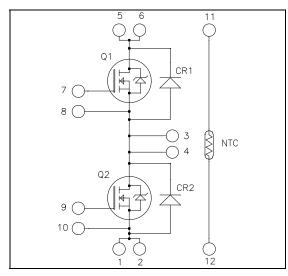


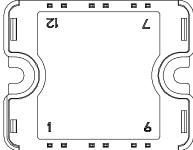




## Phase leg SiC MOSFET Power Module

$$\begin{split} V_{DSS} &= 1200 V \\ R_{DSon} &= 49 m \Omega \text{ max } @ \text{Tj} = 25 ^{\circ} \text{C} \\ I_D &= 55 \text{A} @ \text{Tc} = 25 ^{\circ} \text{C} \end{split}$$





Pins 1/2; 3/4; 5/6 must be shorted together

## **Application**

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

#### **Features**

#### • SiC Power MOSFET

- Low R<sub>DS(on)</sub>
- High temperature performance

### • SiC Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Very low stray inductance
- Internal thermistor for temperature monitoring
- High level of integration
- AlN substrate for improved thermal performance

#### **Benefits**

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

## All ratings @ $T_i = 25$ °C unless otherwise specified

## 1. SiC MOSFET characteristics (Per MOSFET)

## Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		1200	V
Ţ	Continuous Drain Current	$T_c = 25^{\circ}C$	55	
$I_D$	Continuous Drain Current	$T_c = 80$ °C	42	A
$I_{DM}$	Pulsed Drain current		110	
$V_{GS}$	Gate - Source Voltage		-10/+25	V
$R_{DSon}$	Drain - Source ON Resistance		49	mΩ
$P_{D}$	Maximum Power Dissipation	$T_c = 25^{\circ}C$	250	W

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



## **Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V$ ; $V_{DS} = 1200V$			25	200	μA
D	Dunin Common on Braintana	$V_{GS} = 20V$ $T_i = 25^{\circ}C$		40	49		
$R_{DS(on)}$	Drain – Source on Resistance	$I_D = 40A$	$T_{\rm j} = 150^{\circ}{\rm C}$		75	104	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 2mA$		1.7	2.2		V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$				500	nA

**Dynamic Characteristics** 

•	Characteristic	Test Conditions		Min	Тур	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$			1900		
$C_{oss}$	Output Capacitance	$V_{DS} = 1000V$	$V_{DS} = 1000V$ f = 1MHz		160		pF
$C_{rss}$	Reverse Transfer Capacitance	f = 1MHz			13		
$Q_{\mathrm{g}}$	Total gate Charge	$V_{GS}=20V$	$V_{co} = 20V$		98		
$Q_{gs}$	Gate – Source Charge	$V_{Bus} = 800V$			22		nC
$Q_{\mathrm{gd}}$	Gate – Drain Charge	$I_D = 40A$	$I_D = 40A$		36		
$T_{d(on)}$	Turn-on Delay Time	$V_{GS} = -5/+20V$	V = 5/120V		12		
$T_{\rm r}$	Rise Time	$V_{\text{GS}} = 37/120 \text{ V}$ $V_{\text{Bus}} = 800 \text{ V}$			14		
$T_{d(off)}$	Turn-off Delay Time	$I_D = 40A$			23		ns
$T_{\mathrm{f}}$	Fall Time	$R_{\rm L} = 20\Omega \; ; \; R_{\rm G} = 25$			18		
Eon	Turn on Energy	$V_{Bus} = 600V$ $I_{Bus} = 40A$	$T_j = 150$ °C		0.9		mJ
$E_{\rm off}$	Turn off Energy		$T_j = 150$ °C		0.5		mJ
$R_{\text{thJC}}$	Junction to Case Thermal Resistan	ce				0.5	°C/W

## 2. SiC diode characteristics (Per SiC diode)

## SiC diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage			1200			V
$I_{RM}$	Maximum Reverse Leakage Current	$V_{p}=1200V$	$T_j = 25$ °C		64	400	μA
1RM	Waximum Reverse Leakage Current		$T_j = 175$ °C		112	2000	μΑ
$I_F$	DC Forward Current		Tc = 125°C		20		A
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 20A$ $T_j = 25$	$T_i = 25^{\circ}C$		1.6	1.8	V
<b>v</b> <sub>F</sub>	Diode Forward Voltage	$I_{\rm F} = 20$ A	$T_{i} = 175^{\circ}C$		2.3	3	V
Qc	Total Capacitive Charge	$I_F = 20A, V_R = 120V$ di/dt = 1000A/ $\mu$ s			160		nC
C	Total Campaitanas	$f = 1 MHz, V_R = 200V$			192		ъE
	Total Capacitance	$f = 1 MHz, V_R =$	$MHz$ , $V_R = 400V$		138		pF
$R_{\text{thJC}}$	Junction to Case Thermal Resistance				0.8	°C/W	

www.microsemi.com



## 3. Thermal and package characteristics

## Package characteristics

Symbol	Characteristic			Min	Тур	Max	Unit	
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz				4000			V
т	On anoting investigation towns arother range		SiC MO	SFET	-40		150	
$T_{J}$	Operating junction temperature range		SiC diod	de	-40		175	
$T_{JOP}$	Recommended junction temperature under switching conditions				-40		T <sub>J</sub> max	°C
1 JOP	ecommended junction temperature under switching conditions				-40		-25	
$T_{STG}$	Storage Temperature Range				-40		125	
$T_{\rm C}$	Operating Case Temperature				-40		125	
Torque	Mounting torque	To he	atsink	M4	2	•	3	N.m
Wt	Package Weight					80	g	

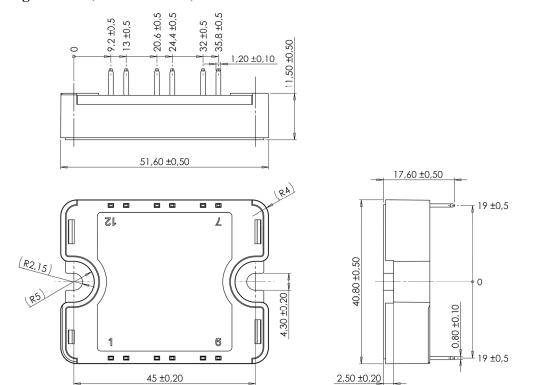
## Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic		Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
$B_{25/85}$	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta B/B$		T <sub>C</sub> =100°C		4		%

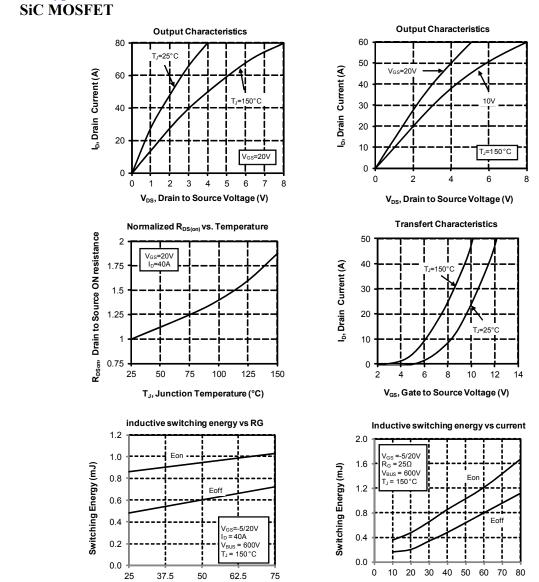
$$R_{T} = \frac{R_{25}}{\exp \left[ B_{25/85} \left( \frac{1}{T_{25}} - \frac{1}{T} \right) \right]} \quad \text{T: Thermistor temperature}$$

$$R_{T}: \text{ Thermistor value at T}$$

## SP1 Package outline (dimensions in mm)



See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com



Gate Resistance (Ohms)

0.0001

0.6

0.5

0.4

0.3

0.1

0.00001

Thermal Impedance (°C/W)

APTMC120AM55CT1AG

Drain current (A)

1111111

i i i i iii

*Microsemi.* 

4. Typical Performance Curves

POWER PRODUCTS GROUP

# APTMC120AM55CT1AG-Rev 1 June, 2013

4 - 6

Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration

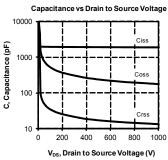
<del>+++++</del>

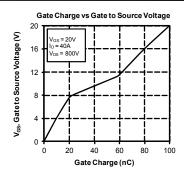
0.001 0.01 0.1

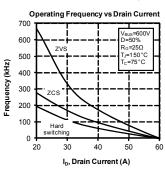
rectangular Pulse Duration (Seconds)

Single Pulse

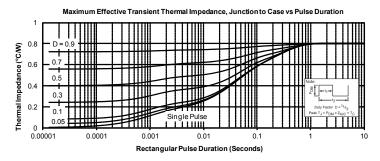


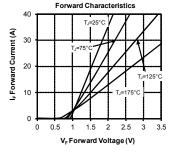


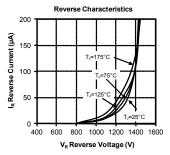


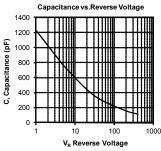


## SiC diode









5 - 6



### **DISCLAIMER**

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with lifesupport or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at http://www.microsemi.com/legal/tnc.asp

## <u>Life Support Application</u>

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.