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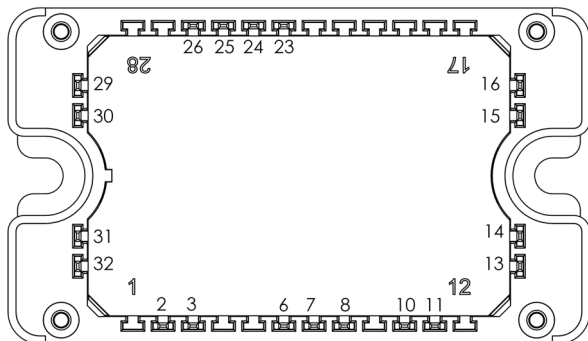
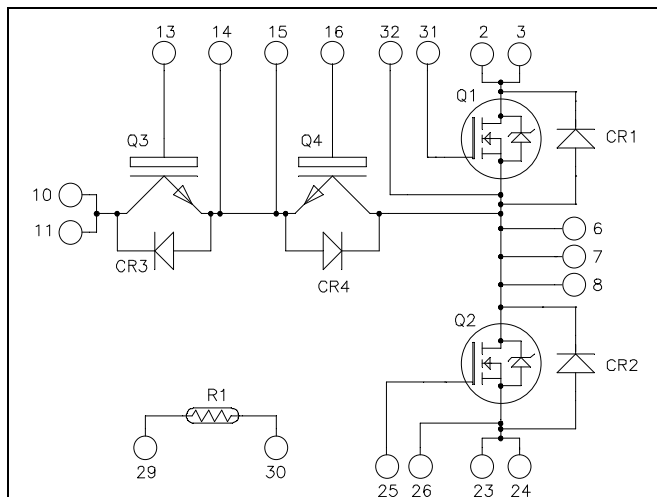
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## Phase Leg & Dual Common Emitter Power Module



All multiple inputs and outputs must be shorted together  
10/11 ; 23/24 ; 2/3 ; ...

### SiC MOSFET (Q1, Q2):

$V_{CES} = 1200V$  ;  $R_{DS(on)} = 98m\Omega$  max @  $T_j = 25^\circ C$

### Trench & Field Stop IGBT3 (Q3, Q4):

$V_{CES} = 600V$  ;  $I_C = 20A$  @  $T_c = 100^\circ C$

### Application

- Solar converter
- Uninterruptible Power Supplies


### Features

- **Q1, Q2 SiC Power MOSFET**
  - Low  $R_{DS(on)}$
  - High temperature performance
- **Q3, Q4 Trench + field Stop IGBT3**
  - Low voltage drop
  - Low tail current
  - Switching frequency up to 20 kHz
- **SiC Schottky Diode (CR1 to CR4)**
  - Zero reverse recovery
  - Zero forward recovery
  - Temperature Independent switching behavior
  - Positive temperature coefficient on VF
- Kelvin emitter for easy drive
- Very low stray inductance
- AlN substrate for improved thermal performance
- Internal thermistor for temperature monitoring

### Benefits

- Stable temperature behavior
- Very rugged
- Solderable terminals for easy PCB mounting
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS Compliant

All ratings @  $T_j = 25^\circ C$  unless otherwise specified

 **CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.  
See application note APT0502 on [www.microsemi.com](http://www.microsemi.com)

## 1. SiC MOSFET characteristics (Per MOSFET)

### Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
$V_{DS}$	Drain - Source Voltage	1200	V
$I_D$	Continuous Drain Current	$T_c = 25^\circ\text{C}$	A
		$T_c = 80^\circ\text{C}$	
$I_{DM}$	Pulsed Drain current	55	
$V_{GS}$	Gate - Source Voltage	-10/+25	V
$R_{DS(on)}$	Drain - Source ON Resistance	98	m $\Omega$
$P_D$	Power Dissipation	$T_c = 25^\circ\text{C}$	W

### Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 1200V$			100	$\mu\text{A}$
$R_{DS(on)}$	Drain - Source on Resistance	$V_{GS} = 20V$ $I_D = 20A$		80	98	m $\Omega$
		$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$		153		
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 5mA$	2.4	3		V
$I_{GSS}$	Gate - Source Leakage Current	$V_{GS} = 20V, V_{DS} = 0V$			250	nA

### Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 1000V$ $f = 1MHz$		950		pF
$C_{oss}$	Output Capacitance			80		
$C_{rss}$	Reverse Transfer Capacitance			7.6		
$Q_g$	Total gate Charge	$V_{GE} = 20V$ $V_{Bus} = 800V$ $I_D = 20A$		62		nC
$Q_{gs}$	Gate - Source Charge			15		
$Q_{gd}$	Gate - Drain Charge			23		
$T_{d(on)}$	Turn-on Delay Time	$V_{GS} = -2/+20V$ $V_{Bus} = 800V$ $I_D = 20A$ $R_L = 40\Omega; R_G = 50\Omega$		12		ns
$T_r$	Rise Time			14		
$T_{d(off)}$	Turn-off Delay Time			23		
$T_f$	Fall Time			18		
$E_{on}$	Turn on Energy	Inductive Switching $V_{GS} = -5/+20V$ $V_{Bus} = 600V$ $I_D = 20A$ $R_G = 50\Omega$	$T_j = 150^\circ\text{C}$	0.45		mJ
$E_{off}$	Turn off Energy			0.25		
$R_{thJC}$	Junction to Case Thermal Resistance				1	$^\circ\text{C/W}$



**SiC diode ratings and characteristics (CR1 & CR2) (per diode)**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$V_{RRM}$	Peak Repetitive Reverse Voltage				1200	V
$I_{RM}$	Reverse Leakage Current	$V_R = 1200V$		10	200	$\mu A$
		$T_j = 25^\circ C$				
		$T_j = 175^\circ C$		500		
$I_F$	DC Forward Current			10		A
$V_F$	Diode Forward Voltage	$I_F = 10A$		1.5	1.8	V
		$T_j = 25^\circ C$				
		$T_j = 175^\circ C$		2.3		
$Q_C$	Total Capacitive Charge	$I_F = 10A, V_R = 600V$ $di/dt = 500A/\mu s$		120		nC
$C$	Total Capacitance	$f = 1MHz, V_R = 200V$		115		pF
		$f = 1MHz, V_R = 400V$		85		
$R_{thJC}$	Junction to Case Thermal Resistance				1.1	$^\circ C/W$

**2. Trench & Field Stop IGBT3 (per IGBT)**
**Absolute maximum ratings**

Symbol	Parameter	Max ratings	Unit
$V_{CES}$	Collector - Emitter Voltage	600	V
$I_C$	Continuous Collector Current	$T_C = 25^\circ C$ $T_C = 100^\circ C$	37 20
$I_{CM}$	Pulsed Collector Current	$T_C = 25^\circ C$	40
$V_{GE}$	Gate – Emitter Voltage	$\pm 20$	V
$P_D$	Power Dissipation	$T_C = 25^\circ C$	78
RBSOA	Reverse Bias Safe Operating Area	$T_J = 150^\circ C$	40A @ 550V

**Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$I_{CES}$	Zero Gate Voltage Collector Current	$V_{GE} = 0V, V_{CE} = 600V$			250	$\mu A$
$V_{CE(sat)}$	Collector Emitter Saturation Voltage	$V_{GE} = 15V$ $I_C = 20A$		1.5	1.9	V
		$T_j = 25^\circ C$ $T_j = 150^\circ C$		1.7		
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 300\mu A$	5.0	5.8	6.5	V
$I_{GES}$	Gate – Emitter Leakage Current	$V_{GE} = 20V, V_{CE} = 0V$			300	nA

**Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C <sub>ies</sub>	Input Capacitance	V <sub>GE</sub> = 0V V <sub>CE</sub> = 25V f = 1MHz		1100		pF
C <sub>oes</sub>	Output Capacitance			70		
C <sub>res</sub>	Reverse Transfer Capacitance			35		
Q <sub>G</sub>	Gate charge	V <sub>GE</sub> = ±15V, I <sub>C</sub> = 20A V <sub>CE</sub> = 300V		200		nC
T <sub>d(on)</sub>	Turn-on Delay Time	Inductive Switching (25°C) V <sub>GE</sub> = ±15V V <sub>Bus</sub> = 300V I <sub>C</sub> = 20A R <sub>G</sub> = 12Ω		110		ns
T <sub>r</sub>	Rise Time			45		
T <sub>d(off)</sub>	Turn-off Delay Time			200		
T <sub>f</sub>	Fall Time			40		
T <sub>d(on)</sub>	Turn-on Delay Time	Inductive Switching (150°C) V <sub>GE</sub> = ±15V V <sub>Bus</sub> = 300V I <sub>C</sub> = 20A R <sub>G</sub> = 12Ω		120		ns
T <sub>r</sub>	Rise Time			50		
T <sub>d(off)</sub>	Turn-off Delay Time			250		
T <sub>f</sub>	Fall Time			60		
E <sub>on</sub>	Turn-on Switching Energy	V <sub>GE</sub> = ±15V V <sub>Bus</sub> = 300V I <sub>C</sub> = 20A R <sub>G</sub> = 12Ω	T <sub>j</sub> = 25°C	0.11		mJ
			T <sub>j</sub> = 150°C	0.2		
E <sub>off</sub>	Turn-off Switching Energy		T <sub>j</sub> = 25°C	0.5		mJ
			T <sub>j</sub> = 150°C	0.7		
I <sub>sc</sub>	Short Circuit data	V <sub>GE</sub> ≤ 15V ; V <sub>Bus</sub> = 360V t <sub>p</sub> ≤ 10μs ; T <sub>j</sub> = 150°C		100		A
R <sub>thJC</sub>	Junction to Case Thermal Resistance				1.92	°C/W

**3. SiC diode ratings and characteristics (CR3 & CR4) (per diode)**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage				600	V
I <sub>RM</sub>	Reverse Leakage Current	V <sub>R</sub> = 600V	T <sub>j</sub> = 25°C	10	60	μA
			T <sub>j</sub> = 175°C	20	300	
I <sub>F</sub>	DC Forward Current		T <sub>c</sub> = 100°C	10		A
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 10A	T <sub>j</sub> = 25°C	1.6	1.8	V
			T <sub>j</sub> = 175°C	2	2.4	
Q <sub>C</sub>	Total Capacitive Charge	I <sub>F</sub> = 10A, V <sub>R</sub> = 600V di/dt = 500A/μs		28		nC
C	Total Capacitance	f = 1MHz, V <sub>R</sub> = 200V		65		pF
		f = 1MHz, V <sub>R</sub> = 400V		50		
R <sub>thJC</sub>	Junction to Case Thermal Resistance				2.2	°C/W

#### 4. Temperature sensor NTC

Symbol	Characteristic	Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C		22		kΩ
ΔR <sub>25</sub> /R <sub>25</sub>	Resistance tolerance			5	%
ΔB/B	Beta tolerance			3	
B <sub>25/100</sub>	T <sub>25</sub> = 298.16 K		3980		K

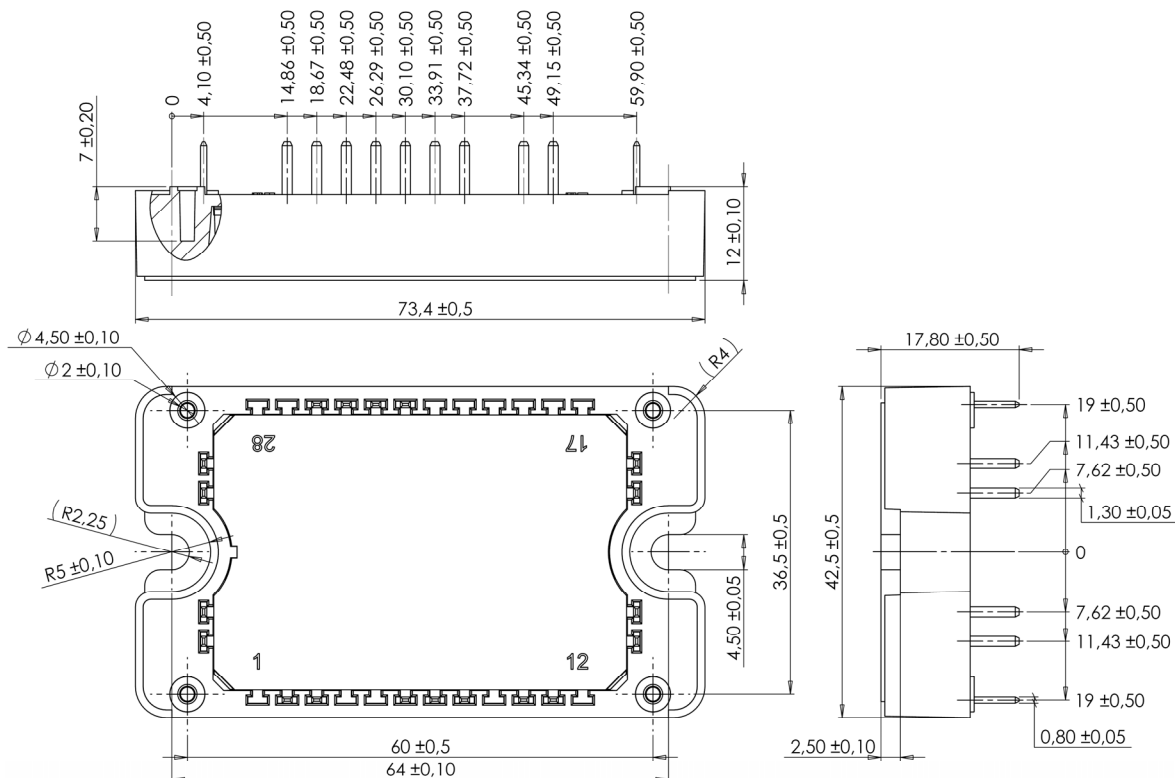
$$R_T = \frac{R_{25}}{\exp \left[ B_{25/100} \left( \frac{1}{T_{25}} - \frac{1}{T} \right) \right]}$$

T: Thermistor temperature  
R<sub>T</sub>: Thermistor value at T

#### 5. Thermal and package characteristics

Symbol	Characteristic	Min	Max	Unit
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t=1 min, 50/60Hz	4000		V
T <sub>J</sub>	Operating junction temperature range			°C
	SiC MOSFET	-40	150	
	SiC diodes + IGBT	-40	175	
T <sub>JOP</sub>	Recommended junction temperature under switching conditions	-40	T <sub>Jmax</sub> -25	
T <sub>STG</sub>	Storage Temperature Range	-40	125	
T <sub>C</sub>	Operating Case Temperature	-40	125	N.m
Torque	Mounting torque	To heatsink	M4	
Wt	Package Weight		110	g

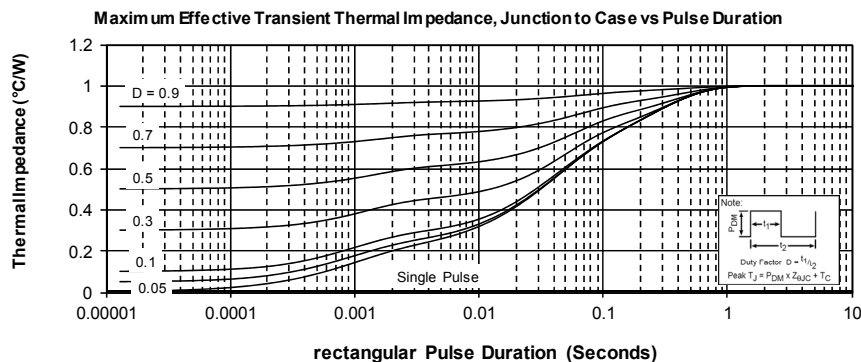
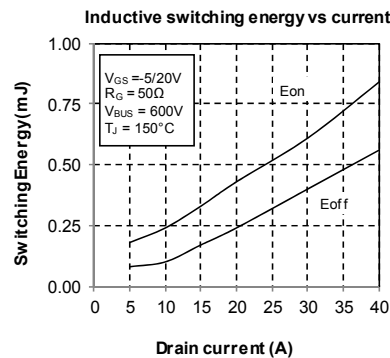
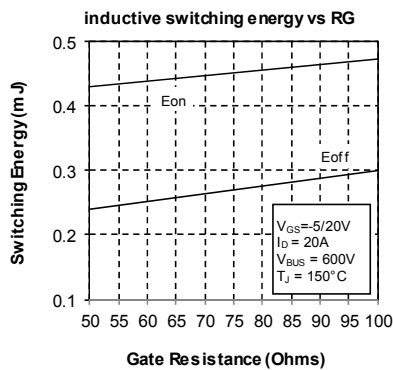
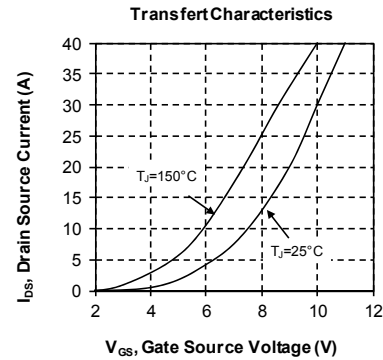
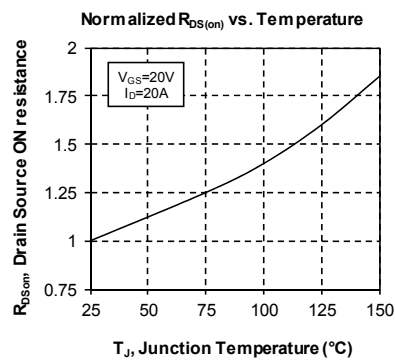
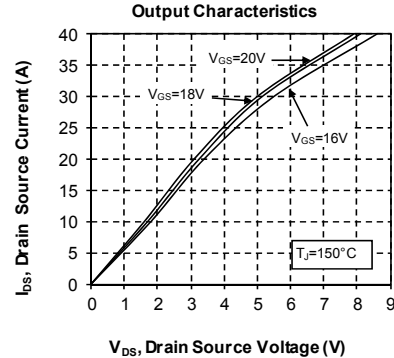
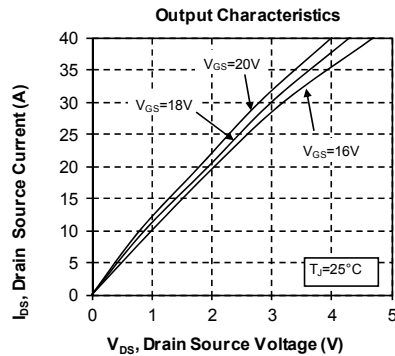
#### Package outline (dimensions in mm)



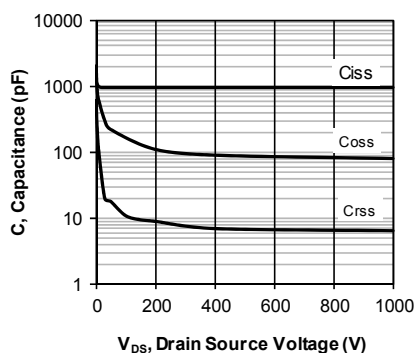
See application note 1906 - Mounting Instructions for SP3F Power Modules on [www.microsemi.com](http://www.microsemi.com)

## 6. Typical performance curve

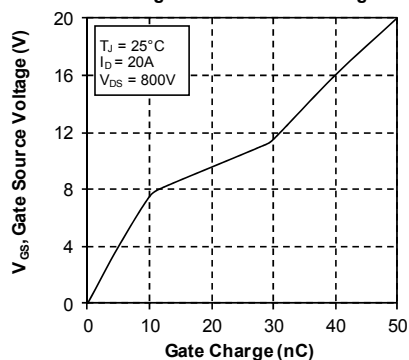
### Q1, Q2 SiC MOSFET



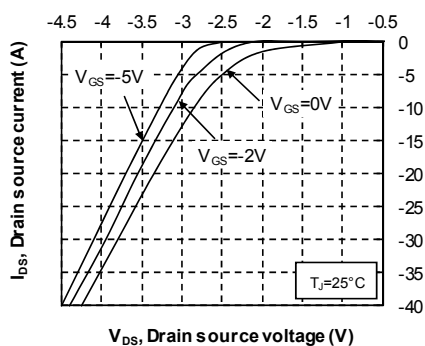
Capacitance vs Drain Source Voltage



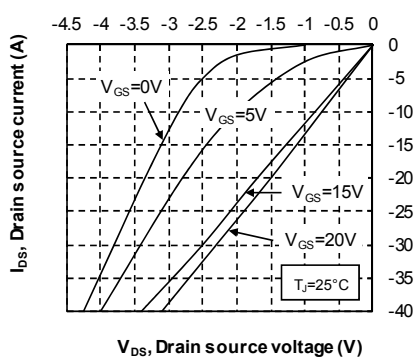
Gate Charge vs Gate Source Voltage



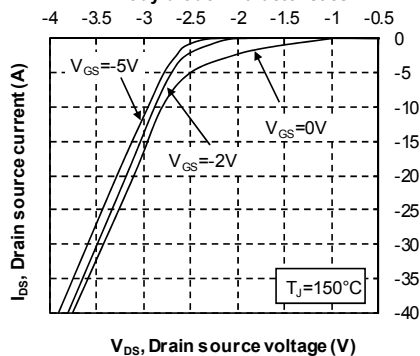
Body diode Characteristics



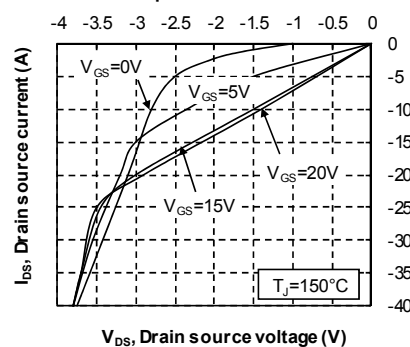
3rd quadrant Characteristics



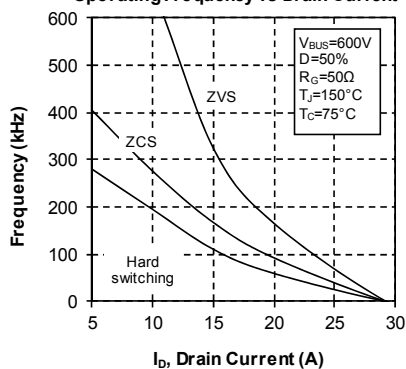
Body diode Characteristics



3rd quadrant Characteristics

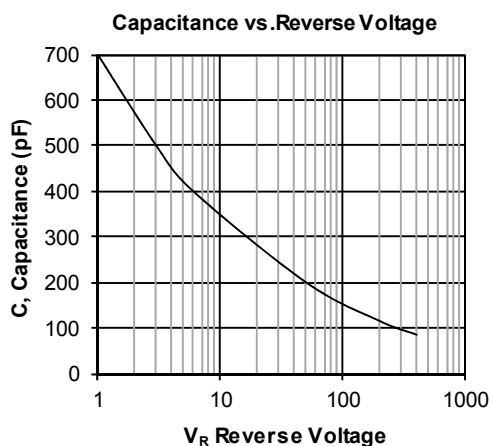
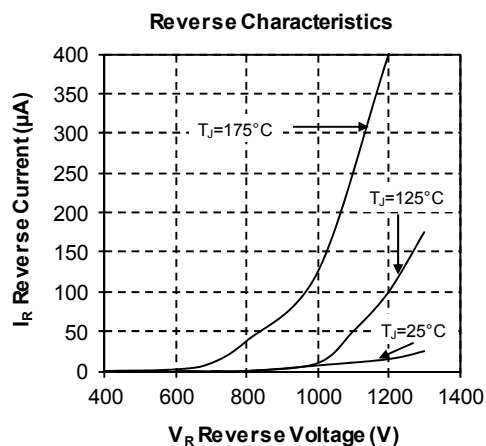
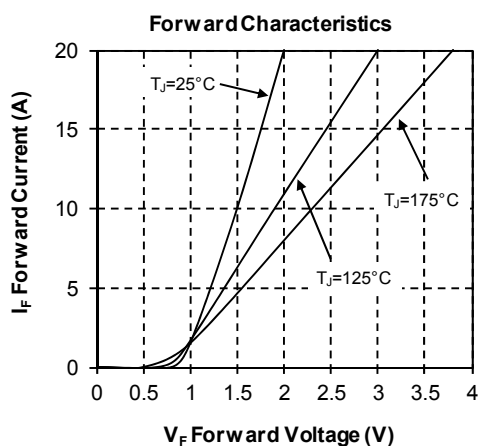
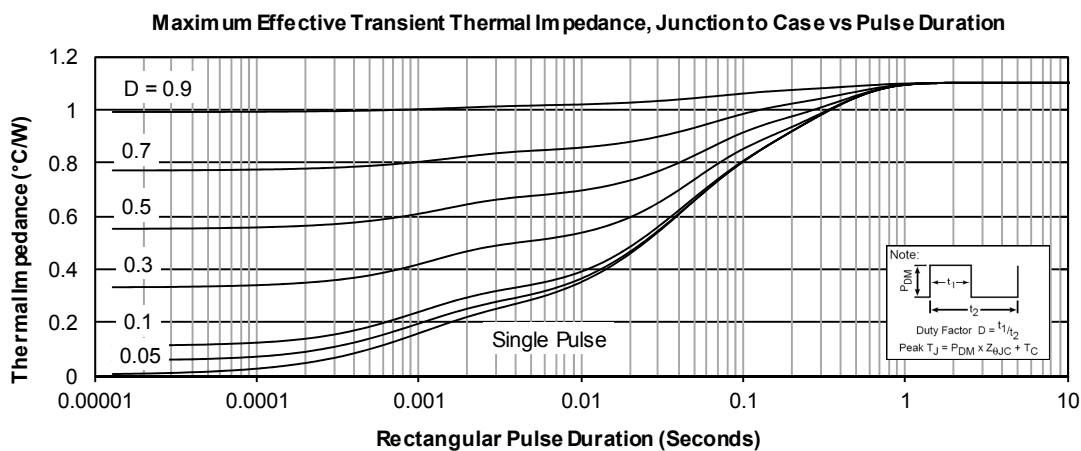


Operating Frequency vs Drain Current

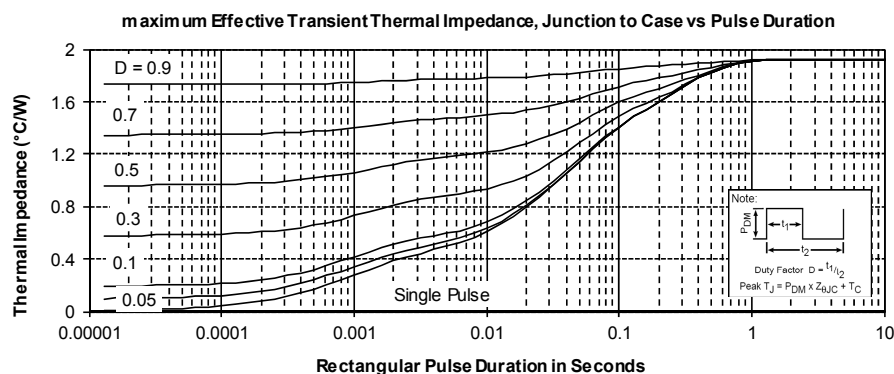
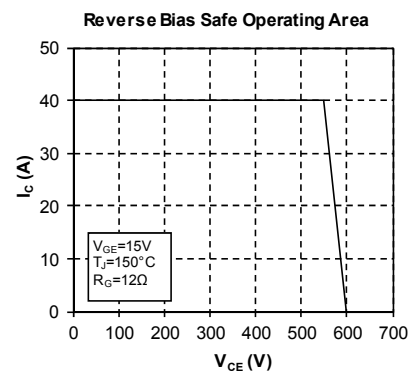
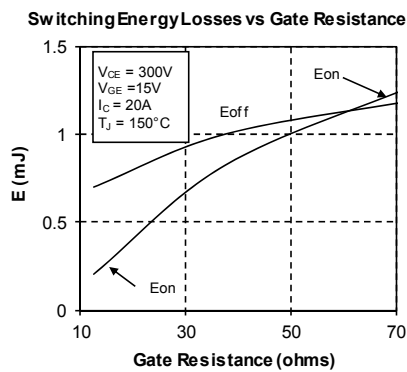
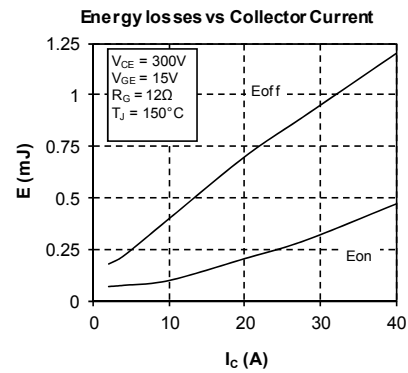
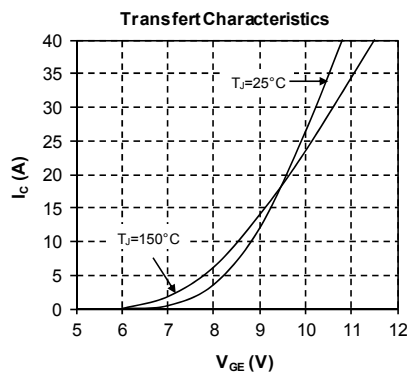
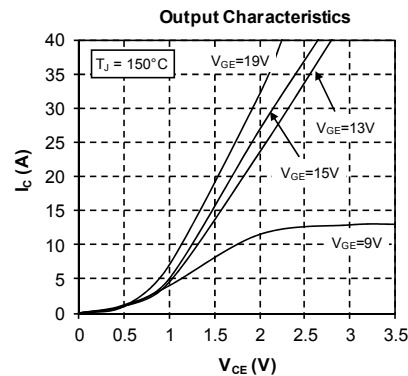
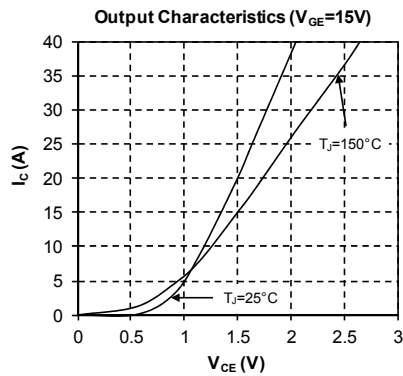




## CR1 & CR2 SiC diode characteristics

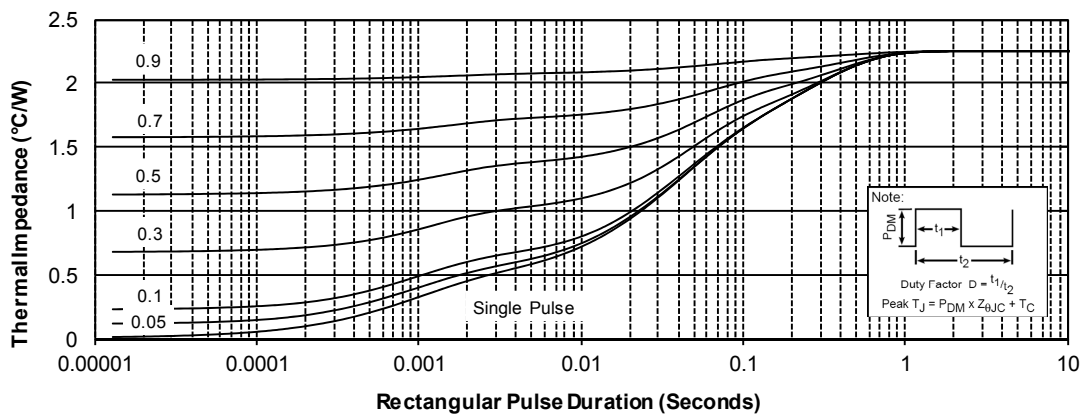


## Q3, Q4 Trench + field stop IGBT3

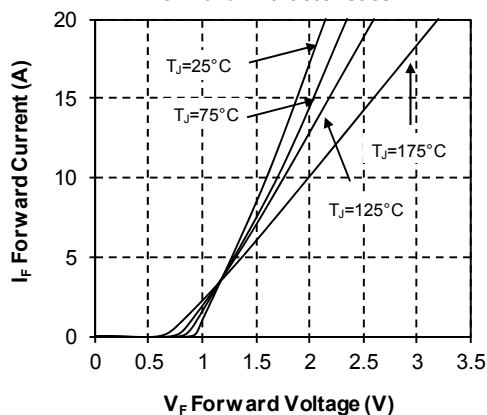


## CR3 & CR4 SiC diode characteristics

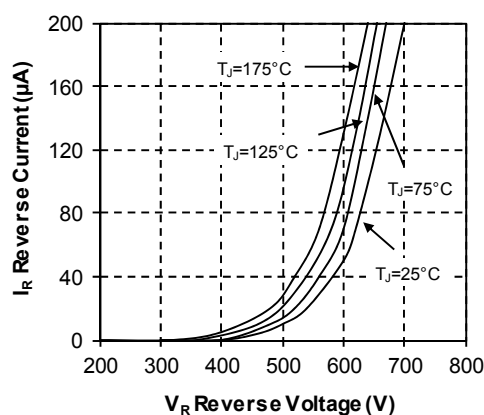
Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration



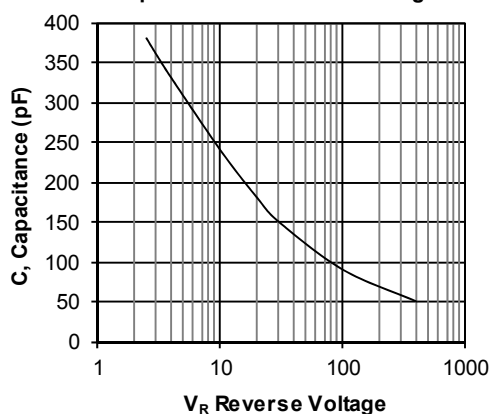
Forward Characteristics



Reverse Characteristics



Capacitance vs. Reverse Voltage



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