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Description

The AR30N60 is a high-performance Active/Synchronous Rectifier used for secondary-side synchronous rectification with the output voltage at less than 10V.

By integrating MOSFET and controller, the AR30N60 emulates the characteristics of a near-ideal diode rectifier. The combination not only reduces the energy consumption of the secondary-side synchronous rectifier, it also improves the power dissipation of the sub-system on the primary side.

The SYNC feature allows the embedded MOSFET of the AR30N60 to immediately turn OFF as soon as the primary-side switch turns ON, preventing the unfavorable reverse condition of the synchronous rectifier. The AR30N60 is ideally suited for AC/DC power converters implemented in either the flyback or the LLC-resonant topology. For the ease of use, the design of AR30N60 is flexible enough to facilitate operation in one of the following modes: Discontinuous Conduction (DCM), Continuous Conduction (CCM), Quasi Resonant (QR).

To further improve conversion efficiency and to ensure system integrity, AR30N60 integrates many innovative features. These include: light-load detection to reduce standby power consumption, TON and TOFF/EN pins to prevent run-away ON-time due to the open-circuit or short-circuit fault conditions, SFB pin to provide over-temperature protection, low $R_{DS(ON)}$ of the embedded MOSFET, and wide output voltage (V_{CC}) range.

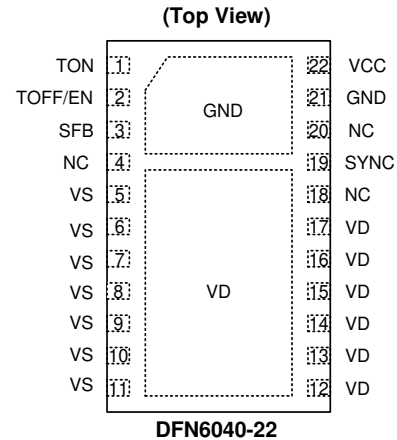
The small footprint of the DFN6040-22 package makes it ideally suited for space constrained applications.

Features

- Secondary-side synchronous rectification
- Low turn-OFF threshold at -4mV
- -40°C to +150°C temperature range
- Supports both DCM, CCM, and QR (Quasi-Resonant)
- Built-in over-temperature protection (OTP) and Undervoltage Lock-Out (UVLO) to ensure system reliability
- Light-load detection to reduce the standby power consumption
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Pin Assignments



Applications

- AC/DC Battery Charger
- USB Type-C Charger
- Flyback Conversion
- PC Power Supply
- Switched-Mode Power Supply (SMPS)

Typical Application Circuit

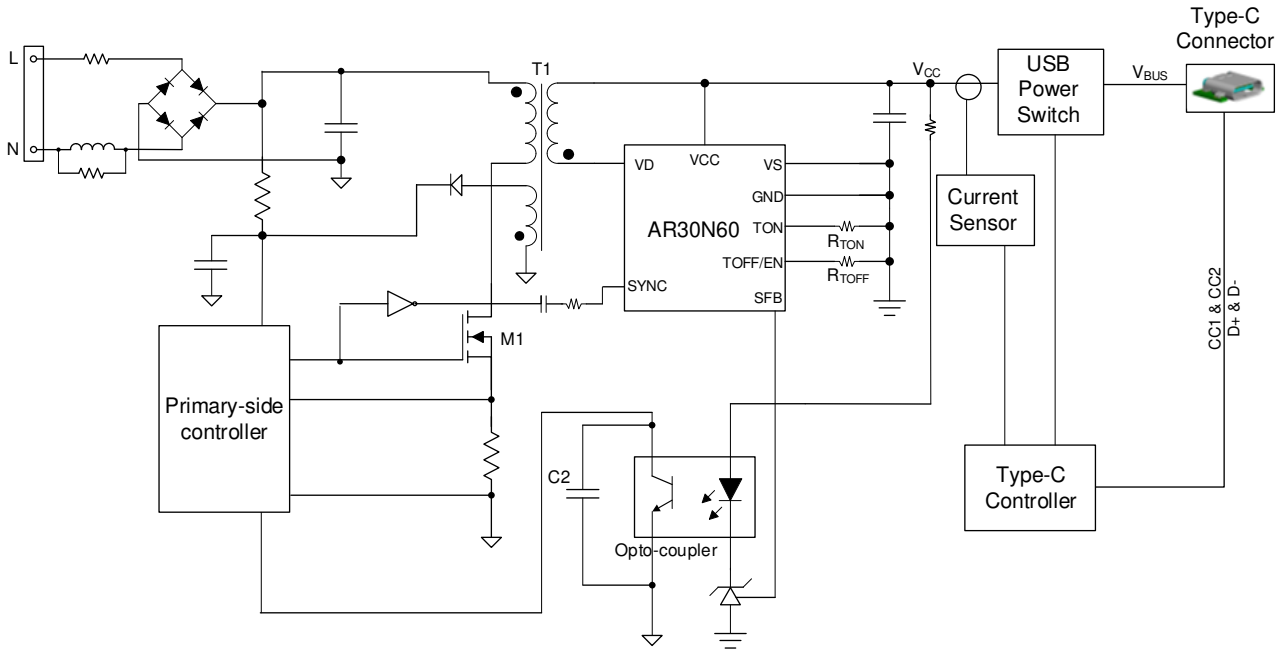


Figure 1 USB Type-C Charger with Secondary-side Regulation (SSR)

Pin Descriptions

Pin Number	Pin Name	Function
1	TON	Minimum ON-time Control Pin.
2	TOFF/EN	Minimum OFF-time / Enable Control Pin. It combines the functions of setting the Minimum OFF-time as well as chip enable.
3	SFB	Secondary-side Feedback Output Pin. It is connected to the internal resistor network for feedback purpose.
4,18,20	NC	No connection.
5-11	VS	Embedded MOSFET Source Terminal. It shall be connected to GND.
12-17, VD pad	VD	Embedded MOSFET Drain Terminal. It shall be connected as close as possible to the transformer.
19	SYNC	Synchronization Input Pin. This must be connected, through a RC filter, to either the output of the gate driver to the embedded MOSFET of the primary-side, or to the drain terminal of the MOSFET of the primary-side.
21,GND pad	GND	Ground Pin.
22	VCC	VCC Pin. A ceramic capacitor of 10 μ F shall be connected between this pin and GND.

Functional Block Diagram

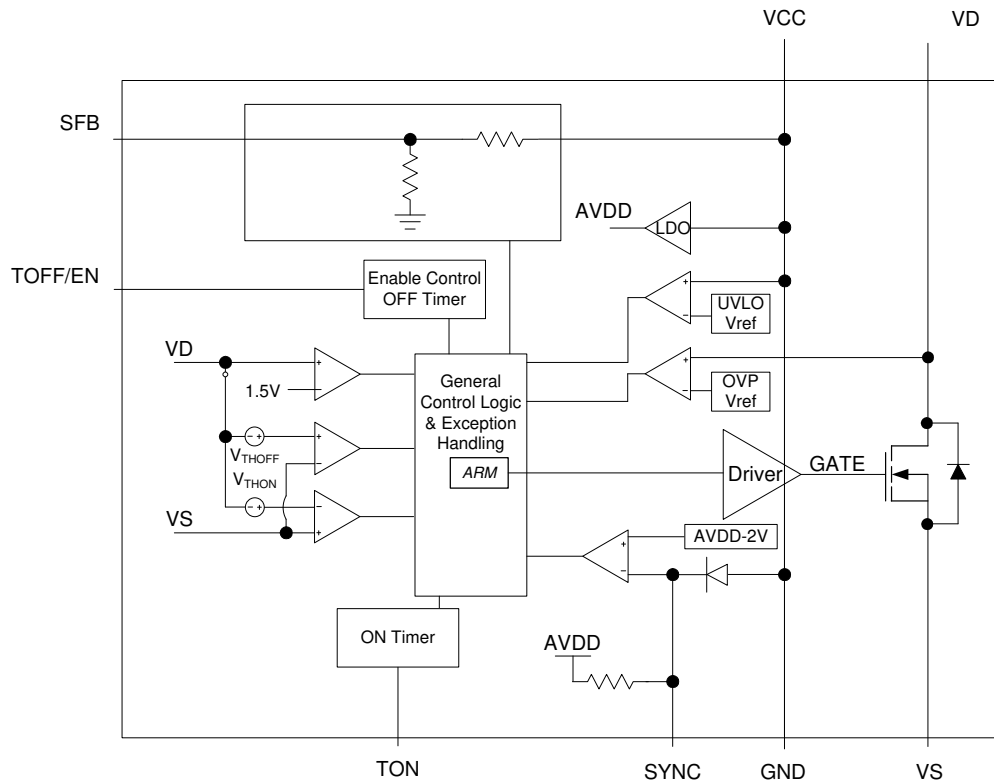


Figure 2 Internal Block Diagrams of AR30N60

Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Ratings	Unit
V _{CC}	Supply Voltage	-0.3 to 10	V
V _{DS}	Voltage Across Drain and Source	-1 to 60	V
V _{TOFF/EN, V_{TON}}	Voltage on TOFF/EN, TON Pins	-0.3 to 6	V
T _{MJ}	Maximum Junction Temperature	+150	°C
T _{ST}	Storage Temperature	-65 to +150	°C
ESD	Human Body Model, JESD22-A114	3.5	kV
	Machine Model, JESD22-A115-A	0.25	
	Charged Device Model, JESD22-C101	1.0	

Note: 4. These are stress ratings only. Operation outside the absolute maximum ratings may cause device failure. Operation at the absolute maximum rating for extended periods may reduce device reliability.

Package Thermal Data (@ T_A = +25°C, unless otherwise specified)

Symbol	Parameter	Rating	Unit
P _D	Power Dissipation (Note 5)	–	W
θ _{JA}	Thermal Resistance, Junction-to-Ambient (Note 6)	50	°C/W
θ _{JC}	Thermal Resistance, Junction-to-Case (Note 7)	15	°C/W

Notes: 5. Device mounted on FR-4 PCB, 2oz with minimum recommended pad layout.
 6. Device mounted on 25mm x 25mm 2oz copper board.
 7. Device mounted on 50mm x 50mm 2oz copper board.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{CC}	Supply Voltage Range	3.5	6	V
V _{DS}	Voltage across Drain and Source	-0.6	60	
F _{SW}	Switching Frequency	20	600	KHz
R _{TON}	TON Resistor	5	125	kΩ
R _{TOFF}	TOFF Resistor w/ SYNC	100	125	
T _J	Operating Junction Temperature	-40	+125	°C

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{AVDD}	Internal Regulator Output	$V_{CC} = 5\text{V}$		4.5		V
I_{CC_START}	Supply Current (Undervoltage)	$V_{CC} = 2.6\text{V}$	–	160		μA
$I_{CC_STANDBY}$	Supply Current (Disabled)	$V_{CC} = 5\text{V}$, $R_{EN/OFF} = 0\Omega$	–	380	500	
I_{CC_ON}	Supply Current (Enabled)	$V_{CC} = 5\text{V}$, $R_{EN/OFF} = 100\text{k}\Omega$	–	2.2	2.9	mA
V_{EN_ON}	TOFF/EN Turn-on Threshold, Rising	TOFF/EN driven, $I_{CC} > 1\text{mA}$	1.3	1.4	1.5	V
V_{EN_OFF}	TOFF/EN Turn-off Threshold, Falling	TOFF/EN driven, $I_{CC} < 200\mu\text{A}$	0.55	0.6	0.65	
I_{EN_START}	TOFF/EN Input Current, Disabled	$V_{TOFF/EN} = 1.3\text{V}$, rising from zero	-21.5	-20	-18.5	μA
I_{EN_ON}	TOFF/EN Input Current, Enabled	$V_{TOFF/EN} = 2.0\text{V}$	-10.7	-10	-9.3	
Undervoltage Lock-out (UVLO)						
$UVLO_{TH}$	VCC Undervoltage Lock-out Threshold Rising	–	2.8	3.0	3.20	V
$UVLO_{HYS}$	VCC Undervoltage Lock-out Threshold Hysteresis	–	–	200	–	mV
MOSFET Voltage Sensing						
V_{THARM}	Arming Threshold	V_D to GND, rising	1.3	1.5	1.7	V
V_{THON}	MOSFET Turn-ON Threshold	$(V_D - V_S)$ falling, $V_S = \text{GND} = 0\text{V}$	-200	-130	-70	mV
V_{THOFF}	MOSFET Turn-OFF Threshold	$(V_D - V_S)$ rising, $V_S = \text{GND} = 0\text{V}$, $V_{CC} \geq 4.2\text{V}$	-6	-4	-2	mV
		$(V_D - V_S)$ rising, $V_S = \text{GND} = 0\text{V}$, $2.8\text{V} < V_{CC} < 4.2\text{V}$	-30	-20	-10	mV
T_{DON}	MOSFET Turn-ON Propagation Delay	V_{THON} to 20% level on rising of V_{GS} of embedded MOSFET	-	30	50	ns
T_{DOFF}	MOSFET Turn-OFF Propagation Delay	V_{THOFF} to 80% level on falling of V_{GS} of embedded MOSFET	-	30	60	
MOSFET Static Characteristics						
R_{DSON}	Drain Source ON Resistance	$V_{CC} = 5\text{V}$, $I_D = 10\text{A}$	–	15	30	m Ω
Minimum ON-time						
T_{ON}	Minimum ON-time	$R_{TON} = 8.25\text{K}\Omega$	0.26	0.34	0.42	μs
		$R_{TON} = 100\text{K}\Omega$	2.25	3	3.75	
Minimum OFF-time						
T_{OFF}	Minimum OFF-time w/ SYNC	$R_{TOFF} = 100\text{K}\Omega$	0.3	0.5	0.7	μs
		$R_{TOFF} = 125\text{K}\Omega$	0.6	0.9	1.2	
		$V_{TOFF} = 4\text{V}$	0.7	1	1.3	
Synchronization						
V_{THSYNC}	SYNC Falling Threshold		$V_{AVDD} - 2.4$	$V_{AVDD} - 2.0$	$V_{AVDD} - 1.6$	V
T_{SDLY}	SYNC Propagation Delay	SYNC falling 50% to MOSFET Turn-OFF	–	40	70	ns
R_{SYNC}	SYNC Pull-up Resistor to AVDD		–	2.0	–	K Ω
Exception Handling						
T_{OTP}	Over-temperature	–	–	+150	–	$^\circ\text{C}$
$T_{RECOVER}$	Temperature to recover from Over-temperature Exception	–	–	+125	–	
Fault Detection	Minimum ON-time Resistance Fault	–	–	–	5	K Ω
		150	–	–	–	

Note: 8. Device mounted on FR-4 PCB, 2oz with minimum recommended pad layout.

Typical Performance Characteristics

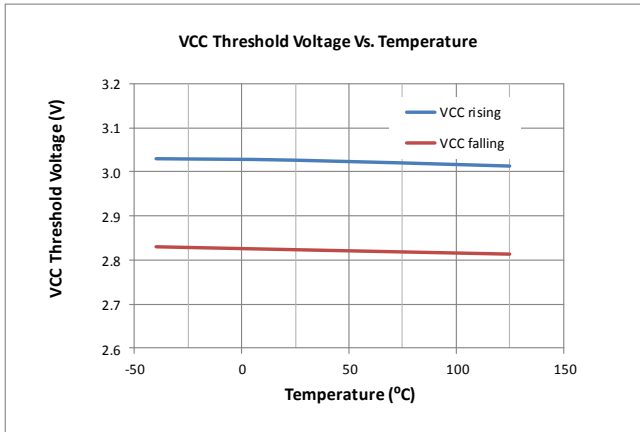


Figure 3

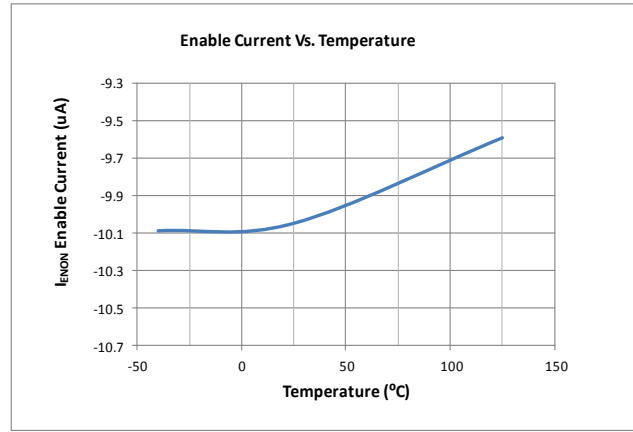


Figure 4

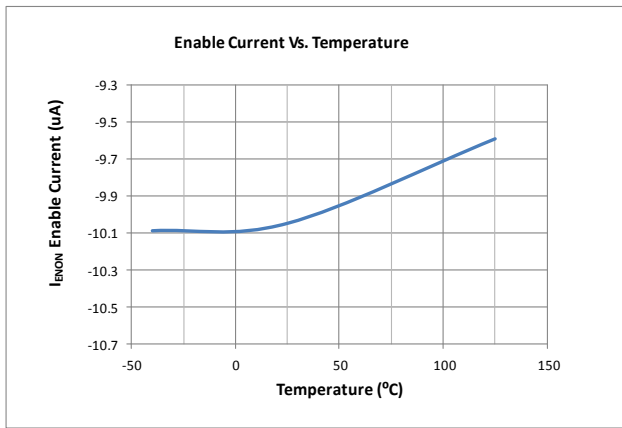


Figure 5

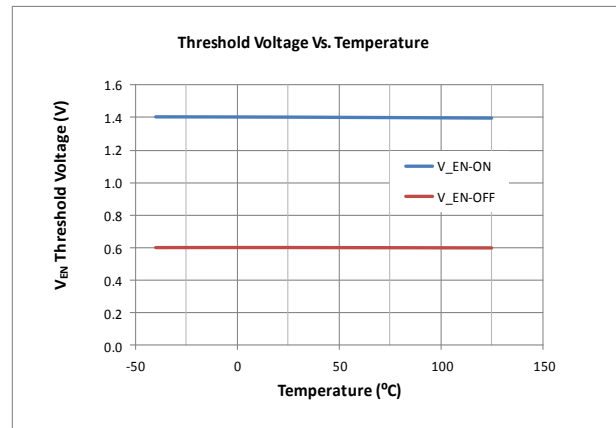


Figure 6

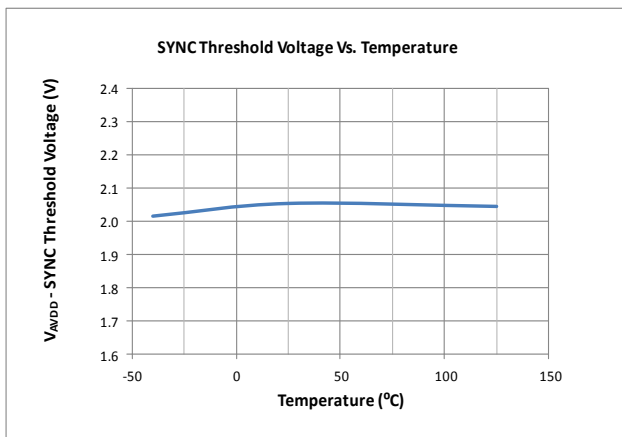


Figure 7

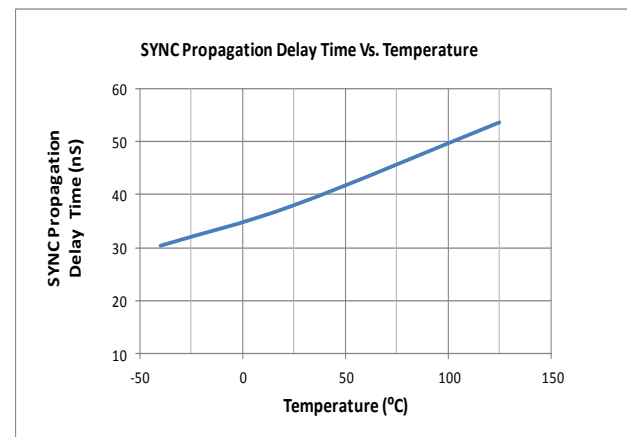


Figure 8

Typical Performance Characteristics (continued)

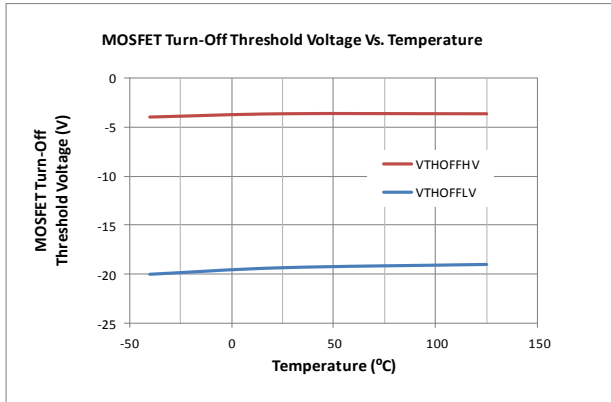


Figure 9

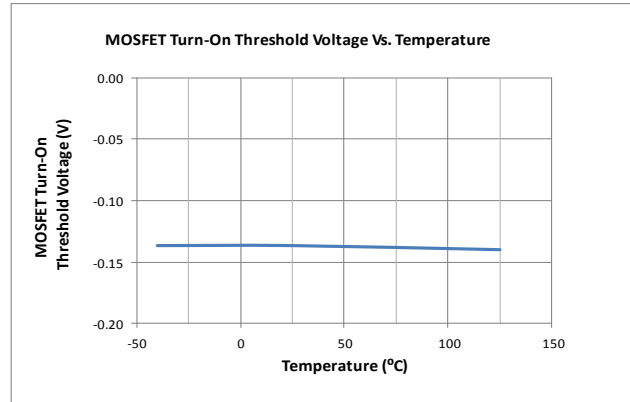


Figure 10

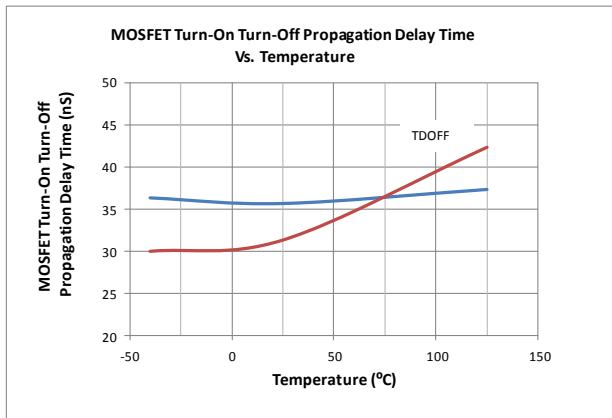


Figure 11

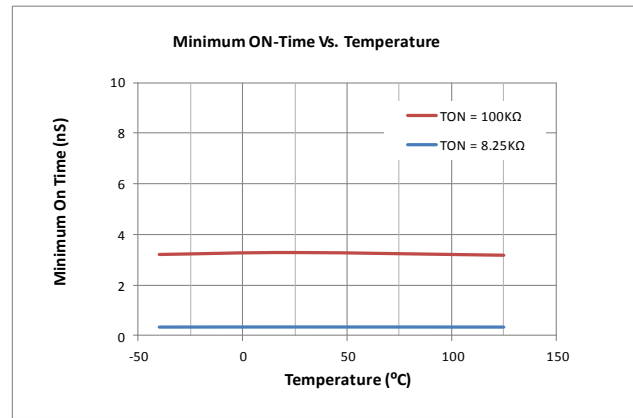


Figure 12

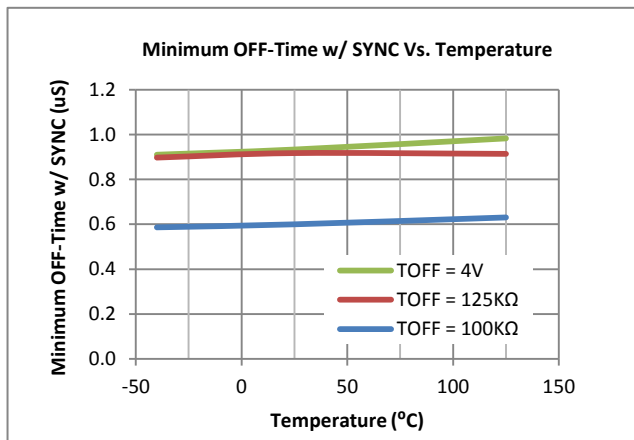


Figure 13

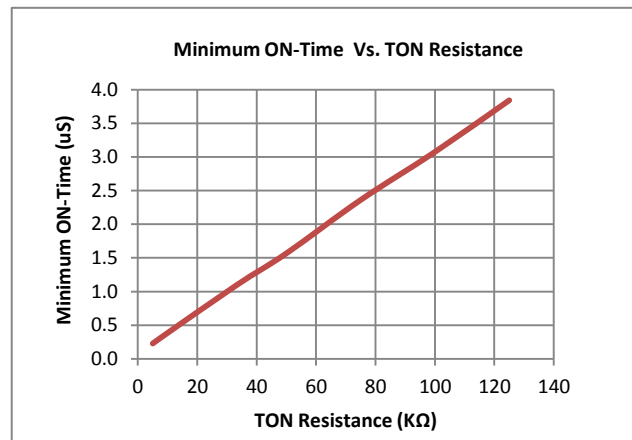


Figure 14

Typical Performance Characteristics (cont.)

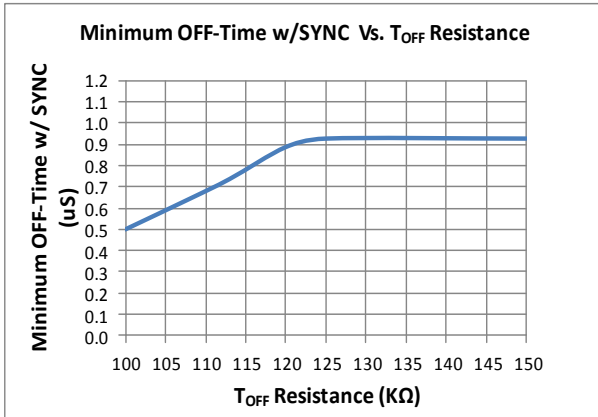


Figure 15

Functional Description

General Description

The AR30N60 is an Active/Synchronous Rectifier with embedded MOSFET. Together, with a primary-side controller of the typical flyback architecture, a compact AC/DC power conversion system with high efficiency can be built. The AR30N60 is applicable to both the PSR implementation where space is at a premium and the SSR implementation where the highest possible efficiency is often needed.

Operating under the SSR mode, the AR30N60 provides the necessary feedback to the primary-side controller via the highly popular AS431 and the widely available opto-coupler. Innovative techniques like near-zero voltage detection are incorporated into the design to extend the ON time of the embedded MOSFET to maximize the power conversion efficiency. In addition, the AR30N60 also works in exact synchronization to the primary-side controller through the SYNC pin to mitigate the risk of shoot-through.

If the output of the voltage differentiator ($V_{DS} = V_D - V_S$) falls below the turn-OFF threshold (V_{THOFF}) of the embedded MOSFET within the minimum ON-time period (T_{ON}), the AR30N60 will transition into the Light-load Mode at the next cycle. When the load condition changes such that $V_{DS} \approx V_{THOFF}$, the AR30N60 reverts to its nominal mode of operation after the T_{ON} timer expires.

Before the embedded MOSFET can be set, the voltage level of V_{DS} must be greater than the arming threshold (V_{THARM}) and the Minimum OFF-time (T_{OFF}) timer must be reset. Once these conditions are met and the voltage internally sensed over the V_D pin is 150mV lower than the V_S pin, the embedded MOSFET is turned ON, and the T_{ON} timer starts decrementing. The embedded MOSFET will remain ON for at least the length of the T_{ON} period. This can only be overridden when a negative pulse is detected over the SYNC pin. After the T_{ON} timer expires, the embedded MOSFET remains ON until $V_{DS} \approx V_{THOFF}$, at which point the embedded MOSFET is turned OFF. As iterated before, if the condition [$V_{DS} \approx V_{THOFF}$] becomes true before the T_{ON} timer expires, the AR30N60 will enter the Light-load Mode. Consequently, the embedded MOSFET shall stay OFF at the next cycle. When the drain voltage V_D increases to 1.5V, the T_{OFF} timer shall start decrementing, during which the embedded MOSFET is prevented from being turning ON.

The SYNC pin is pulled-up internally through a 2K Ω resistor to the AVDD. It is nominally at 4.5V. The pin is typically driven by the primary-side controller to turn ON/OFF the embedded MOSFET. Once the voltage level at the SYNC pin falls by more than 2V, the embedded MOSFET inside the AR30N60 shall be turned OFF. Note that an external resistor should be used to limit the input current to less than 2mA.

When the AR30N60 operates under CCM, shoot-through between the MOSFET on the primary-side and the embedded MOSFET on the secondary-side must be avoided. That is, one MOSFET must be turned OFF before the other one is going to be turned ON. In Figures 1 & 2, the recommended connection for the SYNC pin is shown. Whenever the gate of primary-side controller pulls 'H', a pulse signal shall be transmitted via an RC filter to the SYNC pin as a "pull-down signal". As a result, the embedded MOSFET is turned OFF before the MOSFET on the primary-side can be fully turned ON. To avoid the fault condition while the MOSFET on the primary-side is still ON, the T_{OFF} timer shall start decrementing whenever the SYNC pin is "pulled down". After the T_{OFF} timer expires, the AR30N60 starts responding to the V_{DS} signal at the next cycle.

In summary, the SYNC pin shall always be terminated in the application circuit on the primary-side to ensure proper system operation. In addition, the protection and fault detection schemes like UVLO and OTP are incorporated in the AR30N60 to guarantee system reliability.

Modes of Operation

Sleep Mode

Sleep Mode is a low-power operating mode. Entry is triggered when the voltage level appearing at the TOFF/EN pin becomes lower than the V_{EN_OFF} threshold. Under the Sleep Mode, current consumed by the AR30N60 is close to $I_{CC_STANDBY}$ while the overall power consumption is down to 1mW or less.

Upon entry into Sleep Mode, the embedded MOSFET along with the other blocks are turned OFF to minimize the power consumption. Once the voltage level at the TOFF/EN pin rises above the V_{EN_ON} , the AR30N60 shall exit Sleep Mode and transition into the Light-Load Mode. This transition typically takes around 25 μ s during which the internal circuits are powered up in an orderly manner.

Regardless of the mode at which the AR30N60 is operating, the AR30N60 can be forced into the Sleep Mode whenever the voltage level at the TOFF/EN pin drops to zero. The transition shall be immediate.

Light-Load Mode

When the inductor current on the secondary-side is small, the body-diode of the embedded MOSFET likely conducts for a time period much less than T_{ON} . As long as the conduction time is less than T_{ON} , the embedded MOSFET shall stay OFF. The voltage across the body-diode, V_{DS} , is being monitored continuously. If the body-diode conduction time is larger than T_{ON} , the AR30N60 shall transition into the Active Mode at the next cycle.

Functional Description (continued)

Active Mode

This is the normal operation mode under which the inductor current is large enough and the body-diode conduction time is larger than T_{ON} . During the ON time, V_{DS} is masked from affecting the operating status of the embedded MOSFET. At the expiration of the T_{ON} timer, monitoring of the V_{DS} resumes. As soon as $V_{DS} \approx V_{THOFF}$, the embedded MOSFET is turned OFF. At the next cycle, the embedded MOSFET shall be turned ON if and only if:

- 1) the ON time of the embedded MOSFET in the previous cycle is larger than T_{ON} and
- 2) $V_{DS} < V_{THON}$

If the ON time of the embedded MOSFET at the last cycle becomes (for whatever reason), less than the T_{ON} , AR30N60 shall transition into the Light-load Mode at the next immediate cycle. In general, the embedded MOSFET alternates between the ON and OFF states in accordance to the values of the following parameters: V_{DS} , T_{ON} , T_{OFF} , voltage level at the SYNC pin.

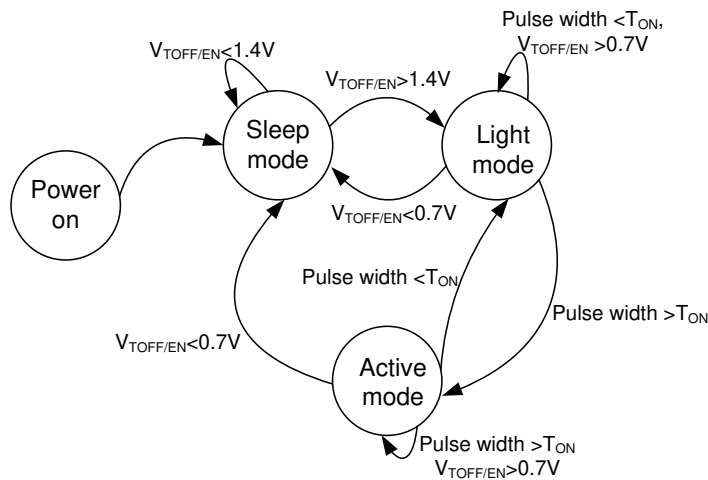


Figure 16 State Diagram

Fault Detection and System Protection

Over-temperature Protection

When the temperature of the AR30N60 rises above T_{OTP} , the output of the SFB pin shall be pulled up. As a result, the primary-side controller is informed of the abnormal condition via the typical opto-coupler. The primary-side controller typically responds by shutting down the whole operation.

Note that the OTP protection mechanism has a built-in hysteresis of $+25^{\circ}\text{C}$ to minimize false triggering.

UVLO Protection

Whenever the voltage level at the VCC pin falls below 2.8V (i.e. $UVLO_{TH} - UVLO_{HYS}$), the UVLO protection shall be invoked. The embedded MOSFET is then turned OFF. Once the voltage level at the VCC pin rises above the $UVLO_{TH}$, the AR30N60 will return to the normal operation at the next cycle.

Application Information

Device Power-On

When $V_{CC} < UVLO_{TH}$, the AR30N60 transitions into the Sleep Mode. At this time, the TOFF/EN pin is connected to ground through an internal resistor.

When the V_{CC} rises above $UVLO_{TH}$, an internal current source starts to operate and eventually delivers $20\mu A$ (I_{EN_START}) to the external resistor, R_{TOFF} , which is connected to the TOFF/EN pin. Assuming a typical value of $R_{TOFF} \geq 70K\Omega$, the $V_{TOFF/EN}$ eventually reaches $1.4V$ (V_{EN_ON}) and above. Then, the AR30N60 shall exit the Sleep Mode and transition into the Light-load Mode. Thereafter, the internal current source shall reduce the output current to $10\mu A$ (I_{EN_ON}).

ON Timer Setting

The T_{ON} period is set by adding a resistor between the TON pin and ground. During typical operation, the OFF/ON of the embedded MOSFET is typically followed by successive oscillation in declining amplitude, i.e. ringing. This is caused by the parasitic existed on the printed circuit board. Activating the T_{ON} timer prevents the AR30N60 from responding to the ringing. Once the T_{ON} timer starts decrementing, the voltage level of V_{DS} is ignored. After the T_{ON} timer expires, the value of V_{DS} shall be checked continuously. Eventually, one of the following two situations shall become true:

- 1) If $V_{DS} \geq V_{THOFF}$, the embedded MOSFET is turned OFF. The AR30N60 shall enter the Light-load Mode at the next cycle.
- 2) If the voltage level of the V_{DS} remains substantially lower than the V_{THOFF} , the embedded MOSFET shall remain ON. When $V_{DS} \geq V_{THOFF}$, the AR30N60 shall be turned OFF. If, for whatever reason, V_{DS} remains lower than V_{THOFF} , the primary-side controller eventually shall turn OFF the AR30N60. Regardless, the AR30N60 will enter the Active Mode at the next cycle.

$$T_{ON} (\mu s) = 0.028\mu s/K\Omega \times R_{TON} + 0.1\mu s; \text{ where } 0.24\mu s \leq T_{ON} \leq 4.3\mu s, 5K\Omega \leq R_{TON} \leq 125K\Omega$$

OFF Timer Setting

In the typical applications of AR30N60 as illustrated in Figures 1 & 2, the SYNC pin is connected to the primary-side controller. The embedded MOSFET is turned OFF whenever one of the following conditions becomes true:

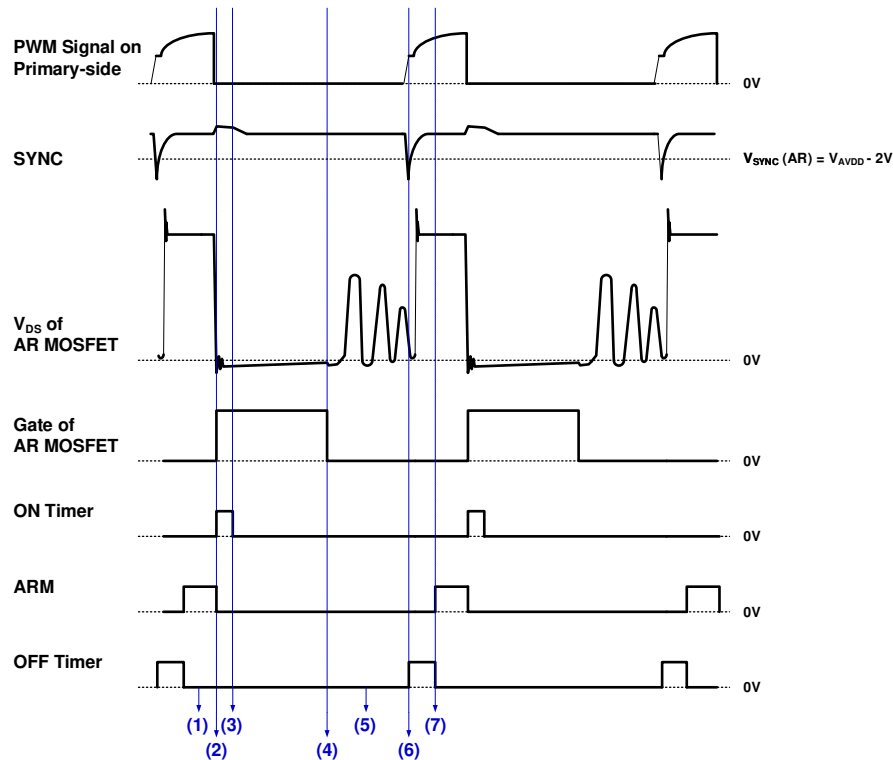
- 1) $V_{DS} \approx V_{THOFF}$
- 2) The voltage level at the SYNC pin is pulled low

While the T_{OFF} timer ensures that the embedded MOSFET stays OFF when the primary-side is conducting, the value of T_{OFF} is set by an external resistor, R_{TOFF} . The value of R_{TOFF} can be calculated from the following equation,

$$T_{OFF} (\mu s) = [0.019\mu s/K\Omega \times R_{TOFF} (K\Omega)] - 1.4\mu s; \text{ where } 0.5\mu s \leq T_{OFF} \leq 1.0\mu s, 100K\Omega \leq R_{TOFF} \leq 125K\Omega$$

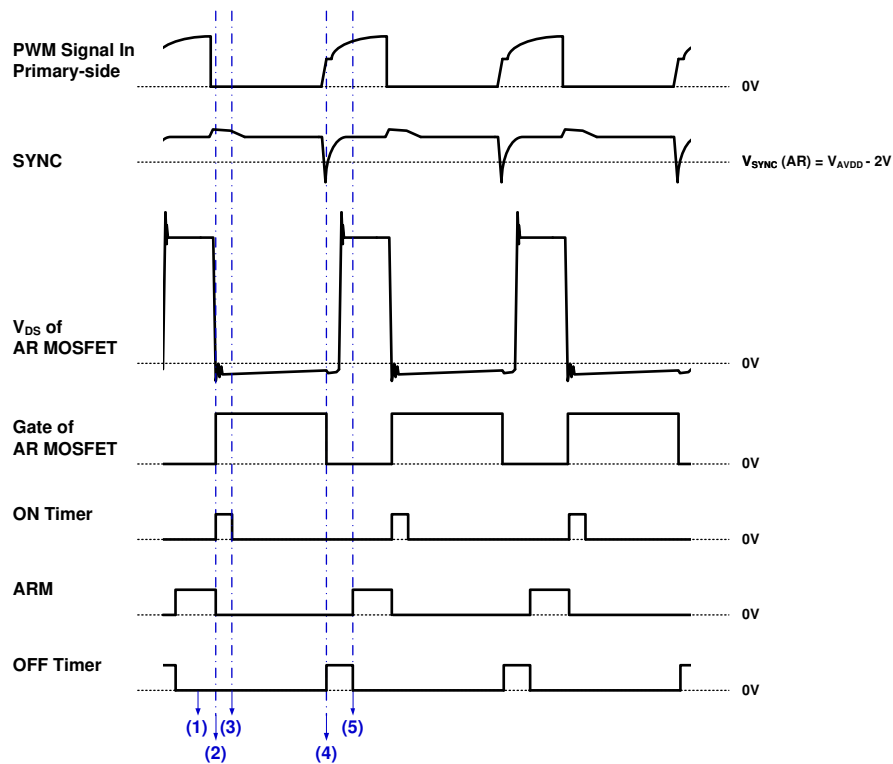
MOSFET Turn-ON/OFF Control Sequence

DCM Operation with SYNC-pin Connected



Steps of Operation

- (1) At the logic 'H' of the internal signal ARM, the voltage level of V_{DS} shall be checked continuously. At this time, no current should flow through the embedded MOSFET (neither its body-diode).
- (2) When the primary-side controller stops conducting, the transfer of energy to the secondary-side commences. At this time, the current flowing through the body-diode of embedded MOSFET shall rise rapidly. When $V_{DS} \leq V_{THON}$ (-130mV typical), the embedded MOSFET is turned ON, the T_{ON} timer starts decrementing, and the ARM signal is reset. Before the T_{ON} timer expires, the embedded MOSFET shall remain ON.
- (3) Now, the T_{ON} timer expires. If V_{DS} is still lower than V_{THOFF} , the embedded MOSFET shall remain ON.
- (4) Eventually, the current flowing through the embedded MOSFET falls to zero. Once $V_{DS} \approx V_{THOFF}$, the embedded MOSFET shall be turned OFF.
- (5) Because the ARM signal remains at the OFF state, the embedded MOSFET is kept at the OFF state even if $V_{DS} \leq V_{THON}$. That is, the value of V_{DS} is ignored.
- (6) The primary-side controller starts conducting. This causes the signal at the input of the SYNC pin to be pulled down. As soon as the condition [$V_{SYNC} < V_{AVDD} - 2V$] becomes true, the T_{OFF} timer starts decrementing.
- (7) After the T_{OFF} timer expires, the AR30N60 starts to monitor the voltage level of V_{DS} as soon as the ARM signal becomes 'H'.

CCM Operation with SYNC-pin Connected

Steps of Operation

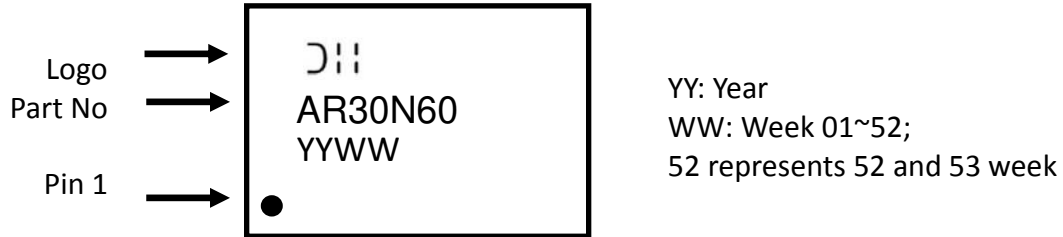
- (1) At the logic 'H' of the internal signal ARM, the voltage level of V_{DS} shall be checked continuously. At this time, no current shall flow through the embedded MOSFET (neither its body-diode).
- (2) When the primary-side controller stops conducting, the transfer of energy to the secondary-side commences. At this time, the current flowing through the body-diode of embedded MOSFET shall rise rapidly. When $V_{DS} \leq V_{THON}$ (-130mV typical), the embedded MOSFET is turned ON, the T_{ON} timer starts decrementing, and the ARM signal is reset. Before the T_{ON} timer expires, the embedded MOSFET shall remain ON.
- (3) Now, the T_{ON} timer expires. If V_{DS} is still lower than V_{THOFF} , the embedded MOSFET shall remain ON.
- (4) In contrast to the operation under DCM, the current flowing through the embedded MOSFET never falls to zero. As a result, the voltage level of V_{DS} can never reach the V_{THOFF} (-4mV typical). Therefore, unlike in the case of the DCM operation, the embedded MOSFET shall remain ON until the primary-side controller starts conducting. To avoid any possibility of shoot-through, the embedded MOSFET is turned OFF as soon as the condition $[V_{SYNC} < V_{AVDD} - 2V]$ becomes true. When the embedded MOSFET is turned OFF, the remaining energy starts to flow through the body-diode. This creates a voltage drop far smaller than V_{THON} . Therefore, the T_{ON} timer is activated to prevent the embedded MOSFET from being turned ON.
- (5) After the T_{OFF} timer expires, the level of the ARM signal shall be checked continuously. As soon as it goes 'H', the voltage level of V_{DS} is checked and step #1 repeats.

Ordering Information (Note 9)

Product	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
AR30N60PPA-13	AR30N60	13	12	3,000

Note: 9. For packaging details, please visit our website at <http://www.diodes.com/products/packages.html>.

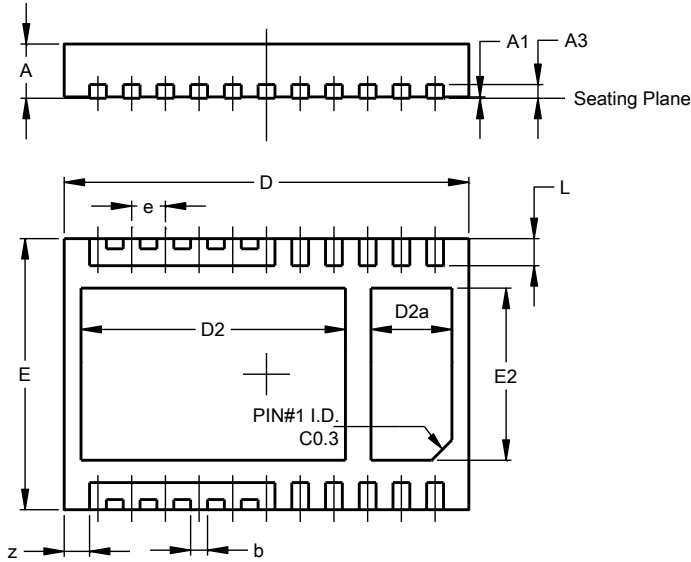
Marking Information



Package Outline Dimensions (All dimensions in mm.)

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

V-DFN6040-22

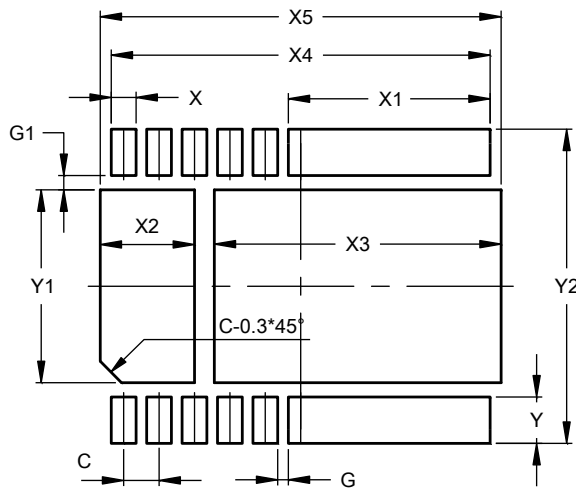


V-DFN6040-22			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	0.02
A3	0.203 BSC		
b	0.20	0.30	0.25
D	5.95	6.05	6.00
D2	3.82	4.02	3.92
D2a	1.10	1.30	1.20
E	3.95	4.05	4.00
E2	2.44	2.64	2.54
e	0.50 BSC		
L	0.35	0.45	0.40
z	0.375 TYP		
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

V-DFN6040-22



Dimensions	Value (in mm)
C	0.500
G	0.150
G1	0.200
X	0.350
X1	2.850
X2	1.330
X3	4.050
X4	5.350
X5	5.660
Y	0.650
Y1	2.700
Y2	4.400

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