imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





AS358/358A/358B

LOW POWER DUAL OPERATIONAL AMPLIFIERS

Description

The AS358/358A/358B consists of two independent, high gain and internally frequency compensated operational amplifiers, they are specifically designed to operate from a single power supply. Operation from split power supply is also possible and the low power supply current drain is independent of the magnitude of the power supply voltages. Typical applications include transducer amplifiers, DC gain blocks and most conventional operational amplifier circuits.

The AS358/358A/358B series is compatible with industry standard 358. The AS358A has more stringent input offset voltage than the AS358.

The AS358 is available in PDIP-8, TDIP-8, SO-8, TSSOP-8 and MSOP-8 packages, the AS358A is available in PDIP-8 and SO-8 packages and AS358B is available in TSSOP-8 package.

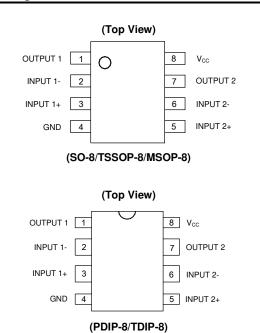
Features

- Internally Frequency Compensated for Unity Gain
- Large Voltage Gain: 100dB (Typical)
- Low Input Bias Current: 20nA (Typical)
- Low Input Offset Voltage: 2mV (Typical)
- Low Supply Current: 0.5mA (Typical)
- Wide Power Supply Voltage:
 - Single Supply: 3V to 36V
 - Dual Supplies: ±1.5V to ±18V
- Input Common Mode Voltage Range Includes Ground
- Large Output Voltage Swing: 0V to V_{cc} -1.5V
- Lead-Free Packages: SO-8, PDIP-8 and TSSOP-8
 - Totally Lead-Free; RoHS Compliant (Notes 1 & 2)
- Lead-Free Packages, Available in "Green" Molding Compound: SO-8, PDIP-8, TDIP-8, TSSOP-8 and MSOP-8
 - Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
 - Halogen and Antimony Free. "Green" Device (Note 3)

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Pin Assignments

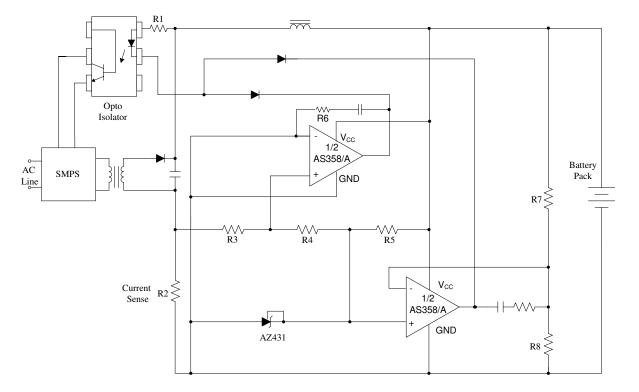


Applications

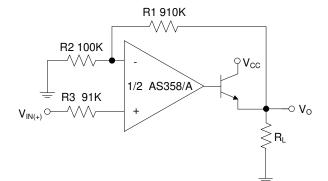
- Battery Charger
- Cordless Telephone
- Switching Power Supply

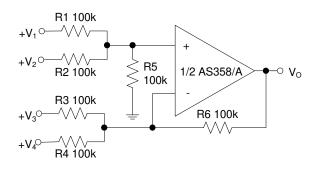


Typical Applications Circuit



Battery Charger



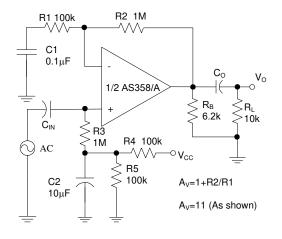


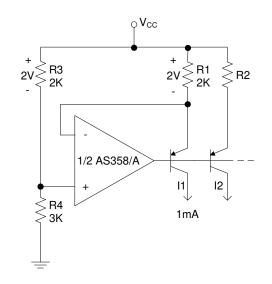
Power Amplifier

DC Summing Amplifier



Typical Applications Circuit (Cont.)



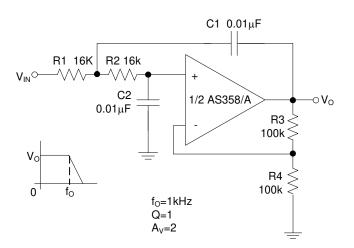


AC Coupled Non-Inverting Amplifier

 $\begin{array}{c} R1 & 1M \\ 0.001 \mu F \\ \hline R2 & 100k \\ \hline \\ R3 & 100k \\ \hline \\ R5 & 100k \\ \hline \\ R4 \\ 100k \\ \hline \\ \end{array}$

Pulse Generator

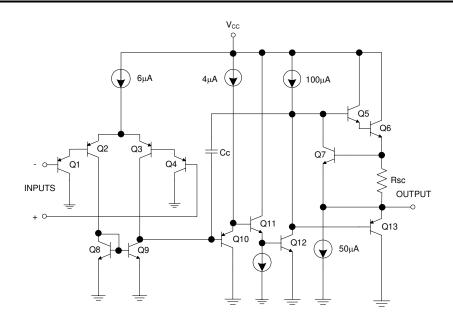
Fixed Current Sources



DC Coupled Low-Pass Active Filter



Functional Block Diagram



Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Parameter	Rati	Unit	
V _{CC}	Power Supply Voltage	40		V
V _{ID}	Differential Input Voltage	40		V
V _{IC}	Input Voltage	-0.3 to 40		V
		PDIP-8	830	
_		SO-8	550	
PD	Power Dissipation ($T_A = +25^{\circ}C$)	TSSOP-8	500	mW
		MSOP-8	470	
TJ	Operating Junction Temperature	perature +150		°C
T _{STG}	Storage Temperature Range	-65 to +150		°C
T _{LEAD}	Lead Temperature (Soldering, 10 Seconds)	+260		°C

Notes: 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

5. ESD sensitivity.

Recommended Operating Conditions

Symbol	Parameter	Min	Мах	Unit
V _{CC}	Supply Voltage	3	36	V
T _A	Ambient Operating Temperature Range	-40	+85	°C



Electrical Characteristics (Limits in standard typeface are for T_A = +25°C, bold typeface applies over -40°C to +85°C (Note 6), V_{CC} = 5V, GND = 0V, unless otherwise specified.)

Symbol	Pa	arameter	Conditions			Тур	Мах	Unit
				10050	_	2	5	
V _{IO} Input Offset Volta				AS358	_	—	7	1
			$V_{O} = 1.4V, R_{S} = 0\Omega,$		_	2	3	- mV
	Input Offset Voltage		$V_{CC} = 5V \text{ to } 30V$	AS358A	_	_	5	
					_	_	2	
				AS358B	_	_	4	
$\Delta V_{IO} / \Delta T$	Average Temperature Coefficient of Input Offset Voltage		$T_{A} = -40^{\circ}C \text{ to } +85^{\circ}C$	$T_{A} = -40^{\circ}C \text{ to } +85^{\circ}C$		7	_	μV/°
			I _{IN+} or I _{IN-} , V _{CM} = 0V		_	20	200	<u> </u>
IBIAS	Input Bias Current					_	200	nA
					_	5	30	
lio	Input Offset Current		$I_{IN}+-I_{IN}-, V_{CM}=0V$		_	—	100	nA
VIR	Input Common Mod	e Voltage Range (Note 7)	$V_{CC} = 30V$		0	_	V _{CC} - 1.5	V
		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C,$ $V_{CC} = 30V$	R _L = ∞,	_	0.7	2	<u> </u>	
lcc	CC Supply Current		$T_A = -40^{\circ}$ C to +85°C, $R_L = \infty$, $V_{CC} = 5$ V		_	0.5	1.2	- mA
Gv Large Signal Voltage				85	100			
	Large Signal Voltage	e Gain	$V_{CC} = 15V, V_O = 1V \text{ to } 11V, R_L \ge 2k\Omega$		80	_		dB
CMRR Common Mode Reject				60	70	_		
	Common Mode Reje	tion Ratio $DC, V_{CM} = 0V$ to (DC, $V_{CM} = 0V$ to (V_{CC})	;-1.5)V	60	_		dB
	Power Supply Rejection Ratio		$V_{CC} = 5V$ to 30V		70	100		dB
PSRR					60	_	_	
CS	Channel Separation		f = 1kHz to 20kHz		_	-120		dE
			$V_{1N+} = 1V V_{1N-} = 0V V_{00} = 15V$		20	40		-
ISOURCE		Source			20	_	_	m/
			Sink $\frac{V_{IN} = 0V, V_{IN} = 1V, V_{CC} = 15V,}{V_{O} = 2V}$ $\frac{V_{IN} = 0V, V_{IN} = 1V, V_{CC} = 15V,}{V_{O} = 0.2V}$		10	15		
	Output Current				5			m/
Isink		Sink			12	50	_	μA
I _{SC}	Output Short Circuit	Current to Ground	V _{CC} = 15V		_	40	60	m/
130					26			
			V_{CC} = 30V, R_L = 2k Ω		26			-
V _{OH}		Output Voltage Swing			27	28		- V
	Output Voltage Swir			$V_{CC}=30V,\ R_L=10k\Omega$				-
	4				27	5	20	
Vol			$V_{CC} = 5V, R_L = 10k\Omega$			5	20 30	m\
θ _{JC}			SO-8		+	17		
	Thormal Pasiatonas	Thermal Decisionan (Institute to Occor)			-			
	Thermal Resistance (Junction to Case)		TSSOP-8 MSOP-8		47 —			
								°C/W
0	The sum of Decks			SO-8		115		
θ_{JA}	Thermal Resistance (Junction to Ambient)		TSSOP-8			209	-	
	1			MSOP-8		160		

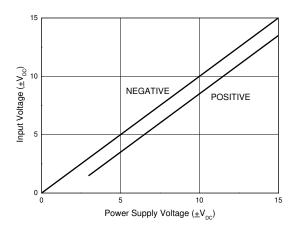
7. The input common-mode voltage of either input signal voltage should not be allowed to go negatively by more than 0.3V (at +25°C). The upper end of the common-mode voltage range is V_{CC}-1.5V (at +25°C), but either or both inputs can go to +36V without damages, independent of the magnitude of the V_{CC}



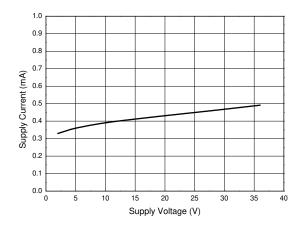
Performance Characteristics

Input Voltage Range

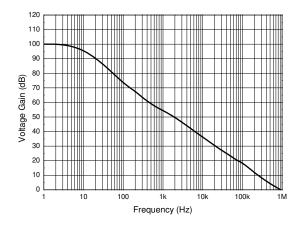
Input Current

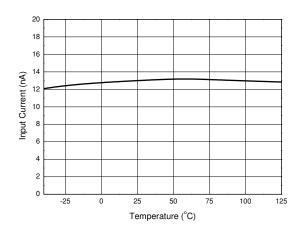


Supply Current

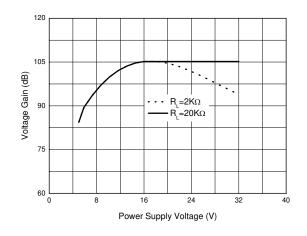


Open Loop Frequency Response

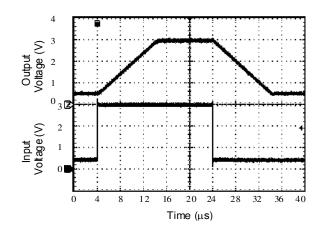




Voltage Gain



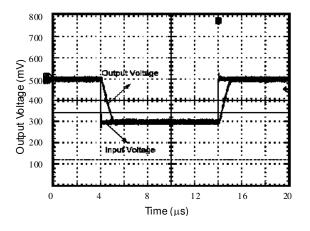
Voltage Follower Pulse Response



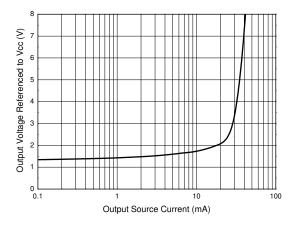


Performance Characteristics (Cont.)

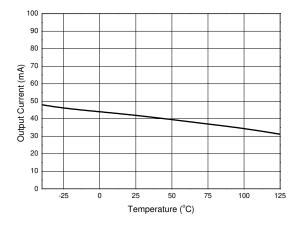
Voltage Follower Pulse Response (Small Signal)



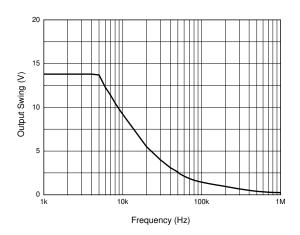
Output Characteristics: Current Sourcing



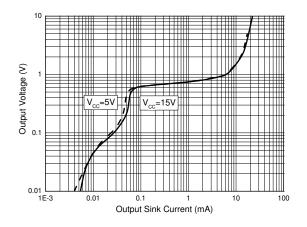
Current Limiting



Large Signal Frequency Response

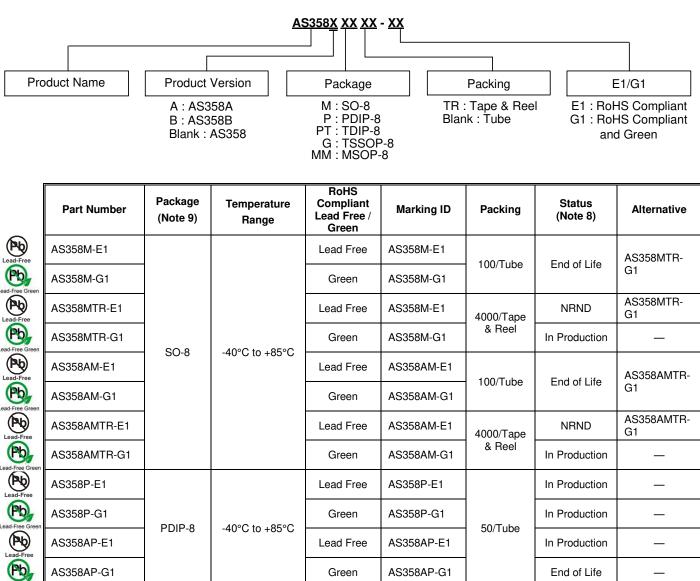


Output Characteristics: Current Sinking





Ordering Information



Lead Free

Green

Green

Lead Free

Green

Green

Lead Free

Green

AS358AP-E1

AS358AP-G1

AS358PT-G1

EG3A

GG3A

GG3F

AS358MM-E1

AS358MM-G1

50/Tube

4000/Tape

& Reel

3000/Tape

& Reel

Pb Notes:

Pb

ead-Fre

Pb

Pb

Pb

8. NRND: Not Recommended for New Design

TDIP-8

TSSOP-8

MSOP-8

9. For packaging details, go to our website at: https://www.diodes.com/design/support/packaging/diodes-packaging/.

-40°C to +85°C

-40°C to +85°C

-40°C to +85°C

AS358AP-E1

AS358AP-G1

AS358PT-G1

AS358GTR-E1

AS358GTR-G1

AS358BGTR-G1

AS358MMTR-E1

AS358MMTR-G1

AS358GTR-

AS358MMTR-

G1

G1

In Production

End of Life

In Production

NRND

In Production

In Production

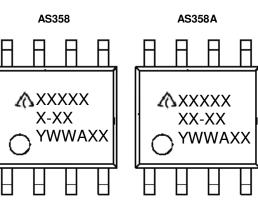
End of Life

In Production

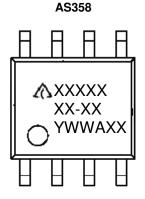


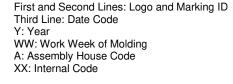
Marking Information

(1) SO-8



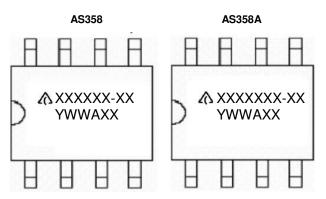
(2) MSOP-8





First and Second Lines: Logo and Marking ID Third Line: Date Code Y: Year WW: Work Week of Molding A: Assembly House Code XX: Internal Code

(3) PDIP-8



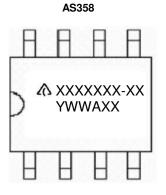
First Line: Logo and Marking ID Second Line: Date Code Y: Year WW: Work Week of Molding A: Assembly House Code XX: Internal Code

1



Marking Information (Cont.)

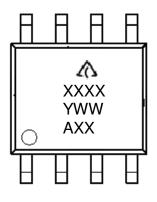
(4) TDIP-8



First Line: Logo and Marking ID Second Line: Date Code Y: Year WW: Work Week of Molding A: Assembly House Code XX: Internal Code

(5) TSSOP-8

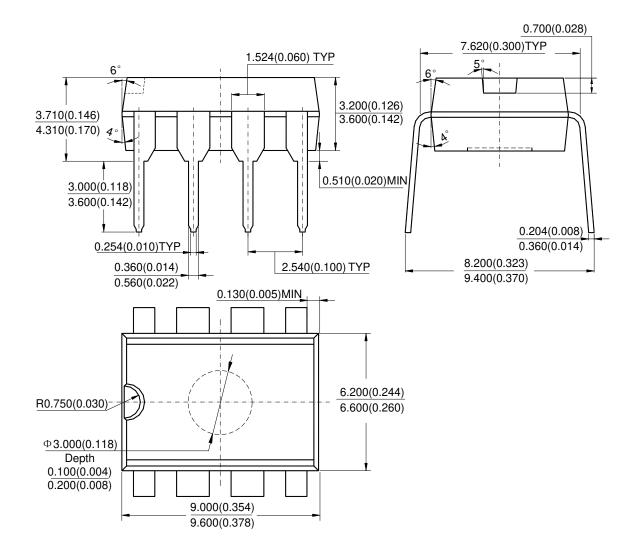
AS358/358B



First Line: Logo Second Line: Marking ID Third and Fourth Lines: Date Code Y: Year WW: Work Week of Molding A: Assembly House Code XX: Internal Code

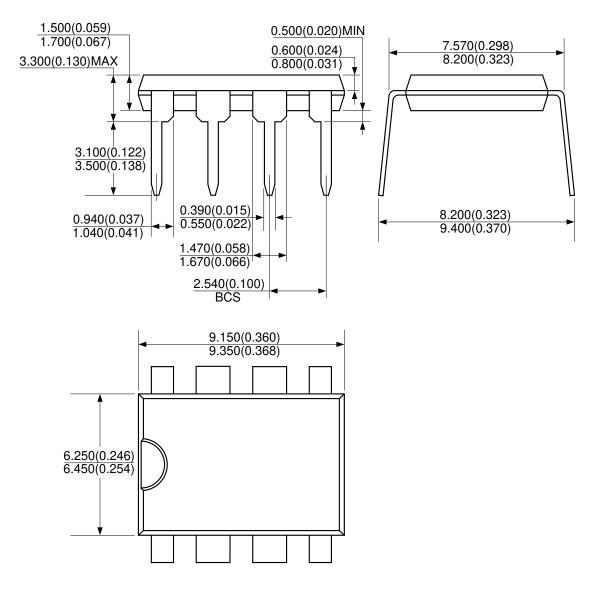


(1) Package Type: PDIP-8



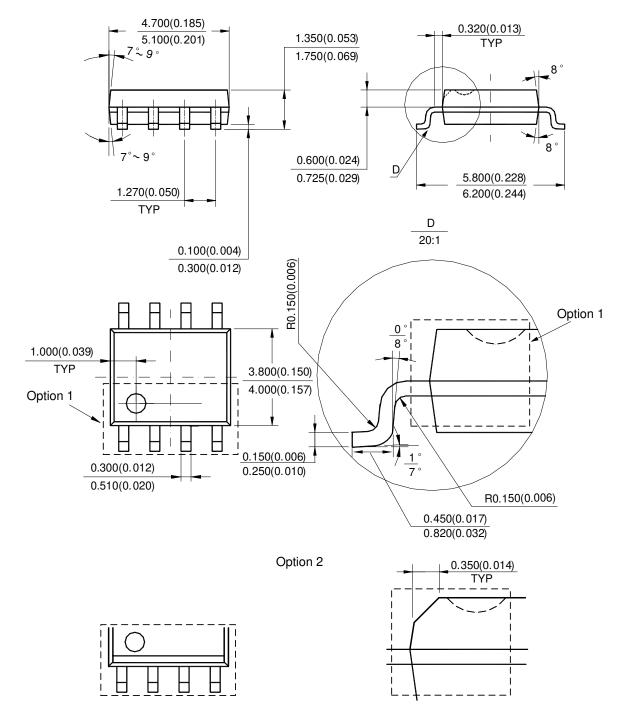


(2) Package Type: TDIP-8





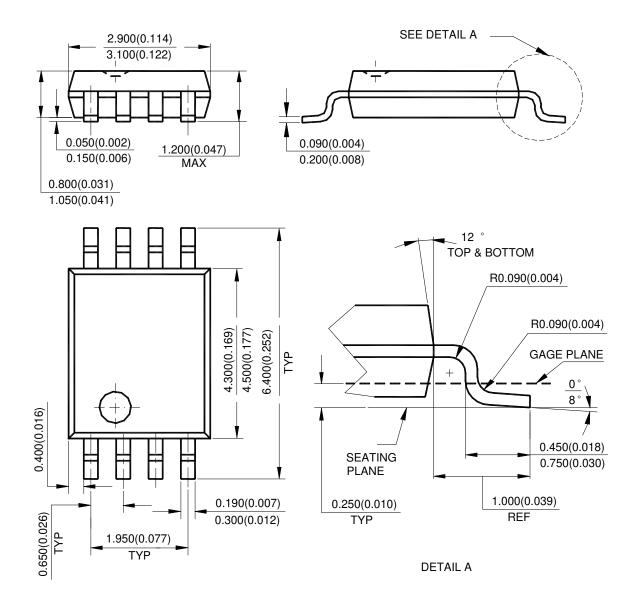
(3) Package Type: SO-8



Note: Eject hole, oriented hole and mold mark is optional.

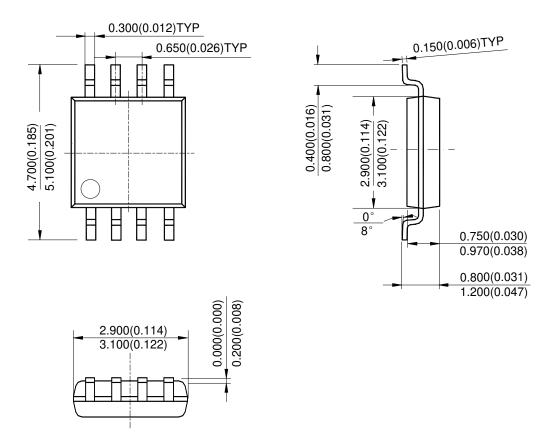


(4) Package Type: TSSOP-8





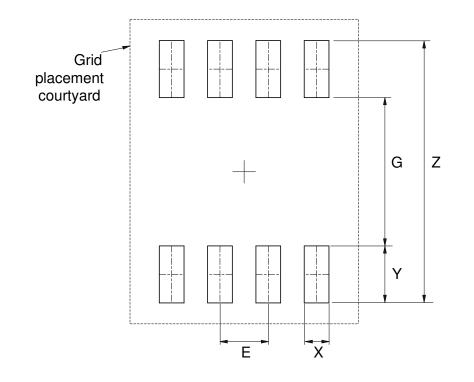
(5) Package Type: MSOP-8





Suggested Pad Layout

(1) Package Type: SO-8

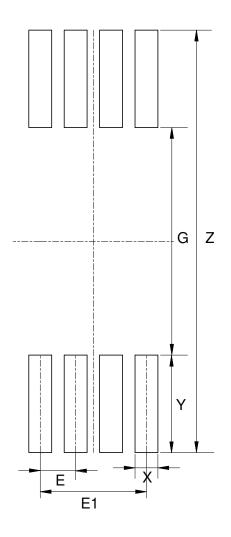


Dimensions	Z	G	X	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050



Suggested Pad Layout (Cont.)

(2) Package Type: TSSOP-8



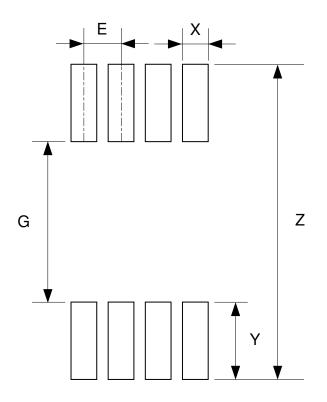
Dimensions	Z	G	X	Y	E	E1
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	7.720/0.304	4.160/0.164	0.420/0.017	1.780/0.070	0.650/0.026	1.950/0.077



AS358/358A/358B

Suggested Pad Layout (Cont.)

(3) Package Type: MSOP-8



Dimensions	Z	G	Х	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	5.500/0.217	2.800/0.110	0.450/0.018	1.350/0.053	0.650/0.026



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2018, Diodes Incorporated

www.diodes.com