imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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3.2 x 2.5 x 0.85 mm

RoHS

Compliant

FEATURES:

- Ultra Miniature Pure Silicon™ Clock Oscillator
- Pin Configurable LVPECL output
- Low Jitter (Period Jitter RMS 2.5ps typical)
- Low Integrated Phase Jitter 2ps max
- Tight Stability +/-10ppm -40 to +85C
- Excellent Shock & Vibration Immunity

> APPLICATIONS:

- Consumer Electronics
- Storage Area Networks
- SATA, SAS, Fibre Channel
- Passive Optical Networks
- EPON, 10G-EPON, GPON, 10G-PON • Ethernet
- 1G, 10GBASE-T/KR/LR/SR, and FCoE
- PCI Express

Low Jitter Pin Configurable CMOS Output 3G MEMS

STANDARD SPECIFICATIONS:

Pre-programmed Output Frequency Configuration

| Ordering Info | Freq (MHz) | Freq Select Bits [FS1, FS0] – Default is [11] | | | | |
|------------------------------|-----------------------------|---|-------|--------|--------|--|
| Ordering into | | 00 | 01 | 10 | 11 | |
| Frequency Configuration 1 | \mathbf{f}_{OUT} | 125 | 212.5 | 106.25 | 156.25 | |
| Custom Configuration | $\mathbf{f}_{\mathrm{OUT}}$ | Contact Abracon for customized configurations | | | | |

Frequency select bits [FS1, FS0] are weakly tied high so if left **floated**, the default setting will be [11] and the device will output the associated frequency highlighted in **Bold**. If other frequency combinations are required, please contact Abracon for customized configuration. Please see the configurable frequency range in the section 2.0

Key Electrical Specifications

| Parameters | | Minimum | Typical | Maximum | Units | Notes |
|---|-----------------|--|---------|-----------------------|--|--------------------------------------|
| Configurable frequency range | | 10 | | 460 | MHz | Commercial, Industrial temp range |
| Operating Temperature | | -20 | | +70 | °C | See options |
| Storage Temperature | | -55 | | +150 | °C | |
| Overall Frequency Stability ^{*1} | | -50 | | +50 | ppm | See options |
| Supply Voltage (Vdd) | | +2.25 | | +3.6 | V | |
| Startup Time | | | | 5 | ms | T=25°C |
| Enable Time | | | | 20 | ns | |
| Disable Time | | | | 5 | ns | |
| Disable Current | | | 21 | 23 | mA | |
| Tri-state Function (Standby/Disable) | | "1" (VIH≥0.75*Vdd) or Open: Oscillation "0" (VIL<0.25*Vdd) : Hi Z | | V | $40k\Omega$ pull-up resistor embedded | |
| Aging | | -5.0 | | +5.0 | ppm | First year |
| Supply Current (I _{dd}) | | | 56.5 | 58 | mA | RL=50Ω, 156.25MHz |
| Output Lagia Laval | V _{OH} | V _{dd} -1.08 | | | v | DI -500 |
| Output Logic Level V ₀ | | | | V _{dd} -1.55 | v | RL=50Ω |
| Peak to Peak Output Swing | | | 800 | | mV | Single Ended |
| Rise Time | Tr | | 250 | | ps | RL=50Ω |
| Fall Time | Fall Time Tf | | 250 | | ps | 20%/80%*VDD |
| Duty Cycle | Duty Cycle | | | 52 | % | Differential |

*1. Frequency stability includes frequency variations due to initial tolerance, temp. and power supply voltage





ASEMCLP





3.2 x 2.5 x 0.85 mm

Key Electrical Specifications (continued)

| Parameters | Minimum | Typical | Maximum | Units | Notes |
|--|---------|---------|---------|-------|------------------------------|
| Period Jitter RMS (J _{PER}) | | 2.5 | | ps | F0=156.25MHz |
| | | 0.25 | 2 | | 200kHz ~ 20MHz, 156.25MHz |
| Integrated Phase Jitter (J _{PH}) | | 0.38 | 2 | ps | 100kHz ~ 20MHz, 156.25MHz |
| | | 1.70 | 2 | | 12kHz ~ 20MHz, 156.25MHz |

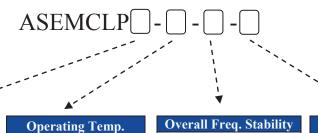
ABSOLUTE MAXIMUM RATINGS:

| Item | Minimum | Maximum | Unit | Condition |
|-----------------|---------|----------------------|------|-----------|
| Supply Voltage | -0.3 | +4.0 | V | |
| Input Voltage | -0.3 | V _{dd} +0.3 | V | |
| Junction Temp. | | +150 | °C | |
| Storage Temp. | -55 | +150 | °C | |
| Soldering Temp. | | +260 | °C | 40sec max |
| ESD | | | V | |
| HBM | | 4,000 | | |
| MM | | 400 | | |
| CDM | | 1,500 | | |

OPTIONS AND PART IDENTIFICATION:

4----

(left blank if standard)



| Frequency | Operating Temp. | Overall Freq. Stability | Packaging |
|---|---|--------------------------------|----------------------|
| Combination | Blank: $-20^{\circ}C \sim +70^{\circ}C$ | Blank: ±50ppm | Blank: 110pcs / Tube |
| (See table below) | L: -40°C ~ +85°C | R: ±25 ppm | T: 1,000pcs / reel |
| 1: Cfg. 1 | | Y: ±10ppm | T3: 3,000pcs / reel |
| Custom configuration available. Please contact | | | T5: 5,000pcs / reel |
| Abracon | | | |

| Ordering Info | Freq (MHz) | Freq Select Bits [FS1, FS0] – Default is [11] | | | | |
|------------------------------|-----------------------------|---|-------|--------|--------|--|
| | | 00 | 01 | 10 | 11 | |
| Frequency Configuration 1 | \mathbf{f}_{OUT} | 125 | 212.5 | 106.25 | 156.25 | |
| Custom Configuration | $\mathbf{f}_{\mathrm{OUT}}$ | Contact Abracon for customized configurations | | | | |

Default condition: Frequency select bits [FS1, FS0] are all left floated. FS1, FS0 are pulled high [11] Frequency combination and default frequency is customized upon request. Please contact Abracon for the frequency combinations.

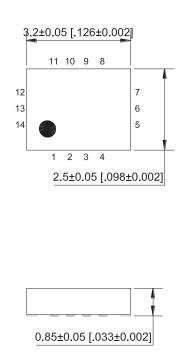


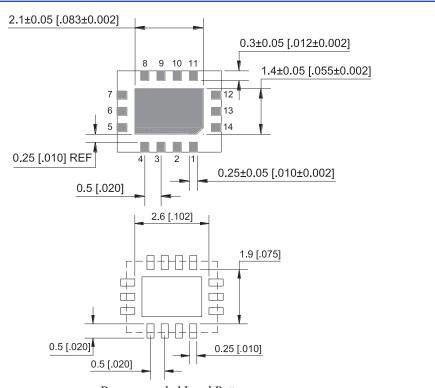


3.2 x 2.5 x 0.85 mm

ASEMCLP

MECHANICAL DIMENSIONS





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Pb

Recommended Land Pattern

| Pin No. | Pin Name | Pin Type | Description | |
|-----------|---------------|----------|---|--|
| 1 | Enable | Ι | Enables outputs when high and disables when low | |
| 2 | NC | NA | Leave unconnected or grounded | |
| 3 | NC | NA | Leave unconnected or grounded | |
| 4 | GND | Power | Ground | |
| 5 | FS0 | Ι | Least significant bit for frequency selection | |
| 6 | FS1 | Ι | Most significant bit for frequency selection | |
| 7 | NC | NA | Leave unconnected or grounded | |
| 8 | Output+ | 0 | Positive LVPECL Output | |
| 9 | Output- | 0 | Negative LVPECL Output | |
| 10 | NC | NA | Leave unconnected or grounded | |
| 11 | NC | NA | Leave unconnected or grounded | |
| 12 | VDD2 | Power | Power Supply | |
| 13 | VDD | Power | Power Supply | |
| 14 | NC | NA | Leave unconnected or grounded | |
| Dimension | s• mm (inches |) | | |

Dimensions: mm (inches)

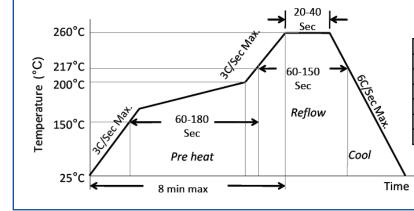




ASEMCLP

3.2 x 2.5 x 0.85 mm

REFLOW PROFILE

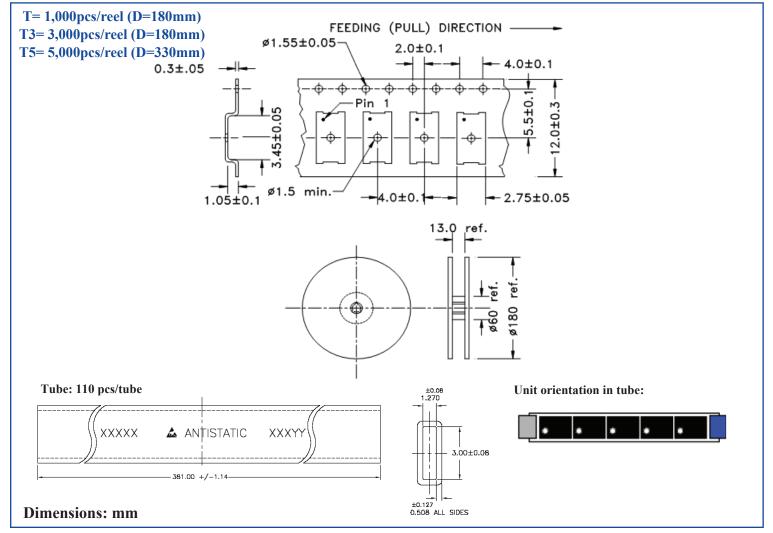


| 3°C/Sec Max. |
|--------------|
| 60-180 Sec |
| 60-150 Sec |
| 255-260°C |
| 20-40 Sec |
| 6°C/Sec Max. |
| 8 min Max. |
| |

RoHS

Compliant

TAPE & REEL:



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