

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Programmable - High Performance SMD XO & VCXO (LVPECL Output)

ASG2-P







Moisture Sensitivity Level (MSL) – 1

FEATURES:

- +2.5V or +3.3V operation
- -40°C to +85°C standard operating temperature range
- Miniature size 2.5 x 2.0 x 1.0 mm Ceramic SMT Package
- Short lead time

> **APPLICATIONS**:

- Networking
- SONET/SDH
- WiMax / WLAN
- Computing
- Phase Locked Loops
- Direct Digital Synthesis (DDS)
- DSL/ADSL
- Base Terminal Stations

> STANDARD SPECIFICATIONS:

Parameters		Minimum	Typical	Maximum	Units	Notes	
Frequency Range			8	v -	1500	MHz	
Operating Temperature			-40		+85	°C	
Storage Temperature			-55		+125	°C	
Overall Frequency Stability			-50		+50	ppm	See Note # 1
Initial Tolerance + Stability over operating temperature			-35.00		+35.00	ppm	
Supply Voltage (Vdd)		$V_{dd} = 3.3V$	3.135	3.300	3.465	V	
		$V_{dd} = 2.5V$	2.375	2.500	2.625	V	
Input Current				65	mA	Frequency dependent	
LVPECL Output (OUT & OUT)	Output High Voltage		V _{dd} - 1.025			V	50Ω nominal load
	Output Low Voltage				$V_{dd}-1.62$	V	50Ω nominal load
	Duty Cycle		45		55	%	@Vdd-1.3V
	Rise Time				600	ps	80%/20%
	Fall Time				600	ps	80%/20%
Enable/Disable Function :		"1" ($V_{IH} \ge 0.7*Vdd$) or Open: Oscillation "0" ($V_{IL} < 0.3*Vdd$) : High Z					
Control Voltage Range			0		Vdd	V	For VCXO Only
Absolute Pull Range			±35			ppm	For VCXO Only
Control Port Bandwidth		10			kHz	For VCXO Only	
Linearity				15	%	For VCXO Only	
Slope		Positive				For VCXO Only	
Phase jitter RMS (12kHz to 20MHz offset)			1.0	1.8	ps	See Note #2	

Note #1: Inclusive of initial tolerance at 25°C±3°C, operating temperature range, input voltage variation, load variation & 15 years aging at 25°C.

Note #2: The rms jitter integrated over 12kHz to 20MHz Bandwidth is dependent on the carrier and whether or not the final frequency is achieved without engaging the Fractional Mode





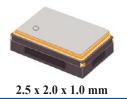
Programmable - High Performance

SMD XO & VCXO (LVPECL Output)

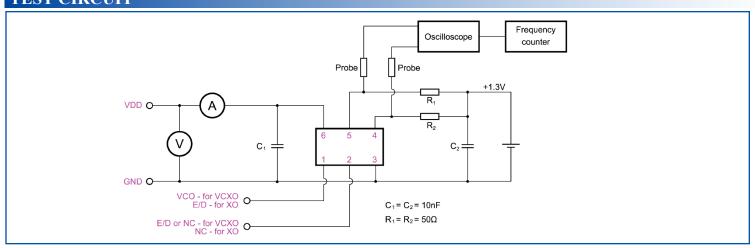




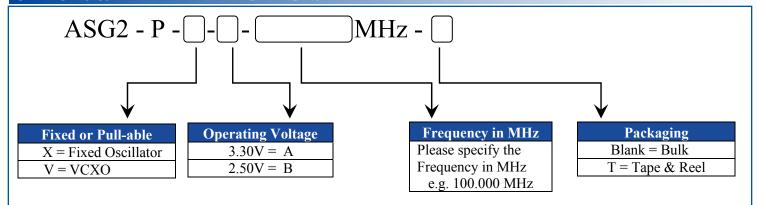




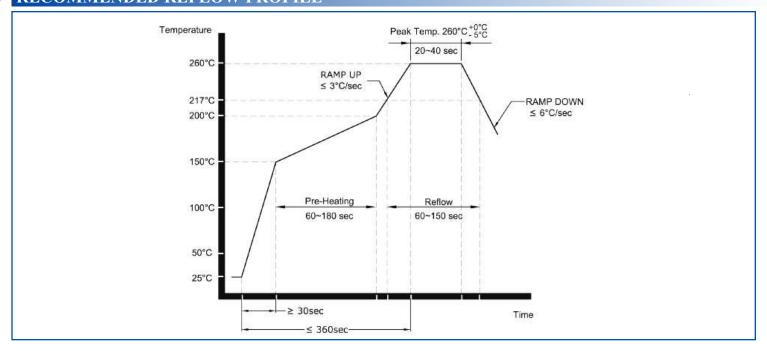




> OPTIONS & PART IDENTIFICATION:



RECOMMENDED REFLOW PROFILE







SMD XO & VCXO (LVPECL Output)







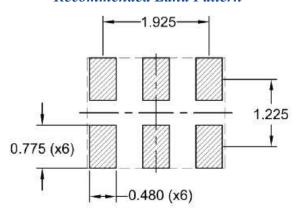


OUTLINE DIMENSION:

Pin#	Pin Description For VCXO configuration				
1	Voltage Control for VCXO				
2	Output Enable (OE) <i>or</i> No Connect (N/C)				
3	GND				
4	RF Output				
5	Complimentary Output				
6	Vdd				

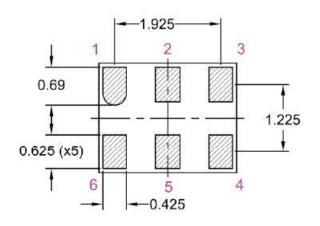
Pin #	Pin Description For XO configuration				
1	Output Enable (OE)				
2	No Connect (N/C) for XO				
3	GND				
4	RF Output				
5	Complimentary Output				
6	Vdd				

Recommended Land Pattern

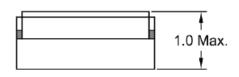


Top View 2.5±0.2 2.0±0.2

Bottom View



Side View



Dimensions: mm





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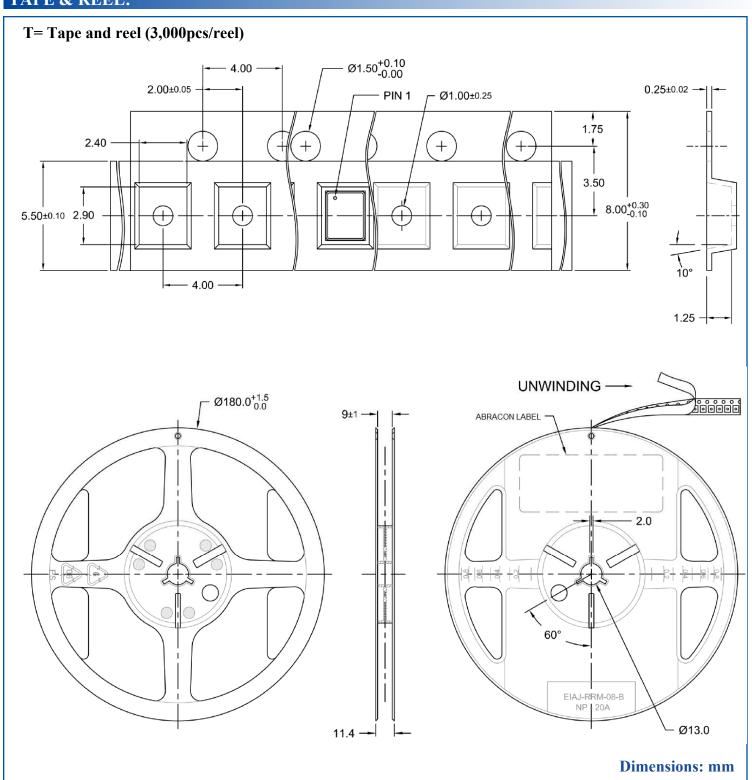
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TAPE & REEL:



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