



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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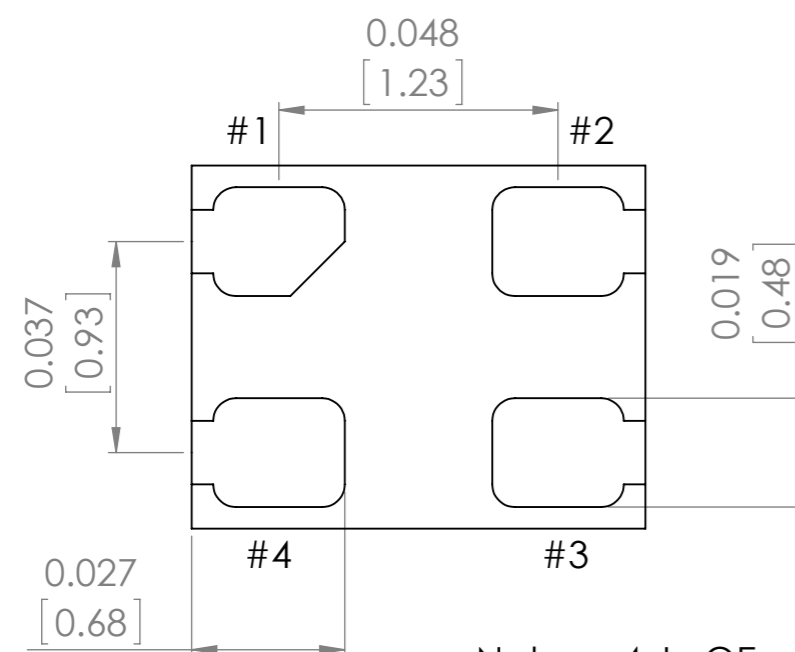
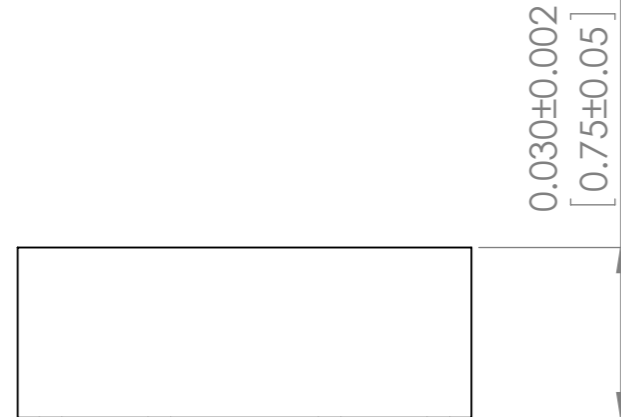
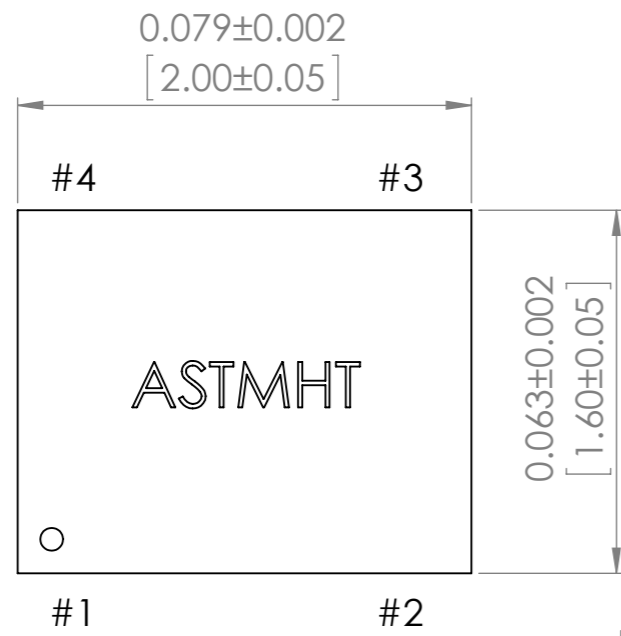
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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

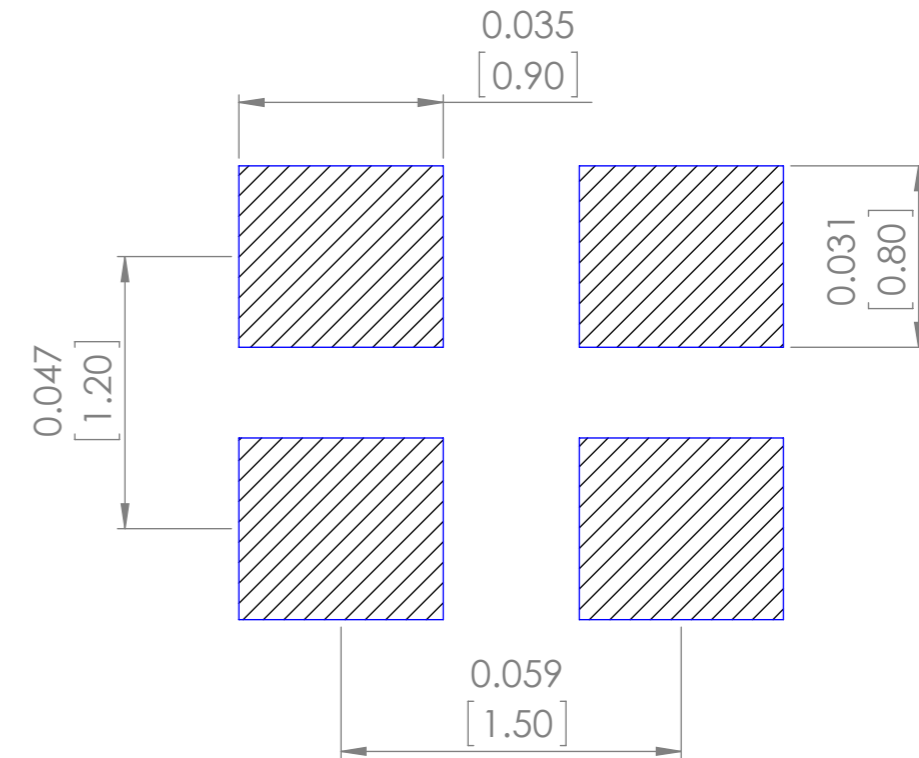


2.0 x 1.6mm Package (Option "A")

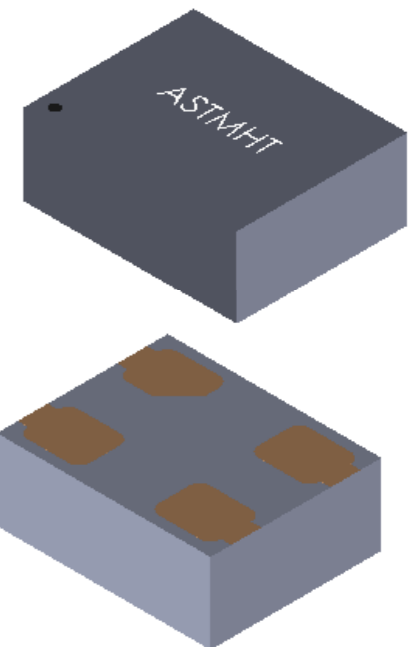


- Notes:
- In OE or \overline{ST} mode, a pull-up resistor of 10k Ω or less between OE/ \overline{ST} pin and Vdd is recommended if pin 1 is not externally driven. If pin 1 needs to be left floating, use the NC option.
 - A capacitor value of 0.1 μ F or higher between Vdd and GND is required.

Recommended Land Pattern



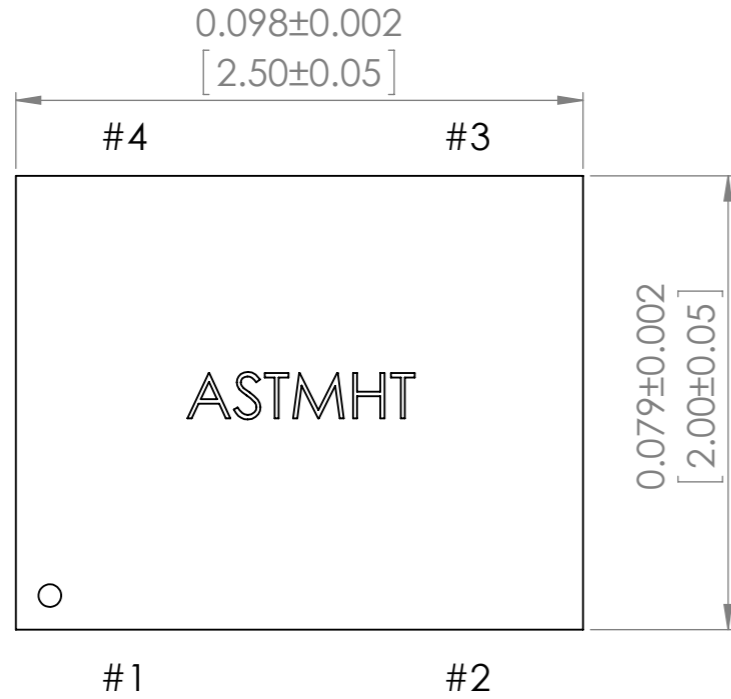
Pin	Name	Functionality	
1	Output Enable	H or Open (4): specified frequency output L: output is high impedance. Only output driver is disabled	
	Standby	H or Open (4): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.	
	No Connect	Any voltage between 0 and Vdd or open (4): specified frequency. pin 1 has no function	
2	GND	Power	Electrical ground (5)
3	Out	Output	Oscillator clock output
4	Vdd	Power	Power supply voltage (5)



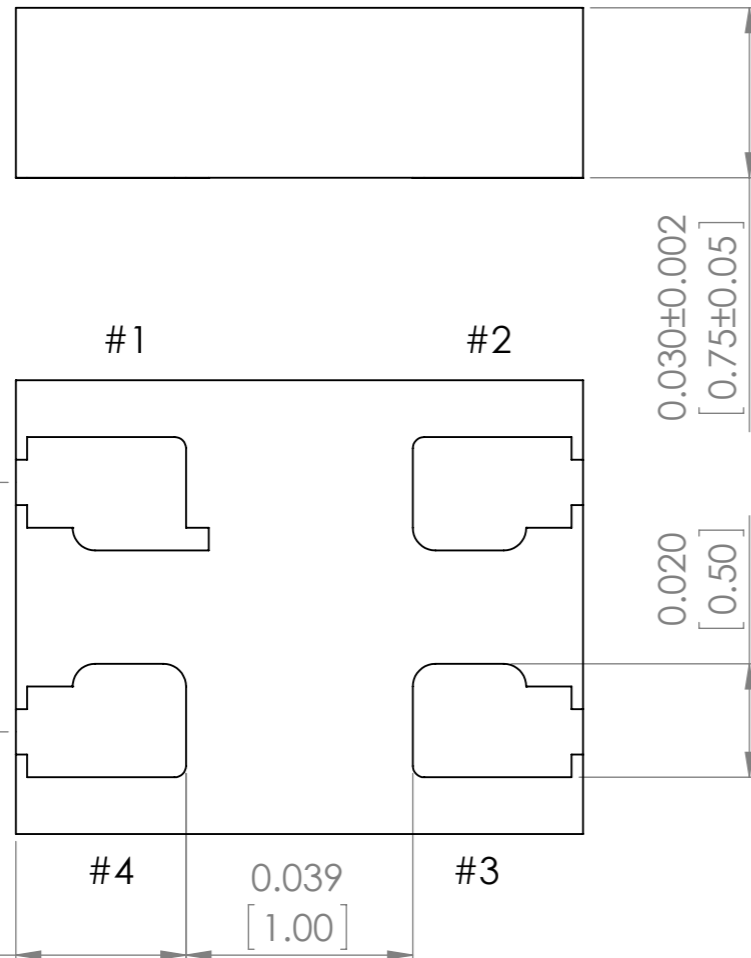
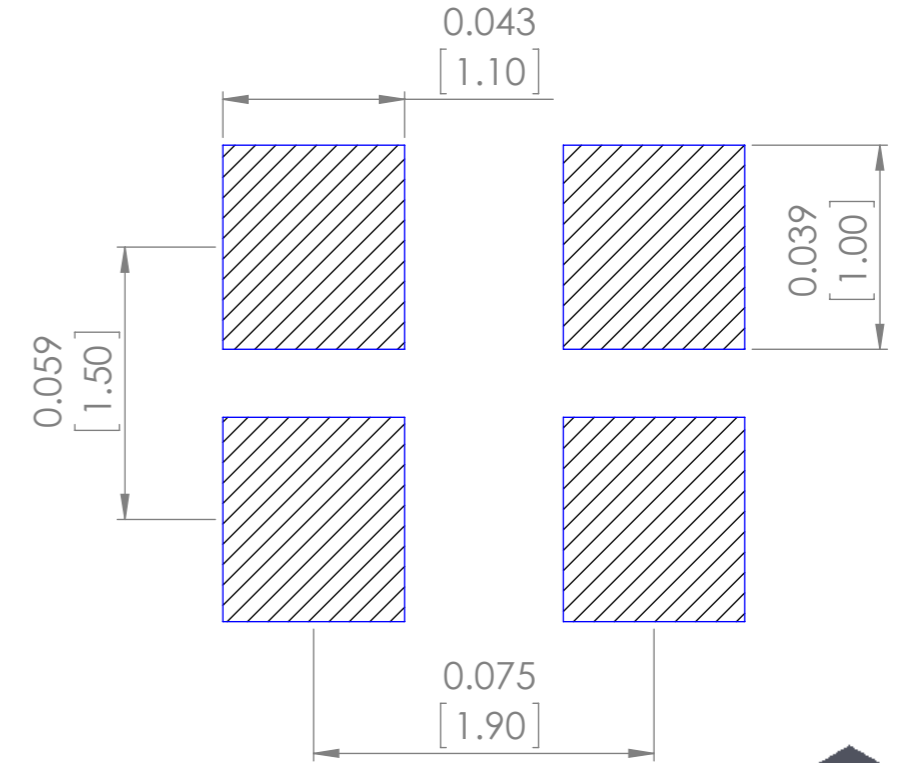
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TITLE:			
DWG NO.	ASTMHT		A3
SCALE:30:1	SHEET 1 OF 5		

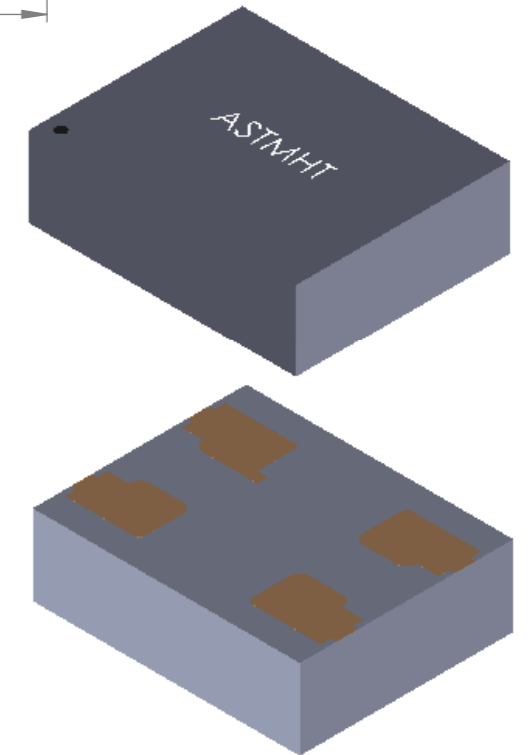
2.5 x 2.0mm Package (Option "D")




Recommended Land Pattern



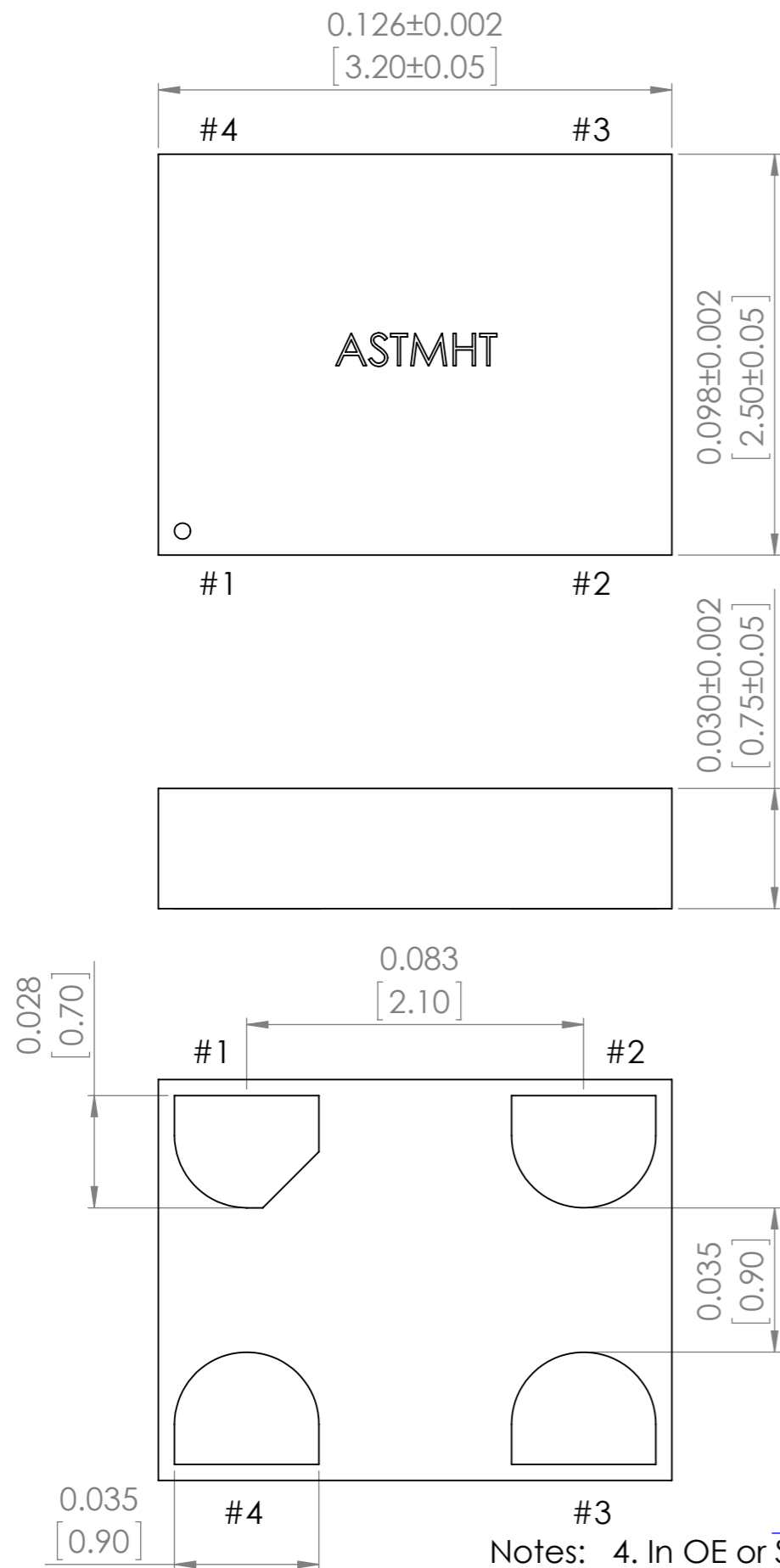
Pin	Name	Functionality
1	Output Enable	H or Open (4): specified frequency output L: output is high impedance. Only output driver is disabled
	Standby	H or Open (4): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
	No Connect	Any voltage between 0 and Vdd or open (4): specified frequency. pin 1 has no function
2	GND	Power Electrical ground (5)
3	Out	Output Oscillator clock output
4	Vdd	Power Power supply voltage (5)



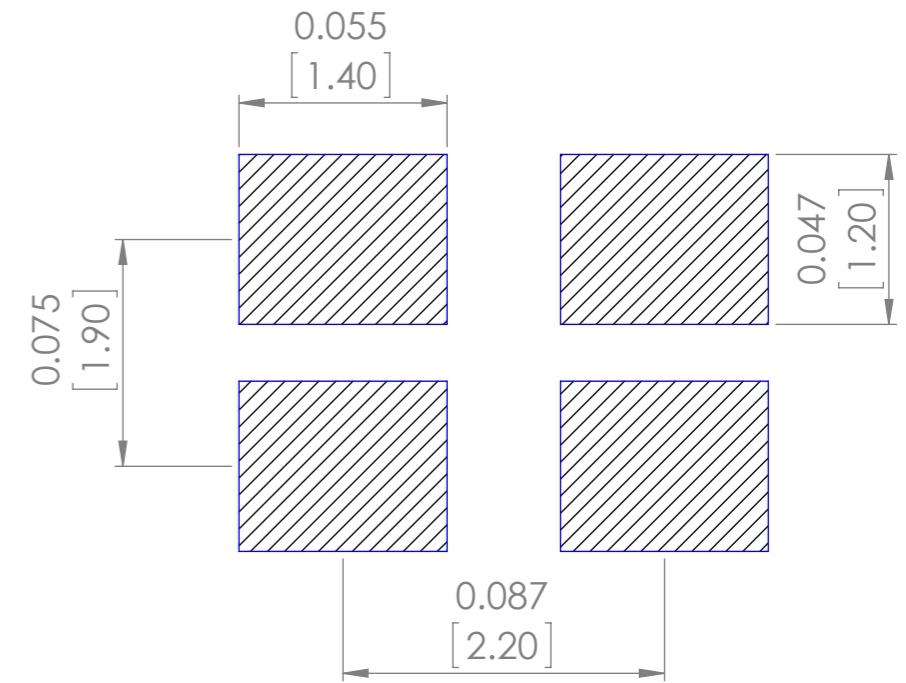
Notes: 4. In OE or \overline{ST} mode, a pull-up resistor of 10k Ω or less between OE/ \overline{ST} pin and Vdd is recommended if pin 1 is not externally driven. If pin 1 needs to be left floating, use the NC option.
5. A capacitor value of 0.1 μ F or higher between Vdd and GND is required.

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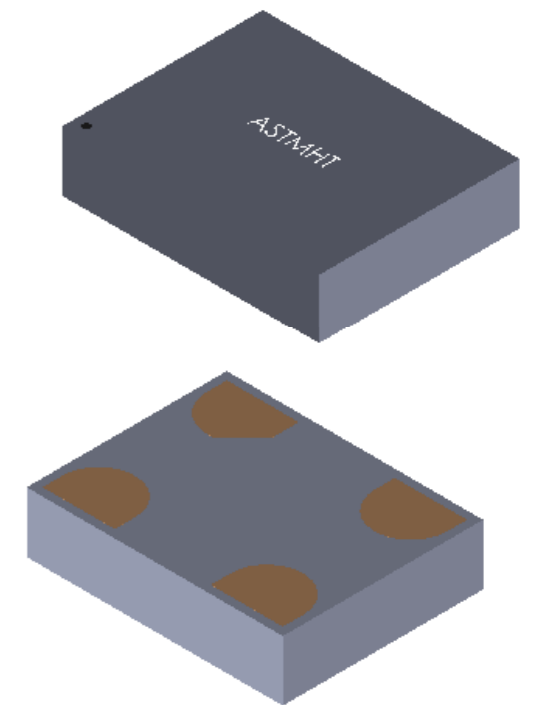
3.2 x 2.5mm Package (Option "E")




Recommended Land Pattern



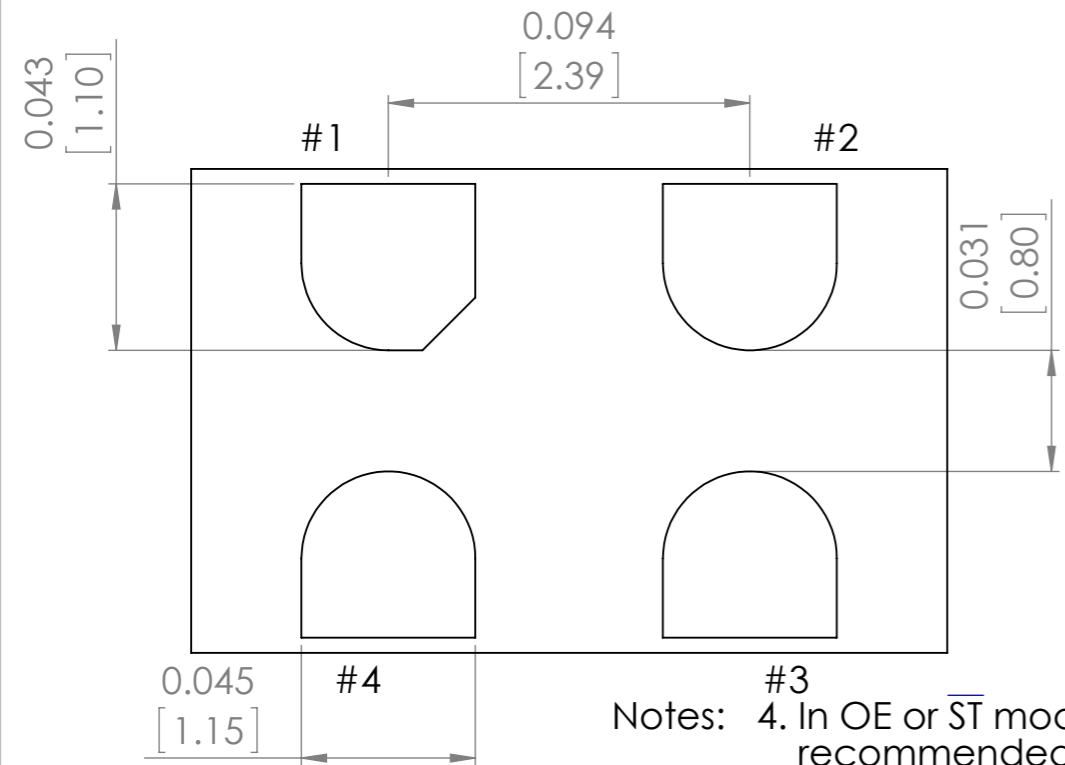
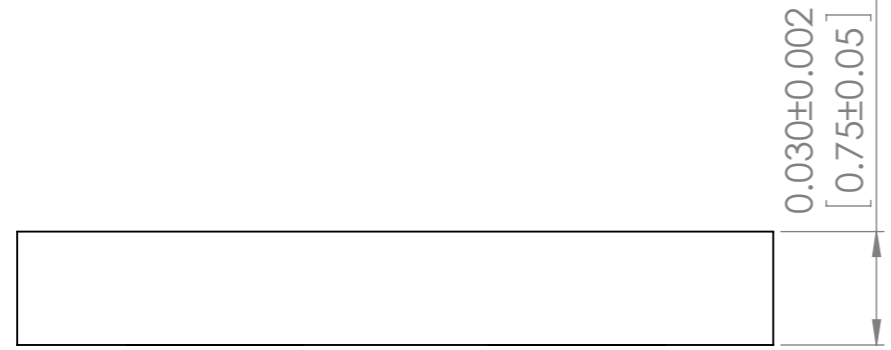
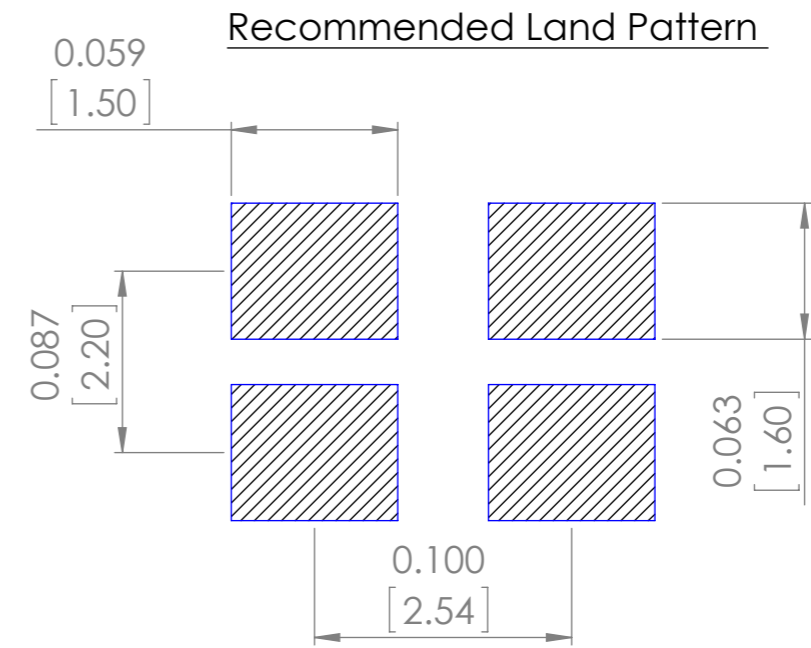
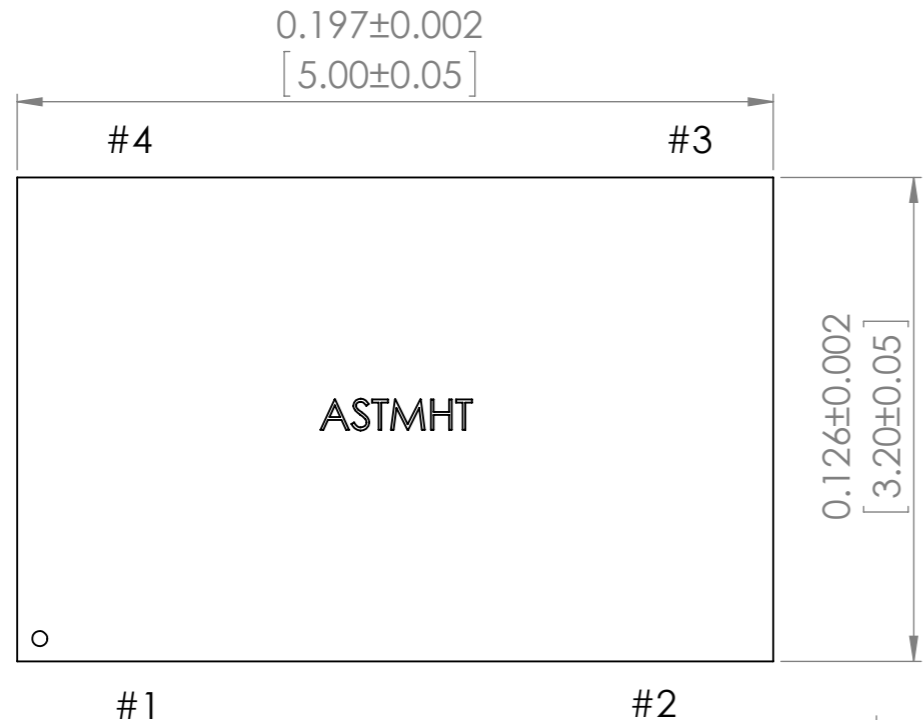
Pin	Name	Functionality
1	OE/ST	Output Enable H or Open (4): specified frequency output L: output is high impedance. Only output driver is disabled
	Standby	H or Open (4): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
	No Connect	Any voltage between 0 and Vdd or open (4): specified frequency. pin 1 has no function
2	GND	Power Electrical ground (5)
3	Out	Output Oscillator clock output
4	Vdd	Power Power supply voltage (5)



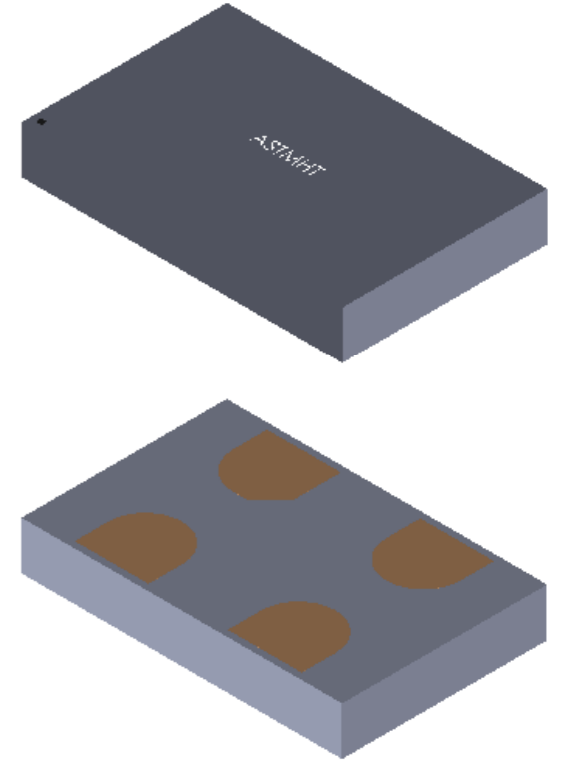
Notes: 4. In OE or ST mode, a pull-up resistor of 10kΩ or less between OE/ST pin and Vdd is recommended if pin 1 is not externally driven. If pin 1 needs to be left floating, use the NC option.
5. A capacitor value of 0.1μF or higher between Vdd and GND is required.

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SCALE:25:1		SHEET 3 OF 5	

5.0 x 3.2mm Package (Option "FL")



Pin	Name	Functionality
1	Output Enable	H or Open (4): specified frequency output L: output is high impedance. Only output driver is disabled
	Standby	H or Open (4): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
	No Connect	Any voltage between 0 and Vdd or open (4): specified frequency. pin 1 has no function
2	GND	Power Electrical ground (5)
3	Out	Output Oscillator clock output
4	Vdd	Power Power supply voltage (5)



Notes: 4. In OE or ST mode, a pull-up resistor of 10kΩ or less between OE/ST pin and Vdd is recommended if pin 1 is not externally driven. If pin 1 needs to be left floating, use the NC option.
5. A capacitor value of 0.1μF or higher between Vdd and GND is required.

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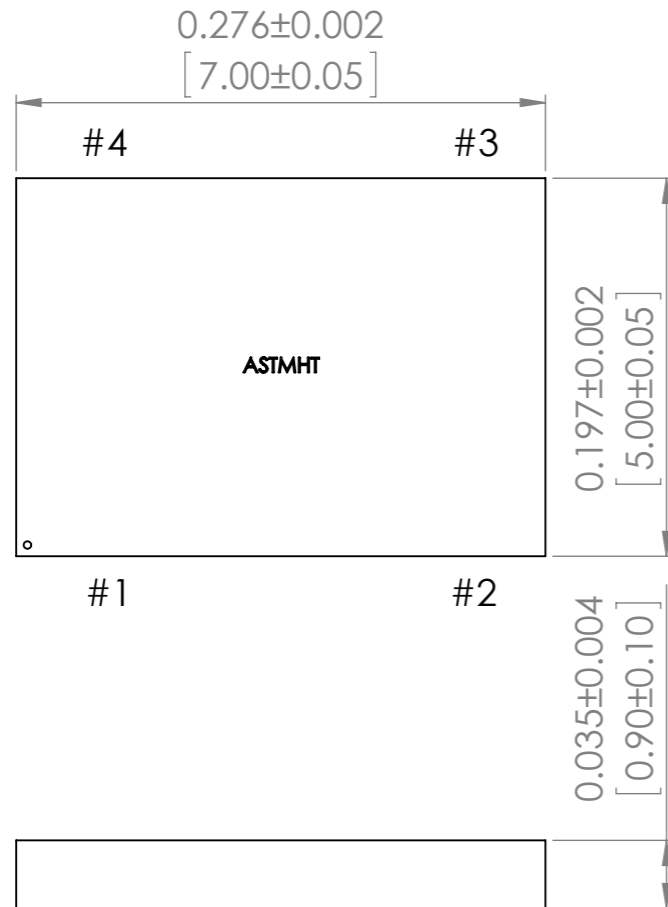
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TITLE: ASTMHT SHEET 4 OF 5

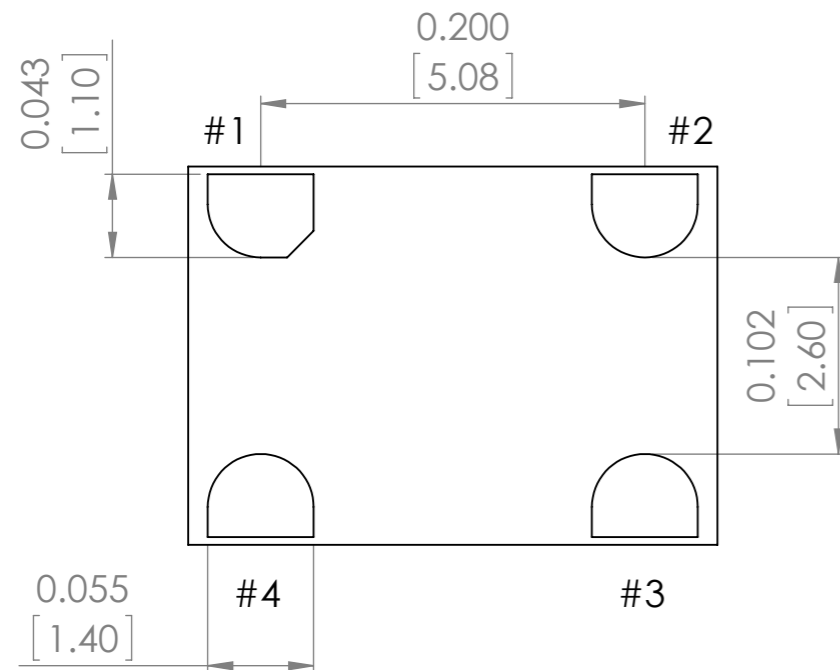
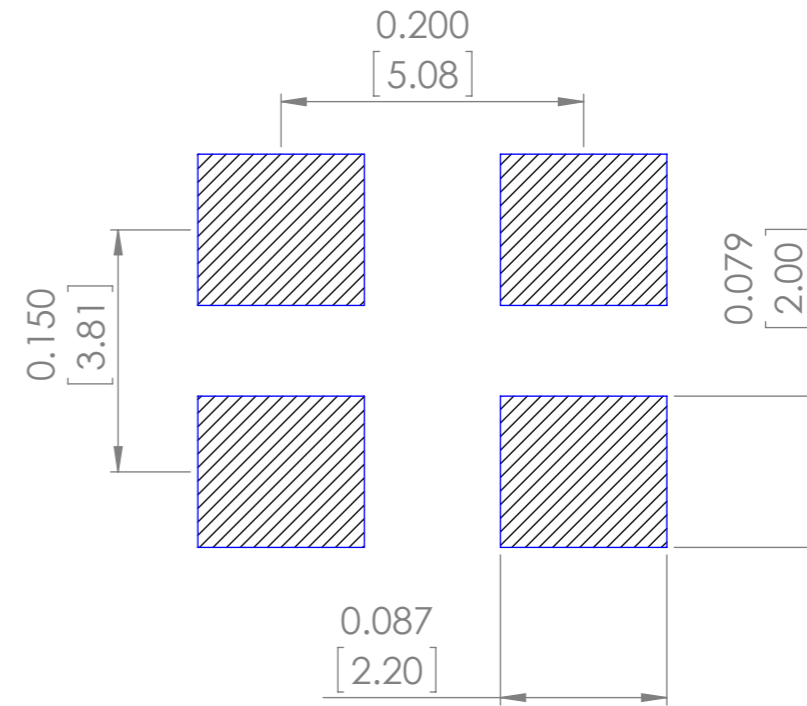
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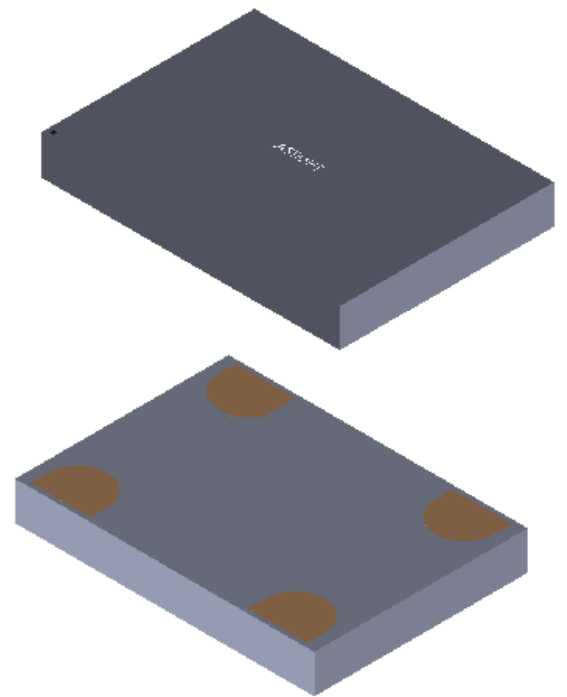
7.0 x 5.0mm Package (Option "V")



Recommended Land Pattern




Pin	Name	Functionality	
1	Output Enable	H or Open (4): specified frequency output L: output is high impedance. Only output driver is disabled	
	Standby	H or Open (4): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.	
	No Connect	Any voltage between 0 and Vdd or open (4): specified frequency. pin 1 has no function	
2	GND	Power	Electrical ground (5)
3	Out	Output	Oscillator clock output
4	Vdd	Power	Power supply voltage (5)



- Notes:
- In OE or \overline{ST} mode, a pull-up resistor of 10k Ω or less between OE/ \overline{ST} pin and Vdd is recommended if pin 1 is not externally driven. If pin 1 needs to be left floating, use the NC option.
 - A capacitor value of 0.1 μ F or higher between Vdd and GND is required.

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		A3	
SCALE:10:1		SHEET 5 OF 5	