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Features

- 300 mA/1.9V/2.5V DC to DC for Co-processor Core
- 80 mA/2.8V Dual-mode LDO for Memories (LDO1)
- 130 mA/2.7V/2.8V LDO for Camera Module (LDO2)
- 130 mA/2.8V LDO for Analog Section Supply of Audio Stereo Codec (LDO6)
- 10 mA/1.8V/2.8V LDO for Digital Section Supply of Audio Stereo Codec (LDO7)
- 130 mA/2.8V LDO for Analog Section Supply of Bluetooth® Module (LDO4)
- 130 mA/2.8V LDO for Digital Section Supply of Bluetooth® Module (LDO5)
- 2 mA/2.4V/2.7V LDO for Low-power Device Control (LDO3)
- Open Drain Switch
- Three-channel Level Shifters
- LED Driver
- 0.5 mA/1.5V Bufferized Voltage Reference
- Power Management Start-up Controller and Reset Generation
- Over- and Under-voltage Protections
- Over-temperature Protection
- Shutdown, Sleep and Enable Modes
- Straightforward and Easy Interfacing to any Baseband Controller
- Small 5 mm x 5 mm, 49-ball BGA Package

Description

The AT73C204 device provides an integrated power management solution for the add-on multimedia features in new-generation mobile phones. These features include a camera module, sound system for polyphonic ringing tones, memory module for downloaded MP3 files, Bluetooth module for cordless headset, etc. The most common approach to the IC architecture of these new-generation mobile phones is a baseband processor for the basic telephony functions and a separate co-processor for the multimedia features. Atmel proposes the AT73C202 for power management of the baseband processor and RF elements, and the AT73C204 for power management of the multimedia features.

The AT73C204 is suitable for any telecommunications standard: GSM/GPRS, PDS, CDMA, CDMA2000, WCDMA or UMTS. It is packaged in a small form-factor 49-ball 5 mm x 5 mm BGA package.



Power Management for Mobiles (PM)

AT73C204

Summary

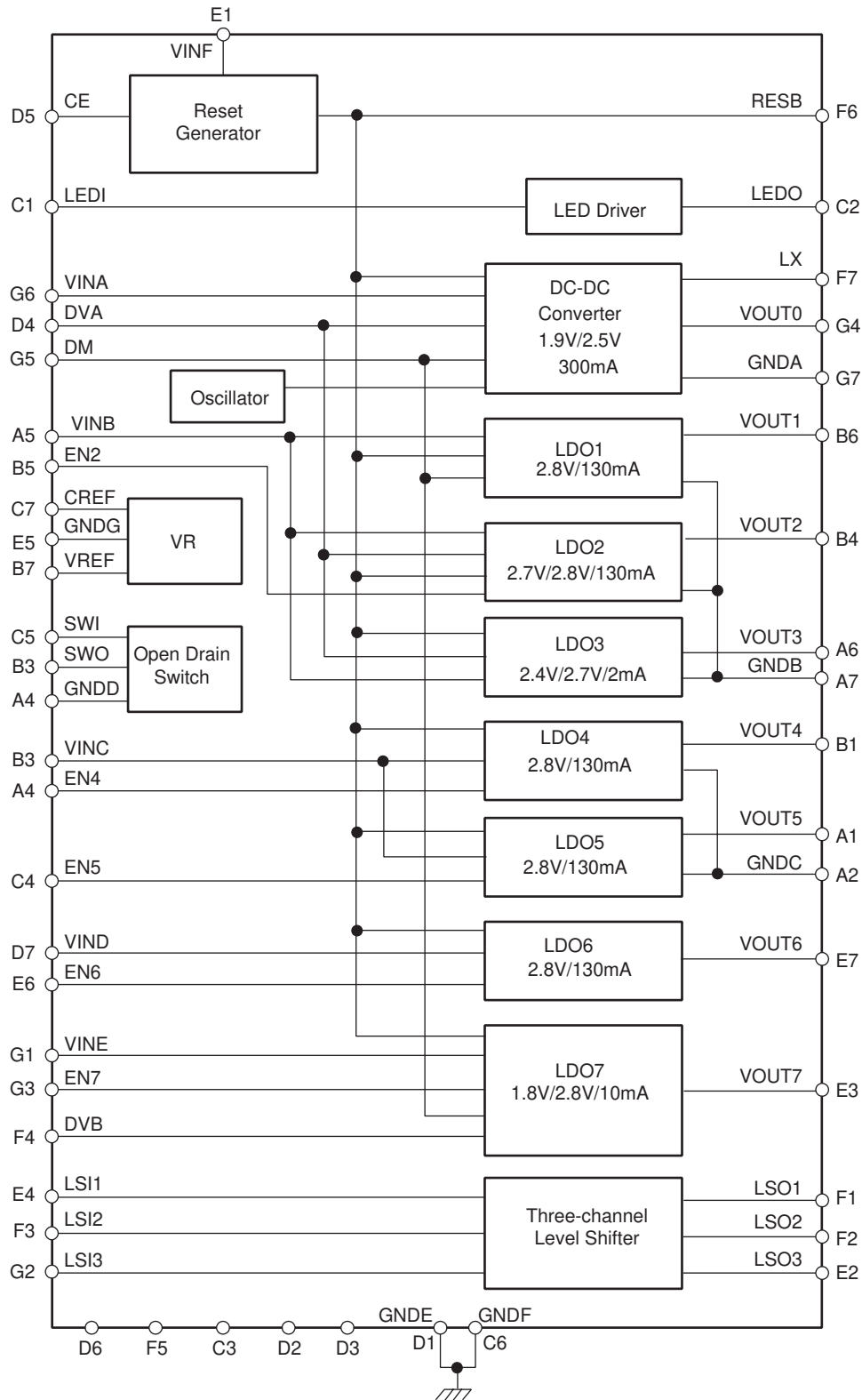
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Note: This is a summary document. A complete document is available on our Web site at www.atmel.com.

Functional Block Diagram

Figure 1. AT73C204 Functional Block Diagram





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