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User Guide



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Section 1 Introduction

Figure 1-1. ATICE10



Congratulations on your purchase of the ATICE10 AVR[®] In-Circuit Emulator (ICE). ICE10^M is a high-end emulator designed to provide a complete, and easy to use, development and debug environment for the AVR Flash microcontrollers from Atmel Corporation.

1.1	About this Manual	This user guide serves as a reference manual for the Atmel AVR ICE10 in-circuit emula- tor. The AVR ICE10 User Guide is an easy introduction on how to use the ICE10, and a detailed reference for advanced users. Throughout the manual, many references to the AVR microcontrollers are made in short form, i.e. AT90S2313 is referred to as S2313 and so on.
1.2	General Description	The Atmel AVR ICE10 is a real-time in-circuit emulator for a wide range of AVR devices. The ICE10 is controlled by AVR Studio [®] , version 3.5 or later.
1.3	ICE10 Features	Devices Supported: ATtiny11, ATtiny12, ATtiny15, ATtiny22, ATtiny28 AT90S1200, AT90S2313, AT90S/LS2323, AT90S/LS2343, AT90S/LS2333, AT90S/LS4433, AT90S4414, AT90S8515, AT90S/LS4434, AT90S/LS8535
		Emulates all On-Chip Functions, both Digital and Analog
		■ Trace Buffer (32K x 96-bit)

1-1

		 Unlimited Number of Breakpoints Full Visibility of and Access to Register File, SP, PC and Memories Access to all I/O Registers Logic Analyzer Interface Output Supports Assembler and C Source Level Debugging 5 Trigger Outputs 5 Trigger Number
		 Internal and External Clock Options
		External Data Memory Emulation
		■ 2.7 - 6.0V Operating Voltage
		Software Upgradable for Future AVR Devices
1.4	ICE10 Contents	The ATICE10 Contains the Following Items: ■ ICE10 In-Circuit Emulator Unit
		Pod Card ATtiny15POD with Cables
		Pod Card AT90ADCPOD with Cables
		■ RS-232 Cable
		Universal Voltage Power Supply (100 - 240V, 50 - 60 Hz)
		American Power Cable
		European Power Cable
		Atmel CD-ROM Containing Software
		■ ATICE10 User Guide
		Warning Note
1.5	System Requirements	For the ICE10 to operate correctly and trouble free, the following software and hardware requirements should be met.
1.5.1	Hardware Requirements	Pentium-class personal computer with: ■ 32 MB RAM
		20 MB free hard disk space
		 CD-ROM or Internet access (for software and databooks)
		VGA monitor
		19200 bps RS-232 port (COM port)
1.5.2	Software Requirements	The following operating systems are currently supported by AVR Studio: ■ Windows [®] 95
		■ Windows 98 (SE) (ME)
		■ Windows NT [®] 3.51
		Windows NT 4.0
		Windows 2000
1.5.3	Operating	■ Operation Temperature: 0°C - 70°C
	· ····	



1.5.4 Host Interface

- RS-232C @ 19200 bps, N81
- 9-pin female connector



Introduction





Section 2

Preparing the ICE10 System for Use

For successful operation, the ICE10 must be connected and configured correctly. This section explains how to connect the system, and how to determine which Pod to use to successfully emulate a specific AVR part.

2.1 General Hardware Description

Figure 2-1 shows a simplified block diagram of the ICE10 connected to a target board. Once the emulator is connected and configured correctly, it will behave like the emulated device, and allows easy prototyping and debugging of applications in real-time.

Figure 2-1. ICE10 Connected to Target Board



Figure 2-2. ICE10 Emulator Unit



The ICE10 emulator unit (Figure 2-2) contains the necessary logic to emulate all digital functions of the emulated AVR. It also contain hardware needed to communicate with

AVR Studio. Configuration of this device is controlled and done directly from AVR Studio.





Figure 2-3 shows the ATtiny15POD which should be used when emulating the ATtiny15 device. The Pod provides a buffer protecting the emulator unit. In addition, the ATtiny15POD contains a 4-channel 10-bit A/D converter with one differential input with optional gain stage. This Pod is configured from AVR Studio, no manual jumper settings are required.





Figure 2-4 shows the AT90ADCPOD which should be used for a wide range of AVR devices. This Pod must be configured manually with jumpers for correctly operation. Configuration of the AT90ADCPOD is described in Section 4, and is also available as an interactive configuration walkthrough in the on-line AVR Studio help system.

There are different Probe cables supplied with the ICE10 emulator. Only one pod, and one probe cable should be used at any given time, and the pin count and shape of the probe should match the device being emulated.

When using the ATtiny15POD only, the 8-pin DIP Probe can be used. When Using the AT90ADCPOD, use the appropriate Probe for the device being emulated.



2.2 Connecting The ICE10 connects to any PC through a standard RS-232 port (COM port). Connect ICE10 to PC the RS-232 Cable between the ICE10 and any free COM port on the PC. AVR Studio will automatically search through all available COM ports and detect the emulator. *Note:* If there are other devices taking control over the COM ports, these have to be shut down before starting AVR Studio. AVR Studio cannot force control over a COM port if other resources have control of the port (e.g., Modem, IrDA, PDA, etc.) 2.3 Connecting Depending on which AVR microcontroller should be emulated, either the ATtiny15POD

or the AT90ADCPOD should be connected to the Pod connector on the ICE10. Table 2-ICE10 to Target 1 shows which Pod to use.

Table 2-1. Pod Selection

POD	Supported AVR Microcontrollers
ATtiny15POD	ATtiny15
AT90ADCPOD	ATtiny11, ATtiny12, ATtiny22, ATtiny28 AT90S1200, AT90S2313, AT90S/LS2323, AT90S/LS2343, AT90S/LS2333, AT90S/LS4433, AT90S4414, AT90S8515, AT90S/LS4434, AT90S/LS8535

In short, the ATtiny15POD should be used for ATtiny15 only. The AT90ADCPOD is used for all other listed devices.

Jumper Settings 2.4 The configuration system on the ATtiny15POD and AT90ADCPOD is different. The AT90ADCPOD uses jumpers that need to be placed manually on the Pod. The ATtiny15POD uses a jumperless system where all configuration is done directly from AVR Studio. For in-depth information about AT90ADCPOD jumper settings see Section 4.

2.5 Connecting The ICE10 system has an internal power regulator that delivers 15W at 5V. The ICE10 itself uses about 10W. The power supply delivered with the ICE10 is dimensioned to Power meet the requirements of the emulator. If another power supply is used, it should supply a voltage between 9 and 15 VDC and a minimum of 20W. The battery eliminator connector on the ICE10 system is a standard type with 2.1 mm center tap. Ground should

> be connected to the center tap. *Note:* The target application power must not be present when the emulator is turned off, as this may cause damage to the pod.

2.6 Summary

Board

Complete the following procedure in order to start using the ICE10. Before connecting the probe cable to the user application:

- Connect the RS-232 cable between the ICE10 unit and the PC serial port.
- Connect the correct pod card to the ICE10 unit with the supplied pod cable.
- Connect the correct probe cable to the pod.
- Make sure that the pod settings (jumpers) on AT90ADCPOD are set according to the requirements.
- Connect the enclosed power supply (9 15 VDC) to the ICE10 unit.
- Turn on the power and check that the red LED marked POWER is lit.
- After a short time (<10 s), the green LED marked READY will be lit and the ICE10</p> system will be ready.



- Turn off the power.
- Plug the probe into the application/adapter. Note: The target power should under no circumstance be present when the probe is connected and the emulator is switched off. Pay attention to connect with the correct orientation. If it is not connected correctly, the ICE10 system may be damaged.
- Turn on the power on ICE10.
- Connect power to the target application.
- Start AVR Studio.





Section 3 Using the ICE10

		This section will cover main features and considerations when using the ICE10 and AVR Studio to emulate AVR devices. AVR Studio is a professional front-end for both high- level and assembly level debugging. If no In-Circuit Emulator is connected AVR Studio will start up as a stand-alone simulator. AVR Studio is described in the on-line help sys- tem in AVR Studio, and should be studied carefully in order to take full advantage of all available options and features.
3.1	Installing AVR Studio	To install AVR Studio insert the supplied Atmel CD-ROM in the computer and navigate to "Products -> AVR 8-bit RISC -> Software". Right click with the mouse on the "astudio3.exe" file and select "save link as". Select an empty directory and save the file. Execute the "astudio3.exe" file. This is a self-extracting file that will extract all required files to the current directory. Execute the "Setup.exe" file. This will guide you through the setup process. Note: AVR Studio version 3.5 or later is required for ICE10 support.
3.2	Emulating with ICE10	It is assumed that the reader has general knowledge of how to use the AVR Studio soft- ware. This section will focus on features and considerations applicable to the ATICE10 emulator.
3.2.1	Initial Setup	Before emulating with the ICE10, the emulator, pod and probe must be connected and configured correctly. This is described in Section 2.2 to Section 2.6 and Section 4.
3.2.2	Starting AVR Studio	 When the system is correctly connected and powered up, AVR Studio should be launched. AVR Studio will look for any supported tool connected to the COM ports. Note that AVR Studio searches through the COM ports in a sequential manner. If other Atmel tools are connected to the COM ports, make sure that these are switched off, or disconnected, as AVR Studio will look for any supported tool, and connect to the first tool it finds. <i>Note:</i> Make sure no other applications have control of the COM port that the ICE10 is connected to.
3.3	ICE10 Emulator Options	When the emulator is started with a new project the "Emulator Options" dialog will appear. This dialog determines how the emulator behaves. Note: The emulator options dialog box (Options \rightarrow Emulator Options) will not be available before a project has been loaded and the emulator has been detected by AVR Studio.

Using the ICE10

3.3.1	Device	Depending on which device is selected, unavailable options will be grayed out. A grayed out option indicates that this option is not applicable for the selected device. The ATtiny15POD is completely configured by this emulator options dialog. The AT90ADCPOD requires additional jumper settings to complete the configuration as described in Section 4.
3.3.2	Clock Source	When using the AT90ADCPOD three clock source options are available. The AT90ADCPOD can use one out of three available clock sources; the programmable internal clock in the ICE10, a crystal or an external oscillator in the user application. The ATtiny15POD only uses the internal clock option.
3.3.3	Internal Frequency	The internal clock can be adjusted between 400 kHz and 20 MHz. Any frequency within this range can be selected, and will be produced with an accuracy better than 200 PPM for most frequencies. In addition jumpers needs to be configured on the AT90ADCPOD as described in Section 4.
3.3.4	External Range	 When external clock source in the user application is used, this must be in the range between 32.768 kHz and 10 MHz. To use an external clock source, select External Oscillator in the menu. When using external crystal, it is important to select the proper range in the External Range menu to make the clock system work properly. <i>Note:</i> Make sure the jumper settings on the AT90ADCPOD are set according to the options selected in the Emulator Options menu.
3.4	Emulator	The ICE10 has room to store 6 different devices in the Emulator unit Flash memory.

EmulatorThe ICE10 has room to store 6 different devices in the Emulator unit Flash memory.ConfigurationUsing the AVR Emulator Configuration system found in AVR Studio (Tools -> ICEPRO/
AVRICE/ICE10 Configuration system), it is possible to select which device that should
be downloaded to the emulator. Only devices downloaded to the emulator can be emu-
lated. Figure 3-1 shows the AVR Configuration System dialog box where devices can be
added or removed from the ICE unit.

Figure 3-1. AVR Configuration System Dialog



The left side column shows which devices are supported by the emulator, but not currently loaded. To be able to emulate one of these devices, the appropriate device should be marked, and the "Add" button should be pressed.

Note: If 6 devices are already stored in the emulator, remove the appropriate number of devices before trying to add more.

Once all wanted devices are listed in the right column press "Download" to start the actual update of the ICE10. This might take a few minutes, and the green LED will flash during the update. A dialog box will appear at the end of the update giving a confirmation that the update was successful.

Note: The update will not take effect util the next time the emulator is turned on.



3.5 Using Breakpoint ICE10 supports an unlimited number of breakpoints. Breakpoints can be placed directly in the source code. When running the code, execution will be halted **before** executing the code line with the breakpoint. ICE10 does not support advanced or complex breakpoints. For in-depth description of breakpoints and how to use them, see the on-line help system in AVR Studio.

3.6 Using Traces The ICE10 has a 32K x 96-bit trace buffer that stores information about program execution for every clock cycle. When the emulator is stopped, this trace buffer can be examined to extract information about the history of the emulated program. The details on which data are stored and how to retrieve them are described in the "AVR Studio Online Help." When the trace buffer is full, it will wrap around and start overwriting the oldest entries.

The trace buffer can be turned on or off at any program line. This makes it possible to skip tracing delay loops and other subroutines which would otherwise fill the trace memory with unnecessary data. The trace buffer is inactive by default. To trace an entire program, a **Trace on** marker should be placed on the first line of the program.

For in-depth description of Traces and how to understand the contents of the trace buffer, see the on-line help system in AVR Studio.

3.7 Using Triggers The ICE10 has five external trigger inputs and five trigger outputs, all located on the Aux connector next to the Pod connector. The pinout is shown in Figure 3-2 and Table 3-1.

- The trigger inputs can act as break signals to the emulator and/or they can be logged in the trace buffer. Any inputs set up to break the emulator are activated when a rising edge is detected.
- The trigger outputs may be set as trigger points on any instruction in the code window in AVR Studio. If enabled on an instruction, the output(s) will remain high for one AVR clock cycle when the marked instruction is executed. This can be used to trigger a logic analyzer or an oscilloscope.

Figure 3-2. AUX Connector



Signal	Aux		Signal
GND	Pin 1	Pin 2	GND
Input 0	Pin 3	Pin 4	Output 0
Input 1	Pin 5	Pin 6	Output 1
Input 2	Pin 7	Pin 8	Output 2
Input 3	Pin 9	Pin 10	Output 3
Input 4	Pin 11	Pin 12	Output 4
GND	Pin 13	Pin 14	GND

Table 3-1. Pinout for Aux Connector

There are three global mask registers that are used to control the behavior of the triggers:

- The Trigger Output Global Mask Register controls which of the output pins are allowed to be controlled by the trigger settings in the code. An output pin that is disabled will remain low even if a trigger point for that particular pin is set in the code.
- The Trigger Input Global Mask Register controls which of the input pins are allowed to break the emulator. If more than one line is enabled, the emulator will break on either one, but will not store any information about which input caused the event. Note that unconnected inputs are pulled high by internal pull-up resistors. Unused lines must not be enabled.
- The External Trace Mask Register controls which of the input pins will be stored in the trace memory. Input pins that are not enabled in this register will be stored as zero in the trace memory. To be traced, input signals must be valid and stable at the rising edge of the AVR clock and for 50 ns thereafter. It is also necessary that the trace buffer is enabled.

The trigger input and the external trace are two independent functions acting on the same input pins. Note that the trigger logic is asynchronous and edge driven, whereas the trace logic is clocked on the AVR clock. The emulator may therefore break on a glitch signal that is too narrow to be traced.

The details on how to enable and set up triggers and mask registers are presented in "AVR Studio On-line Help".



3.8 Logic Analyzer

ICE10 has two connectors on the front marked Logic Analyzer 1 and 2. These connectors provide signals from the instruction address and data bus. This allows users to use an external Logic Analyzer to monitor the activity on these busses.

Figure 3-3. Logic Analyzer 1 and 2 Connectors



Table 3-2. Pinout for Logic Analyzer 1

Signal	Logic Analyzer 1		Signal
AVRCLK	Pin 1	Pin 2	Low
Low	Pin 3	Pin 4	A15
A14	Pin 5	Pin 6	A13
A12	Pin 7	Pin 8	A11
A10	Pin 9	Pin 10	A9
A8	Pin 11	Pin 12	A7
A6	Pin 13	Pin 14	A5
A4	Pin 15	Pin 16	A3
A2	Pin 17	Pin 18	A1
A0	Pin 19	Pin 20	GND

Table 3-3. Pinout for Logic Analyzer 2

Signal	Logic Analyzer 2		Signal
AVRCLK	Pin 1	Pin 2	Low
Low	Pin 3	Pin 4	D15
D14	Pin 5	Pin 6	D13
D12	Pin 7	Pin 8	D11
D10	Pin 9	Pin 10	D9
D8	Pin 11	Pin 12	D7
D6	Pin 13	Pin 14	D5
D4	Pin 15	Pin 16	D3
D2	Pin 17	Pin 18	D1
D0	Pin 19	Pin 20	GND

Using the ICE10





Section 4

AT90ADCPOD Configuration

4.1 Introduction

This section describes how to set up and use the emulator pod card AT90ADCPOD. The setup procedure is also available as a walk trough guide in "AVR Studio On-line Help".

The AT90ADCPOD supports the following AVR devices:

- AT90S1200
- AT90S2313
- AT90S2323
- AT90S2333
- AT90S2343
- AT90S4414
- AT90S4433
- AT90S4434
- AT90S8515
- AT90S8535
- ATtiny11
- ATtiny12
- ATtiny22
- ATtiny28



Figure 4-1. Component Placement of AT90ADCPOD



Device	PB3/J310	PB4/J311	Jumper Setting	
AT90S2323, ATtiny11 or ATtiny12	OFF	OFF	OFF PB4 ON	
with External Crystal				
			OFF PB3 ON	
AT90S2343, ATtiny11, ATtiny12 or	OFF	ON	OFF PB4 ON	
Altiny22 with External Clock				
			OFF PB3 ON	
			• •	
AT90S2343, ATtiny11, Attiny12 or	ON	ON	OFF PB4 ON	
Al tiny22 with Internal Clock				
			OFF PB3 ON	

Table 4-1. Jumper Settings when Selecting between ATtiny11, ATtiny12, ATtiny22, AT90S2323 and AT90S2343

Note: The jumper setting does not affect operation for other devices.

ATtiny11 and ATtiny12 also have the possibility to disconnect the external RESET pin and use it as a general I/O pin (PB5). This is done by setting the jumper marked RST in position OFF. Jumper RST must always be set in position ON for all other devices.

To use AT90S2333, AT90S4433, AT90S4434, or AT90S8535, the 10-lead cable on the pod must be mounted. Table 4-2 shows how to connect this cable.

Table 4-2. Connections of 10-lead Cable

Device	10llead Cable	
AT90S2333 and AT90S4433	Connect ADC and ADC28	
AT90S4434 and AT90S8535	Connect ADC and ADC40	

Note: Connection of the 10-lead cable does not affect operation for other devices.

4.3 The ICE10 Power The ICE10 system has an internal power regulator that can deliver 15W at 5V. The System ICE10 itself uses about 10W, so if the user application is powered from the ICE10 system, it cannot use more than 5W (i.e. 1A/5V). If this value is exceeded, the ICE10 system may be damaged or not work properly. The power supply delivered with your ICE10 is dimensioned to meet the requirements of the emulator. If another power supply is used, it should supply a voltage between 9 and 15 VDC, minimum 20W. The battery eliminator connector on the ICE10 system is a standard type with 2.1 mm center tap. Center tap is negative. 4.3.1 The Target If the target application should be powered from the ICE10, the jumper named **Applications Power** "EXT.POWER" must be mounted and the jumpers labeled "PW2", "PW1" and "PW0" Requirements removed. If the target application has its own power supply, the jumper named "EXT.POWER" must be removed. The ADCPOD must also be set to convert voltages to the required voltage level. Use Table 4-3 to find the settings on the jumpers named "PW2", "PW1" and "PW0".



Targe V _{CC}	PW2	PW1	PW0
2.7 - 2.9V	(ON)	(ON)	(ON)
3.0 - 3.3V	(ON)	(ON)	• (OFF)
3.4 - 3.7V	(ON)	• (OFF)	(ON)
3.8 - 4.1V	(ON)	• (OFF)	• (OFF)
4.2 - 4.5V	• (OFF)	(ON)	(ON)
4.6 - 4.8V	• (OFF)	(ON)	• (OFF)
4.9 - 5.1V	• (OFF)	• (OFF)	(ON)
5.2 - 5.5V	• (OFF)	• (OFF)	• (OFF)

Table 4-3. Settings of PW2, PW1 and PW0

4.4 The ICE10 Clock System

ck The AVR ICE10 system can use one of three available clock sources:

- Internal programmable clock in the ICE10
- Crystal from the user application
- External oscillator from the user application

The internal clock can be adjusted between 400 kHz and 20 MHz. Any frequency within this range can be selected, and will be produced with an accuracy better than 200 PPM for most frequencies.

If an external clock source from the user application is used this can be in the range of 32.768 kHz to 10 MHz. To use an external clock source, select External Oscillator in the Emulator Options menu in AVR Studio. It is important to tune the oscillator driver by choosing the corresponding range in the Clock Range menu to make the clock system work properly with an external crystal.

On the pod card, the XTAL pins are connected to the ICE10 by using the 2-wire cable. Connect the cable from J103 to the 2-pin header nearest the used probe connector. Table 4-4 shows where to connect the 2-wire for various devices.

Table 4-4. Connections of 2-wire Cable

Device	2-wire Cable	
S1200 and S2313	Connect J103 and J305	
S4414 and S8515	Connect J103 and J306	
ATtiny11, ATtiny12, ATtiny22, S2323, and S2343	Connect J103 and J307	
S4434 and S8535	Connect J103 and J308	
ATtiny28, S4433, and S2333	Connect J103 and J309	

Pin 1 on each connector is labeled with *.

Connector J101 must be left open if the clock source from the user application is a crystal, If the clock source from the user application is an oscillator, a jumper must be mounted on connector J101. Table 4-5 shows the settings for J101. The switch S101 must be in position OFF in both cases.



Table 4-5. Settings for J101

Clock source	Jumper Setting	
Crystal	• (OFF)	
Oscillator	(ON)	

Long leads from your external crystal to the oscillator circuit on the pod may cause problems. It is possible to mount a crystal in the socket near J103. Do not use the 2-lead cable if you choose this option. If the crystal frequency is above 1 MHz and this is selected in the Clock Range menu in Emulator Options in AVR Studio, S101 should be in position ON. S101 should be in position OFF under all other circumstances.

Please note that a crystal cannot be used when emulating AT90S2343 or ATtiny22.

4.4.1 The Timer Oscillator This section only applies to emulation of S4434 and S8535.

A 32.768 kHz crystal is mounted on the pod for use with the Timer oscillator. It is impossible to emulate the Timer oscillator with a crystal in the target application due to the long leads from the target application to the oscillator circuit. If an external oscillator output is used as the clock source of the Timer oscillator, mount jumper J104, otherwise leave it open.

4.5 The Analog The jumpers labeled S0 and S1, must be set correctly to make the analog comparator work properly. Table 4-6 shows how to set these jumpers.

Table 4-6. Jumper Settings to Enable Analog Comparator

Device	S1/J105	S0/J106
ATtiny11, ATtiny12, ATtiny28, S1200, and S2313	(ON)	(ON)
S2333 and S4433	(ON)	• (OFF)
S4414, S4434, S8515, and S8535	• (OFF)	(ON)

Note: The jumper settings does not affect devices without Analog Comparator.

4.5.1 Connectors not Described in this User Guide

The 6-pin header connector is used for production testing.

