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VOLTAGE MODE DUAL OUTPUT PWM CONTROLLER

Description

The AZ494 is a voltage mode pulse width modulation switching regulator control circuit designed primarily for power supply control.

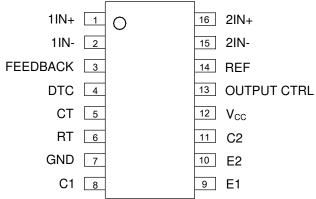
The AZ494 consists of a reference voltage circuit, two error amplifiers, an on-chip adjustable oscillator, a dead-time control (DTC) comparator, a pulse-steering control flip-flop, and an output control circuit. The precision of voltage reference (V_{REF}) is improved up to $\pm 1\%$ through trimming and this provides a better output voltage regulation. The AZ494 provides for push-pull or single-ended output operation, which can be selected through the output control.

The difference between AZ494A and AZ494C is that they have 4.95V and 5V reference voltage respectively.

The AZ494 is available in standard packages of PDIP-16 and SO-16.

Pin Assignments

(Top View)

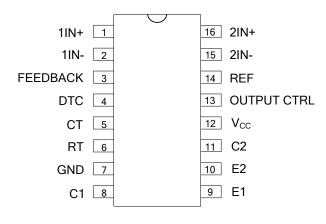


SO-16

Features

- Stable 4.95V/5V Reference Voltage Trimmed to ±1.0% Accuracy
- Uncommitted Output TR for 200mA Sink or Source Current
- Single-End or Push-Pull Operation Selected by Output Control
- Internal Circuitry Prohibits Double Pulse at Either Output
- Complete PWM Control Circuit with Variable Duty Cycle
- On-Chip Oscillator with Master or Slave Operation
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

(Top View)



PDIP-16

Applications

- SMPS
- Back Light Inverter
- Charger

Notes:

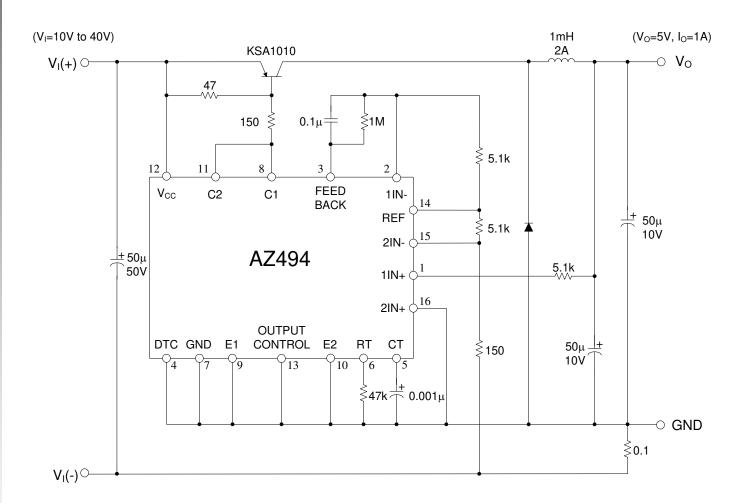
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Output Function Control Table

Signal for Output Control	Output Function
$V_{l} = GND$	Single-ended or parallel output
$V_{I} = V_{REF}$	Normal push-pull operation

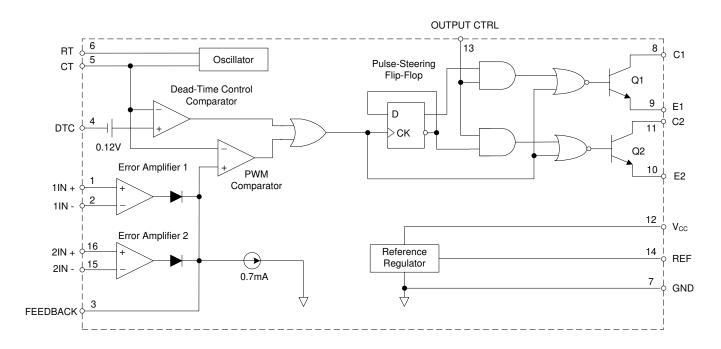
Typical Applications Circuit



Pulse Width Modulated Step-Down Converter



Functional Block Diagram



Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Ra	ting	Unit				
V _{CC}	Supply Voltage (Note 5)	Voltage (Note 5) 40				40		V
Vı	Amplifier Input Voltage	-0.3 to V _{CC} + 0.3		V				
Vo	Collector Output Voltage	40	V					
lo	Collector Output Current	2	50	mA				
	Dealess Theoret Issue (New C)	M Package	73	90.00				
θ JA	Package Thermal Impedance (Note 6)	P Package	67	°C/W				
_	Lead Temperature 1.6mm from case for 10 seconds	+260		°C				
T _{STG}	Storage Temperature Range	-65 to +150		°C				
_	ESD Rating (Machine Model)	2	00	V				

Notes: 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

- 5. All voltage values are with respect to the network ground terminal.
- 6. Maximum power dissipation is a function of $T_J(max)$, θ_{JA} and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of $+150^{\circ}C$ can affect reliability.





AZ494

Recommended Operating Conditions

Symbol	Parameter	Min	Тур	Max	Unit
V _{CC}	Supply Voltage	7	15	36	V
V_{C1}, V_{C2}	Collector Output Voltage	-	30	36	V
I _{C1} , I _{C2}	Collector Output Current(Each Transistor)	-	-	200	mA
Vı	Amplifier Input Voltage	0.3	-	V _{CC} - 2	V
I _{FB}	Current Into Feedback Terminal	-	-	0.3	mA
I _{REF}	Reference Output Current	-	_	10	mA
C _T	Timing Capacitor	0.00047	0.001	10	μF
R _T	Timing Resistor	1.8	30	500	kΩ
f _{osc}	Oscillator Frequency	1.0	40	200	kHz
_	PWM Input Voltage (Pin 3, 4, 14)	0.3	-	5.3	V
T _A	Operating Free-Air Temperature	-40	-	+85	°C

Electrical Characteristics (T_A=+25°C, V_{CC}=20V, f=10kHz, unless otherwise noted.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Reference Section	n					
V	Output Reference Voltage	I _{REF} =1mA	4.90	4.95	5.0	٧
V_{REF}	for AZ494A	I _{REF} =1mA, T _A = -40 to +85°C	4.85	4.95	5.05	V
V	Output Reference Voltage	I _{REF} =1mA	4.95	5.0	5.05	V
V _{REF}	for AZ494C	I _{REF} =1mA, T _A = -40 to +85°C	4.9	5.0	5.1	V
R _{LINE}	Line Regulation	V _{CC} = 7V to 36V	-	2	25	mV
R _{LOAD}	Load Regulation	I _{REF} =1mA to 10mA	-	1	15	mV
Isc	Short-Circuit Output Current	V _{REF} = 0V	10	35	50	mA
Oscillator Section	1					
	Oscillator Frequency	C _T =0.001μF, R _T =30kΩ	-	40	_	
fosc		C _T =0.01μF, R _T =12kΩ	9.2	10	10.8	kHz
1050	ossimate: 1 requestion	C_T =0.01 μ F, R_T =12 $k\Omega$ T_A = -40 to +85 $^{\circ}$ C	9.0	-	12	I I I
Δf /ΔΤ	Frequency Change with Temperature	C_T =0.01 μ F, R_T =12 $k\Omega$ T_A = -40 to +85 $^{\circ}$ C	_	_	1	%



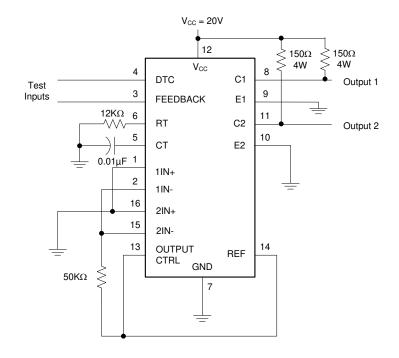


Electrical Characteristics (T_A=+25°C, V_{CC}=20V, f=10kHz unless otherwise noted.) (Cont.)

Symbol	Parameter		Conditions	Min	Тур	Max	Unit
Dead-Time Cont	rol Section			1		l	
I _{BIAS}	Input Bias Current		V _{CC} =15V, V _{PIN4} = 0 to 5.25V	-	-2	-10	μА
D(MAX)	Maximum Duty Cycle		V _{CC} =15V, V _{PIN4} = 0V, V _{PIN13} = V _{REF}	45	-	-	%
V	liano de Thura ala al al Malde		Zero Duty Cycle	-	3	3.3	V
V _{ITH}	Input Threshold Volt	age	Maximum Duty Cycle	0	_	-	V
Error-Amplifier S	Section						
V _{IO}	Input Offset Voltage		V _{PIN3} = 2.5V	_	2	10	mV
I _{IO}	Input Offset Current		V _{PIN3} = 2.5V	-	25	250	nA
I _{BIAS}	Input Bias Current		V _{PIN3} = 2.5V	-	0.2	1	μА
V _{CM}	Common-Mode Inpu Range	it Voltage	V _{CC} =7V to 36V	-0.3	-	V _{CC} -2	V
G _{VO}	Open-Loop Voltage	Gain	V _O =0.5V to 3.5V	70	95	_	dB
BW	Unity-Gain Bandwid	th	-	-	650	-	kHz
CMRR	Common-Mode Reje	ection Ratio	-	65	80	_	dB
I _{SINK}	Output Sink Current	(Feedback)	$V_{ID} = -15 \text{mV to } -5 \text{V}, \text{ V3} = 0.7 \text{V}$	-0.3	-0.7	-	mA
Isource	Output Source Curre	ent (Feedback)	V_{ID} =15mV to 5V, V3 = 3.5V	2	_	_	mA
PWM Comparato	or Section			1	ı		
V _{ITH}	Input Threshold Volt	age	Zero duty cycle	-	4	4.5	V
I _{SINK}	Input Sink Current		V3 = 0.7V	-0.3	-0.7	_	mA
Output Section					ı		
V _{CE} (SAT)	Output Saturation	Common Emitter	V _E = 0V, I _C =200mA	-	1.1	1.3	V
V _{CC} (SAT)	Voltage	Emitter Follower	V _{CC} = 15V, I _E = -200mA	-	1.5	2.5	V
I _C (OFF)	Collector Off-State 0	Current	V _{CE} = 36V, V _{CC} =36V	_	2	100	μΑ
I _E (OFF)	Emitter Off-State Cu	rrent	$V_{CC} = V_C = 36V, V_E = 0$	-	_	-100	μΑ
Total Device	·				•	-	
Icc	Supply Current		V _{PIN6} = V _{REF} , V _{CC} =15V	-	6	10	mA
Output Switchin	g Characteristics						
t _R	Rise Time		Common Emitter Common Collector	-	100	200	ns
t⊦	Fall Time		Common Emitter Common Collector	-	25	100	ns



Parameter Measurement Information



Test Circuit

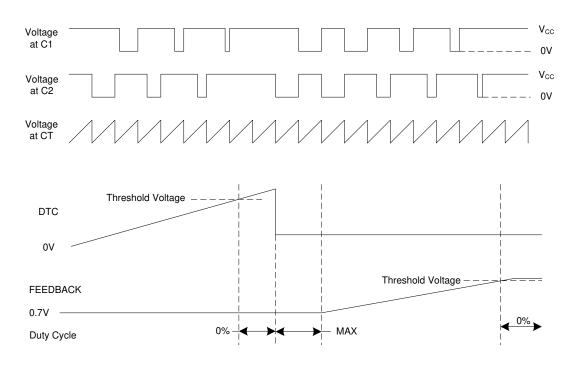


Figure 1. Operational Test Circuit and Waveforms



Parameter Measurement Information (Cont.)

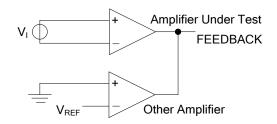
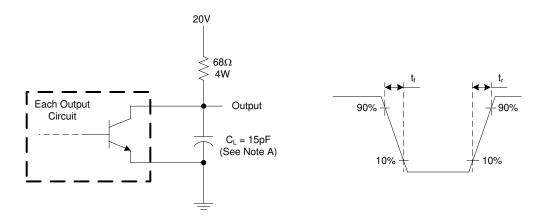
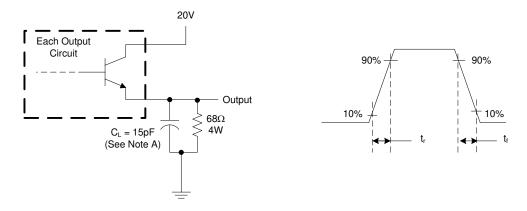


Figure 2. Error Amplifier Characteristics



Note A: C_L includes probe and jig capacitance.

Figure 3. Common-Emitter Configuration



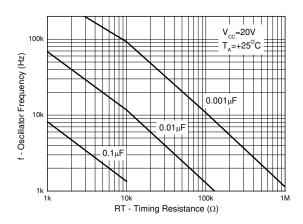
Note A: C_L includes probe and jig capacitance.

Figure 4. Emitter-Follower Configuration

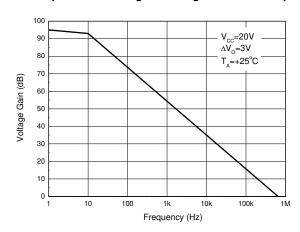


Performance Characteristics

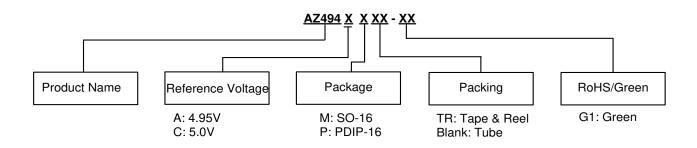
Oscillator Frequency vs. RT and CT



Error Amplifier Small-Signal Voltage Gain vs. Frequency



Ordering Information

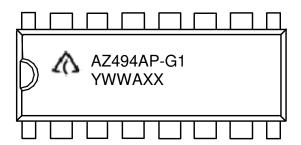


Package	Temperature Range	Part Number	Marking ID	Packing
SO-16	40.4- 0500	AZ494CMTR-G1	AZ494CM-G1	4000/Tape & Reel
PDIP-16	-40 to +85°C	AZ494AP-G1	AZ494AP-G1	25/Tube



Marking Information

PDIP-16 (Top View)

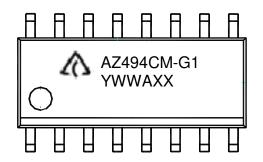


First Line: Logo and Marking ID (See Ordering Information) Second Line: Date Code

Y: Year

WW: Work Week of Molding A: Assembly House Code XX: 7th and 8th Digits of Batch No.

SO-16 (Top View)



First Line: Logo and Marking ID (See Ordering Information) Second Line: Date Code

Y: Year

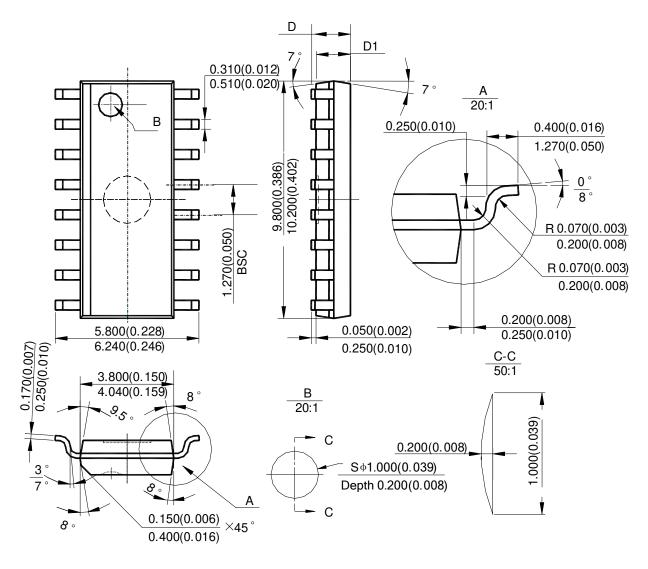
WW: Work Week of Molding

A: Assembly House Code XX: 7th and 8th Digits of Batch No.



Package Outline Dimensions (All dimensions in mm(inch).)

(1) Package Type: SO-16



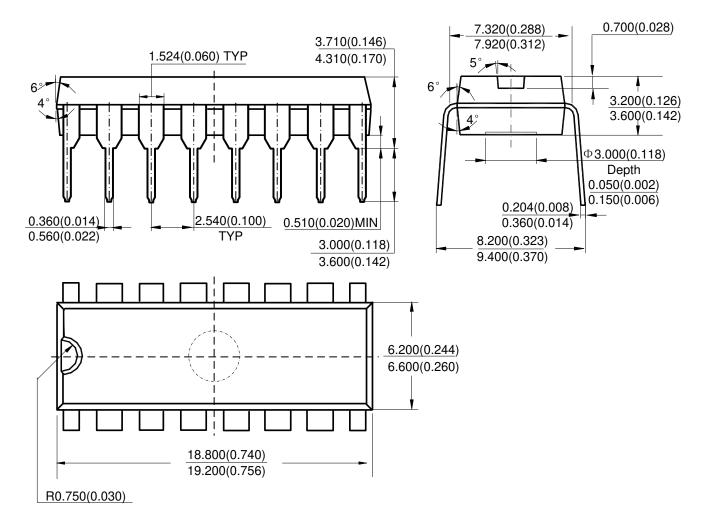
Note: Eject hole, oriented hole and mold mark is optional.

Symbol	D					D1			
Cymbol	min(mm)	max(mm)	min(inch)	max(inch)	min(mm)	max(mm)	min(inch)	max(inch)	
Option1	1.350	1.750	0.053	0.069	1.250	1.650	0.049	0.065	
Option2	-	1.260	-	0.050	1.020	-	0.040	-	



Package Outline Dimensions (All dimensions in mm(inch).) (Cont.)

(2) Package Type: PDIP-16

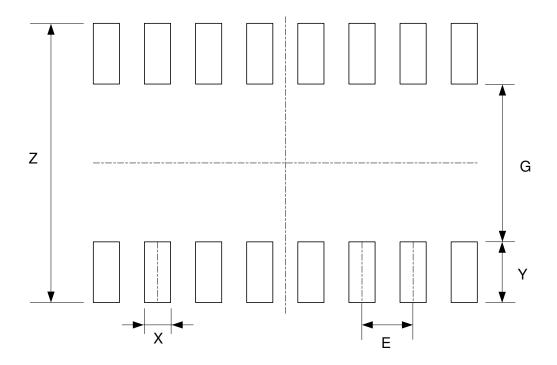


Note: Eject hole, oriented hole and mold mark is optional.



Suggested Pad Layout

(1) Package Type: SO-16



Dimensions	Z	G	X	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050



AZ494

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