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SK-TC179x Starter Kit

MCU Derivates: SAK-TC1796-256F150E

CPU Clock: 150 MHz

On-Chip Memory:
- 2 MByte embedded program flash with ECC,
- 128 KByte data flash for scalable 16 KByte EEPROM emulation,
- 192 KByte on-chip SRAM,
- 16 KByte instruction cache.

On-Boards Memory:
- Burst FLASH up to 16 MBytes (default: 4 MBytes),
- asynchronous SRAM up to 1MBytes (default),
- optional synchronous SRAM up to 8 MBytes.

Interfaces:
- DB9 for RS232-0,
- BERG10 for RS232-1,
- two BERG10 for CAN-0/1 with Transceiver,
- BERG16 for OCDS1,
- SAMTEC QSH-030-01-F-D-A for OCDS2,
- DB25 On Board Wiggler for OCDS1,
- four 80-pin connectors (male) with all I/O signals,
- four 80-pin connectors (female) with all I/O signals.

Includings:
- Technical Documentation: e.g. User manuals (System Unit and Peripheral unit), Architecture manual (pdf-version),
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