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RF360 Europe GmbH

A Qualcomm – TDK Joint Venture

SAW components

SAW RF filter

Automotive telematics

Series/type: B4353 Ordering code: B39162B4353P810

Date: Version: June 01, 2016 2.0

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SAW RF filter Automotive telematics

Series/type:	B4353
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B4353

1582.4 MHz

SAW components

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1 Application

- Low-loss RF GPS, COMPASS, Galileo, GLONASS filter
- Simultaneous usages of GPS, COMPASS, Galileo and GLONASS
- Usable pass band: 2.0 MHz for GPS, 4.092 MHz for COMPASS, 4.092 MHz for Galileo and 7.88 MHz for GLONASS
- Very low insertion attenuation
- High out of band selectivity
- Low amplitude ripple
- No matching network required for operation at 50 Ω

2 Features

- Package size 1.4±0.1 mm × 1.1±0.1 mm
- Package height 0.45 mm (max.)
- Package code QCS5P
- Approximate weight 3 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Filter surface passivated
- AEC-Q200 qualified component family
- Electrostatic Sensitive Device (ESD)

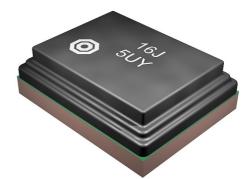


Figure 1: Picture of component with example of product marking.

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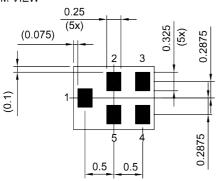
SAW components

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3 Package

BOTTOM VIEW



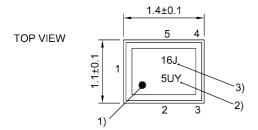
Pad and pitch tolerance ±0.05

4 Pin configuration

- ∎ 1 Input
- 4 Output
- 2, 3, 5 Ground

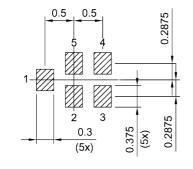
SIDE VIEW

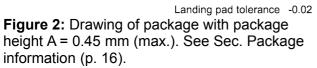




- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number









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5 Matching circuit

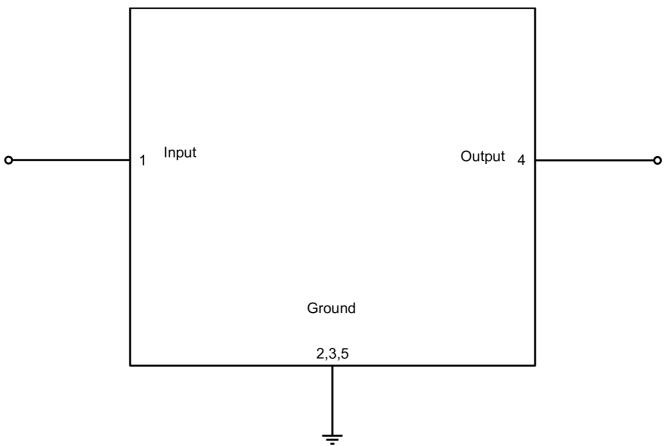


Figure 3: Schematic of matching circuit. No external matching components required.



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6 Characteristics

Temperature range for specification	$T_{_{ m SPEC}}$	= −40 °C +125 °C
Input terminating impedance	Z	= 50 Ω
Output terminating impedance	Z _{OUT}	= 50 Ω

Characteristics				min. for $T_{_{\rm SPEC}}$	typ. @+25 °C	max. for T _{SPEC}	
Center frequency			f _c	—	1582.4	—	MHz
Maximum insertion attenuation			$\alpha_{_{max}}$				
	1559.05 1563.15	MHz		—	1.3	2.0	dB
	1573.37 1577.47	MHz		_	1.0	2.0	dB
	1574.42 1576.42	MHz		—	1.0	1.4	dB
	1597.78 1605.66	MHz		—	1.5	2.2	dB
Variation of group delay			$\Delta\tau_{\rm var}$				
	1597.78 1605.66	MHz		_	4.0	14	ns ¹⁾
Maximum VSWR			VSWR _{max}				
@ input port	1559.05 1563.15	MHz		_	1.6	2.1	
	1573.37 1577.47	MHz		_	1.3	2.1	
	1574.42 1576.42	MHz			1.3	2.1	
	1597.78 1605.66	MHz		—	1.6	2.1	
@ output port	1559.05 1563.15	MHz		_	1.7	2.1	
	1573.37 1577.47	MHz		—	1.4	2.1	
	1574.42 1576.42	MHz		_	1.3	2.1	
	1597.78 1605.66	MHz		_	1.5	2.1	
Minimum attenuation			$\alpha_{_{min}}$				
	50 824	MHz		40	43	_	dB
	824 925	MHz		39	43	_	dB
	1427 1453	MHz		43	48	_	dB
	1710 1785	MHz		32	42	—	dB
	1850 1910	MHz		38	45	—	dB
	1920 1980	MHz		39	46	—	dB
	2400 2500	MHz		43	47	—	dB
	2500 2570	MHz		38	46	—	dB
	2600 3000	MHz		34	41	_	dB

¹⁾ Averaged over 2 MHz.

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7 Maximum ratings

Operable temperature	<i>T</i> _{OP} = −40 °C +125 °C	
Storage temperature	<i>T</i> _{sTG} = -40 °C +125 °C	
DC voltage	$V_{\rm DC} = 0 V$	
Input power	P _{IN}	
@ input port: 915 MHz	23 dBm	Continuous wave for 5000 h @ 50 °C.
@ input port: 1453 MHz	15 dBm	Continuous wave for 100000 h @ 55 °C.
@ input port: 1710 MHz	15 dBm	Continuous wave for 100000 h @ 55 °C.

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2.0

3.0

4.0

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20.0

40.0

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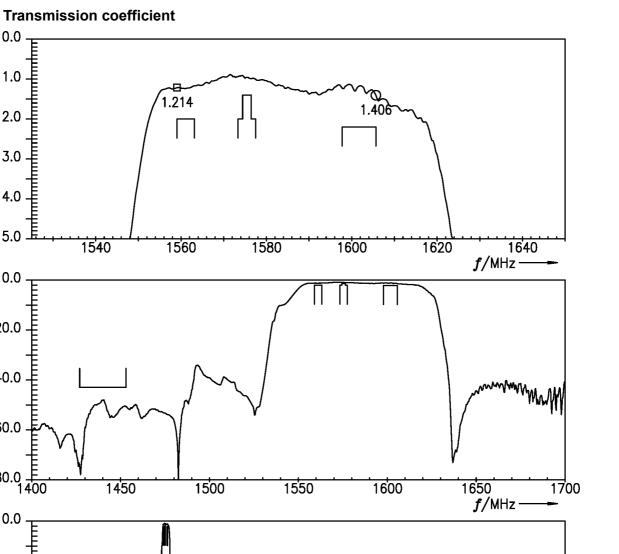
80.0 1

0.0

— α/dB

8

- a/dB



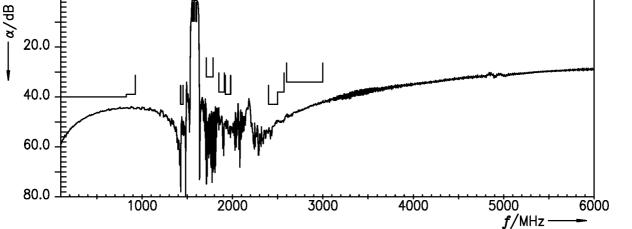


Figure 4: Attenuation.



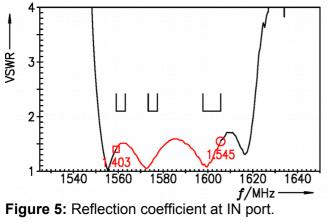
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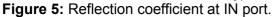
SAW components

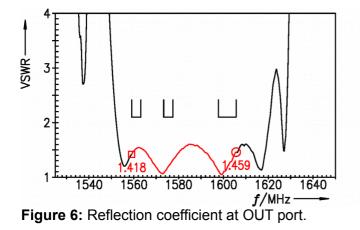
SAW RF filter

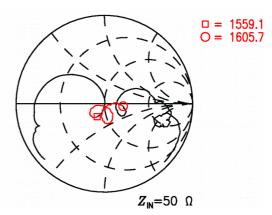
Data sheet

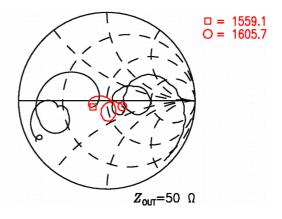
9 **Reflection coefficients**













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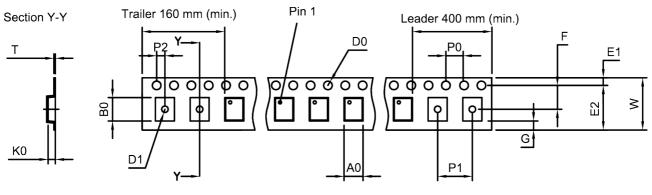
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10 Packing material

10.1 Tape



User direction of unreeling

Figure 7: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A ₀	1.27±0.05 mm
B₀	1.57±0.05 mm
D ₀	1.5+0.1/-0 mm
D ₁	0.5±0.1 mm
E₁	1.75±0.1 mm

E2	6.25 mm (min.)
F	3.5±0.05 mm
G	0.75 mm (min.)
K ₀	0.62±0.05 mm
P ₀	4.0±0.1 mm

P ₁	4.0±0.1 mm
P ₂	2.0±0.05 mm
Т	0.25±0.03 mm
W	8.0+0.3/-0.1 mm

 Table 1: Tape dimensions.

10.2 Reel with diameter of 180 mm

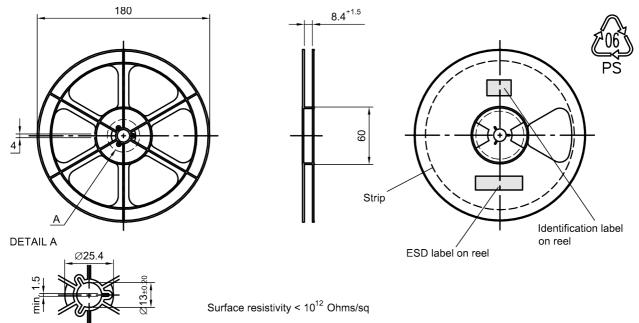


Figure 8: Drawing of reel (first-angle projection) with diameter of 180 mm.



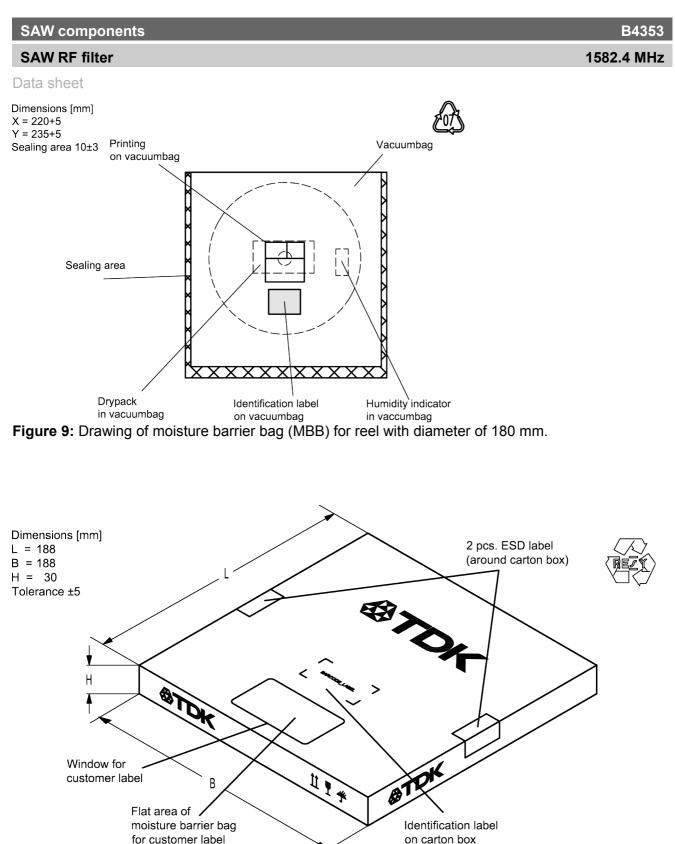


Figure 10: Drawing of folding box for reel with diameter of 180 mm.



SAW con	nponents							B4353
SAW RF filter 1582.4 N						1582.4 MHz		
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11 Markir	ng							
	•	with produc	t type num	her and	l lot number	encoded a	ccording to	Table 2 [.]
		with produc				chooded d	oooranig to	
Type nur	nber.							
		nber of the optical BASE3	•		jit marking.	e.g., E	33xxxxB <u>123</u>	<u>34</u> xxxx,
Example	of decodin	g type num	ber marking	g on de	vice		in decimal	code.
	16J	0 11		0	=>		123	34
The BAS		2 ² + 6 x 32 ¹ for product 1			=		123	34
Lot numl	oer:							
	•	he lot numb on a specia		code in	to a 3 digit r	e.g., narking.	123	345,
Example	of decodin	a lot numbe	er marking (on devi	ce	-	in decimal	code.
Example of decoding lot number marking on device					=>		123	845
	5 x 4	7 ² + 27 (=U)	x 47 ¹ + 31	(=Y) ×	47 [°] =		123	345
Adopte	ed BASE32 co	ode for type i	number		Adopt	ted BASE47 o	ode for lot n	umber
Decimal	Base32	Decimal	Base32		Decimal	Base47	Decimal	Base47
value	code	value	code		value	code	value	code
0	0	16	G	-	0	0	24	R
1	1	17	Н	-	1	1	25	S
2	2	18	J	-	2	2	26	Т
3	3	19	K	-	3	3	27	U
4	4	20	M	-	4	4	28	V
5	5	21	N	-	5	5	29	W
6	6	22	P		6	6 7	30	X Y
7 8	7 8	23 24	Q R	-	<u> </u>	8	31 32	Z
<u> </u>	0 9	24	S R	-	<u> </u>	0 9	32	b
10	A	25	T	-	10	A	34	d
	/ / /	20	•	-		,,	<u> </u>	<u> </u>

Table 2: Lists for encoding and decoding of marking.

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12 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3rd edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
<i>T</i> > 220 °C	30 s to 70 s
<i>T</i> > 230 °C	min. 10 s
<i>T</i> > 245 °C	max. 20 s
<i>T</i> ≥ 255 °C	-
peak temperature T_{peak}	250 °C +0/-5 °C
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

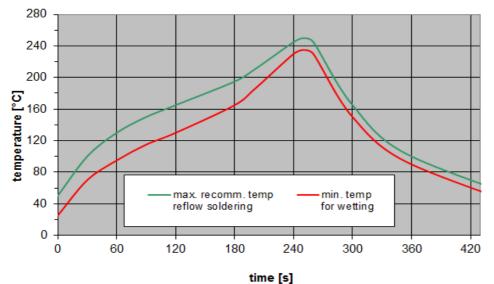


Figure 11: Recommended reflow profile for convection and infrared soldering – lead-free solder.

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13 ESD protection of SAW filters

SAW filters are Electro Static Discharge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, "ESD matching" has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended "ESD matching" topologies.

For wide band filters the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.

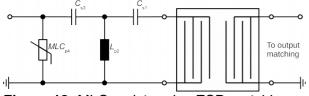


Figure 12: MLC varistor plus ESD matching.

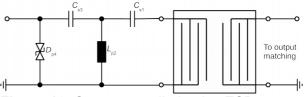


Figure 13: Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.

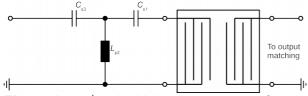


Figure 14: 3rd order high-pass structure for basic ESD protection.

In all three figures the shunt inductor L_{p2} could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to EPCOS Application report: **"ESD protection for SAW filters"**. This report can be found under <u>www.epcos.com/rke</u>. Click on "Applications Notes".

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14 Annotations

14.1 Matching coils

See TDK inductor pdf-catalog <u>http://www.tdk.co.jp/tefe02/coil.htm#aname1</u> and Data Library for circuit simulation <u>http://www.tdk.co.jp/etvcl/index.htm</u>.

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14.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local EPCOS sales office.

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Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

15.3 Moldability

Before using in overmolding environment, please contact your local EPCOS sales office.

15.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on EPCOS internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of EPCOS, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

Projection method

Unless otherwise specified first-angle projection is applied.

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