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RF360 Europe GmbH

A Qualcomm – TDK Joint Venture

SAW components

SAW RF filter

Automotive telematics Bluetooth

Series/type: B4360 Ordering code: B39242B4360P810

Date:December 02, 2016Version:2.1

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B4360

SAW components

SAW RF filter

Data sheet

Table of contents

1 Application	
2 Features	
3 Package	
4 Pin configuration	
5 Matching circuit	5
6 Characteristics	
7 Maximum ratings	
8 Transmission coefficient	8
9 Reflection coefficients	9
10 Packing material	
11 Marking	
12 Soldering profile	14
13 ESD protection of SAW filters	
14 Annotations	
15 Cautions and warnings	17
Important notes	



SAW components

SAW RF filter

Data sheet

1 Application

- Low-loss RF filter for bluetooth
- No external matching components required
- Usable pass band 83.5 MHz

2 Features

- Package size 1.1±0.1 mm × 0.9±0.1 mm
- Package height 0.45 mm (max.)
- Approximate weight 2 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Filter surface passivated
- Electrostatic Sensitive Device (ESD)
- AEC-Q200 qualified component family (Grade 1: -40 °C to +125 °C)



Figure 1: Picture of component with example of product marking.



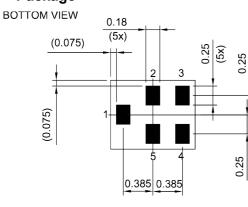
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SAW components

SAW RF filter

Data sheet

3 Package



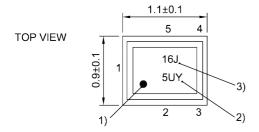
Pad and pitch tolerance ±0.05

4 Pin configuration

- ∎ 1 Input
- 4 Output
- 2, 3, 5 Ground

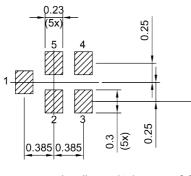
SIDE VIEW

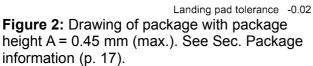




- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number









B4360

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SAW RF filter

Data sheet

5 Matching circuit

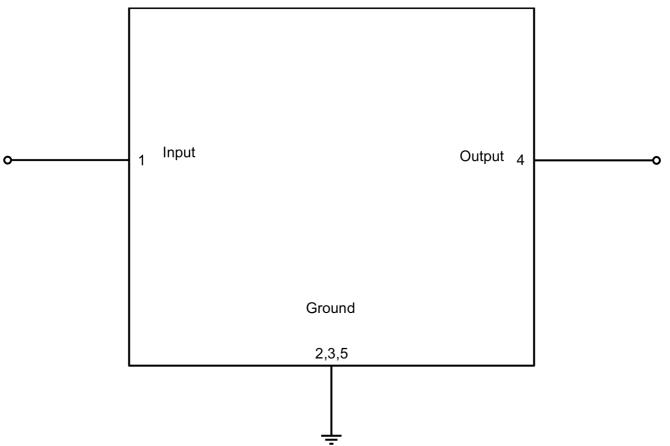


Figure 3: Schematic of matching circuit. No external matching components required.



SAW RF filter

Data sheet

6 Characteristics

Temperature range for specification	T _{SPEC}	= −30 °C +85 °C
Input terminating impedance	Z	= 50 Ω
Output terminating impedance	Z _{OUT}	= 50 Ω

Characteristics				min. for $T_{_{\rm SPEC}}$	typ. @ +25 °C	max. for $T_{_{\rm SPEC}}$	
Center frequency			f _c	—	2441.75	—	MHz
Maximum insertion attenuation			$\alpha_{_{max}}$				
	2400 2483.5	MHz		_	2.1	3.0	dB
Amplitude ripple (p-p)			Δα				
	2400 2483.5	MHz		—	0.9	1.8	dB
Maximum VSWR			$VSWR_{max}$				
@ input port	2400 2483.5	MHz		_	1.8	2.2	
@ output port	2400 2483.5	MHz		—	1.8	2.2	
Minimum attenuation			$\alpha_{_{min}}$				
	699 787	MHz		24	30	_	dB
	791 960	MHz		22	29	—	dB
	1452 1496	MHz		22	28	—	dB
	1710 2170	MHz		23	28	_	dB
	2570 2620	MHz		24	36	—	dB
	2620 2690	MHz		24	33	—	dB
	3400 3800	MHz		24	31		dB

B4360

2441.75 MHz



B4360

SAW components

SAW RF filter

Data sheet

7 **Maximum ratings**

Operable temperature	<i>T</i> _{OP} = -40 °C +125 °C	
Storage temperature	$T_{\rm STG}^{1)} = -40 ^{\circ}{\rm C} \dots +125 ^{\circ}{\rm C}$	
DC voltage	$ V_{\rm DC} ^{2)} = 0 V$	
Input power @ input port: 2400 2483.5 MHz	$P_{\rm IN}$ = 20 dBm	Continuous wave for 5000 h @ 55 °C.

Not valid for packaging material. Storage temperature for packaging material is -25 °C to +40 °C. In case of applied DC voltage blocking capacitors are mandatory. 1)

2)



B4360

SAW components

SAW RF filter

Data sheet

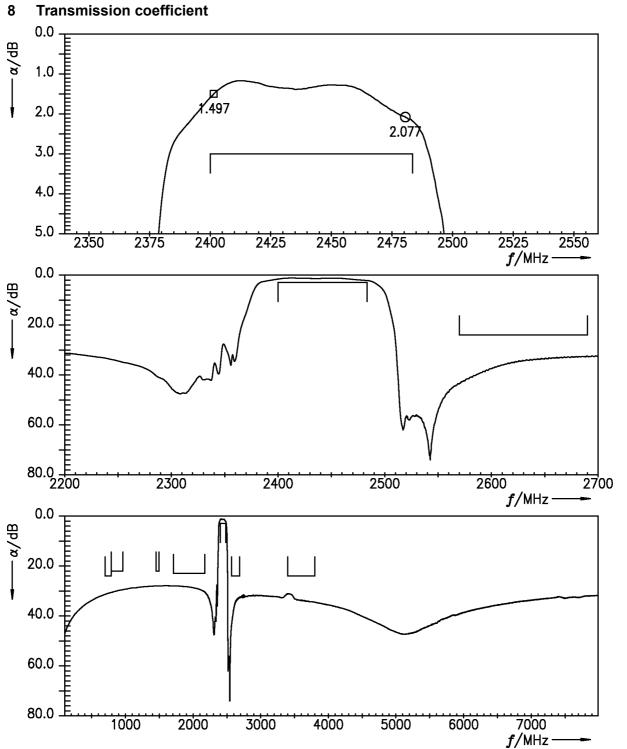


Figure 4: Attenuation.



□ = 2401.5 O = 2480.5

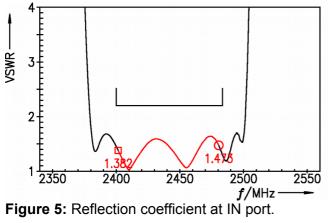
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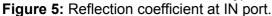
SAW components

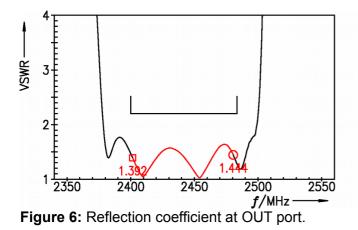
SAW RF filter

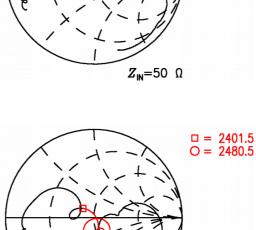
Data sheet

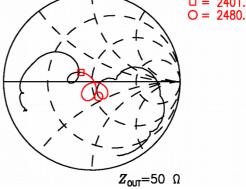
9 **Reflection coefficients**













B4360

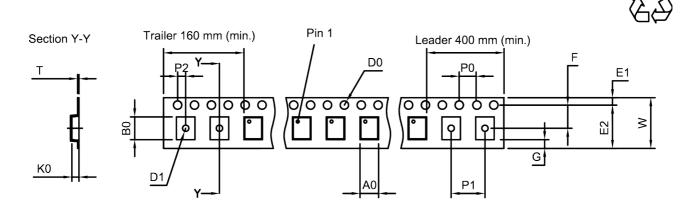
SAW components

SAW RF filter

Data sheet

10 Packing material

10.1 Tape



User direction of unreeling

Figure 7: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A ₀	1.02±0.05 mm
B ₀	1.22±0.05 mm
D ₀	1.55±0.05 mm
D ₁	0.55±0.1 mm
E1	1.75±0.1 mm

Table 1: Tape dimensions.

E2	6.25 mm (min.)
F	3.5±0.05 mm
G	-
K ₀	0.6±0.05 mm
P ₀	4.0±0.1 mm

P ₁	2.0±0.1 mm
P ₂	2.0±0.05 mm
Т	0.25±0.03 mm
W	8.0+0.3/-0.1 mm

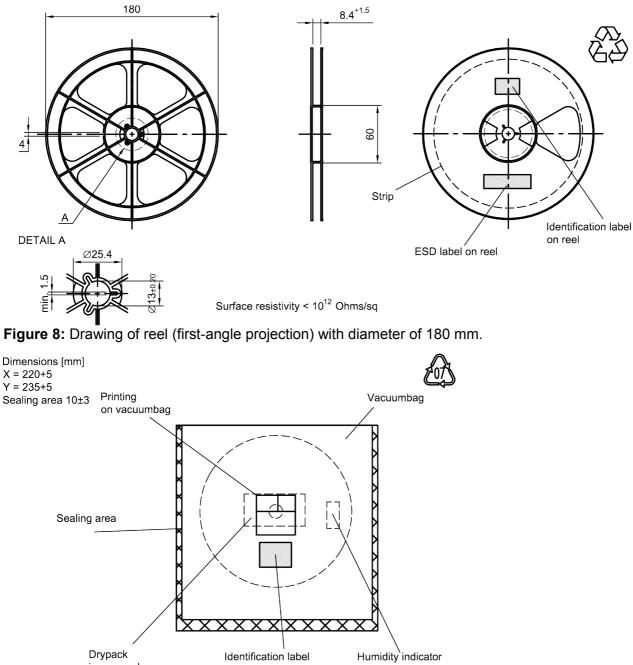


SAW RF filter

Data sheet

B4360 2441.75 MHz

10.2 Reel with diameter of 180 mm



in vacuumbag on vacuumbag in vaccumbag

Figure 9: Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.

⊗TDK

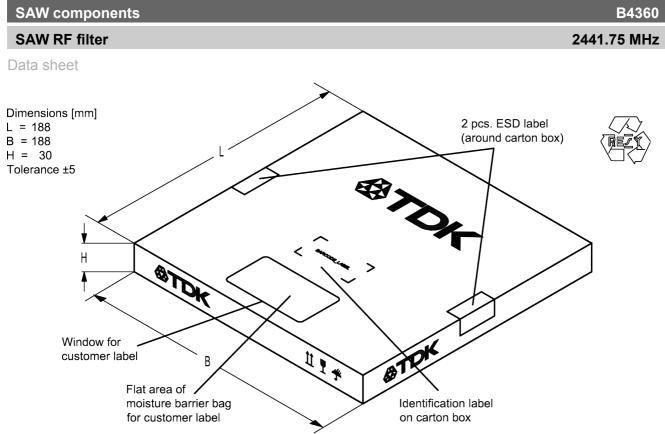


Figure 10: Drawing of folding box for reel with diameter of 180 mm.



SAW con	nponents							B430
SAW RF	filter							2441.75 Mł
Data sheet								
11 Markii	ng							
Products a	are marked	with produc	ct type num	ber and	l lot number	r encoded a	iccordina to	Table 2:
Type nur							J	
Гуре пи	libel.							
	git type nun ed by a spe				git marking.	e.g., I	33xxxxB <u>12</u> ;	<u>34</u> xxxx,
Example	of decodin	g type num	ıber markin	g on de	vice		in decimal	code.
	16J				=>	•	12:	-
T I D / C		$2^2 + 6 \times 32^1$	· · ·		=		123	34
	SE32 code f	or product	type B4360) is 488.				
Lot num	ber:							
	5 digits of t ded based			code int	to a 3 digit r	e.g., marking.	123	345,
Example	of decodin	a lot numbe	er marking	on devi	ce		in decimal	code.
	5UY	0	0		=>		123	345
	5 x 4	7² + 27 (=U)) x 47 ¹ + 31	(=Y) x	47 [°] =		12:	345
Adopte	ed BASE32 co	ode for type	number		Adop	ted BASE47	code for lot n	umber
Decimal	Base32	Decimal	Base32		Decimal	Base47	Decimal	Base47
value	code	value	code		value	code	value	code
0	0	16	G	_	0	0	24	R
1	1	17	H	_	1	1	25	S
2	2	18	J	_	2	2	26	Т
3	3	19	K	_	3	3	27	U
4	4	20	M	_	4	4	28	V
5	5	21	N	-	5	5	29	W
6	6	22	P	-	6	6	30	X
7 8	7 8	23 24	Q R	-	<u> </u>	7 8	31 32	Y Z
<u> </u>	0 9	24	R S	_	<u> </u>	0 9	32	b
10	A	25	З Т	-	10	A	33	d
10	B	20	V	-	11	B	35	f
12	C	28	W	_	12	C	36	h
			×	-			07	

Table 2: Lists for encoding and decoding of marking.

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SAW RF filter

Data sheet

12 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3rd edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
<i>T</i> > 220 °C	30 s to 70 s
<i>T</i> > 230 °C	min. 10 s
<i>T</i> > 245 °C	max. 20 s
<i>T</i> ≥ 255 °C	_
peak temperature T _{peak}	250 °C +0/-5 °C
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

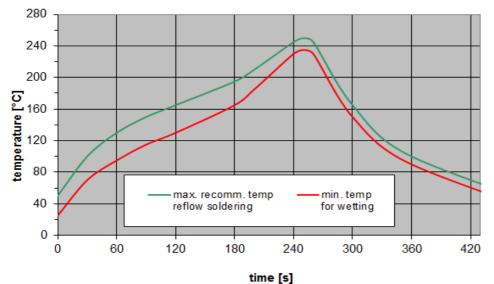


Figure 11: Recommended reflow profile for convection and infrared soldering – lead-free solder.

B4360

2441.75 MHz



SAW RF filter

Data sheet

13 ESD protection of SAW filters

SAW filters are Electro Static Discharge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, "ESD matching" has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended "ESD matching" topologies.

For wide band filters the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.

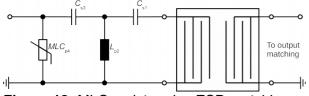


Figure 12: MLC varistor plus ESD matching.

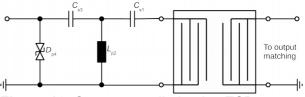


Figure 13: Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.

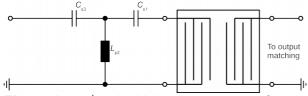


Figure 14: 3rd order high-pass structure for basic ESD protection.

In all three figures the shunt inductor L_{p2} could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to EPCOS Application report: **"ESD protection for SAW filters"**. This report can be found under <u>www.epcos.com/rke</u>. Click on "Applications Notes".



SAW RF filter

Data sheet

14 Annotations

14.1 Matching coils

See TDK inductor pdf-catalog <u>http://www.tdk.co.jp/tefe02/coil.htm#aname1</u> and Data Library for circuit simulation <u>http://www.tdk.co.jp/etvcl/index.htm</u>.

14.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

14.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local EPCOS sales office.

B4360

2441.75 MHz



SAW RF filter

Data sheet

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For information on recycling of tapes and reels please contact one of our sales offices.

15.3 Moldability

Before using in overmolding environment, please contact your local EPCOS sales office.

15.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on EPCOS internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of EPCOS, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

Projection method

Unless otherwise specified first-angle projection is applied.

2441.75 MHz

B4360



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