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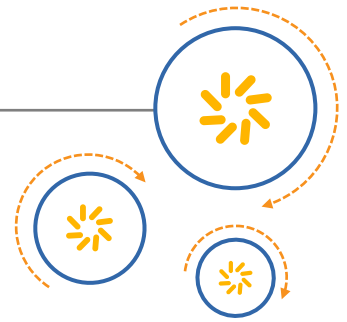
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RF360 Europe GmbH

A Qualcomm – TDK Joint Venture

SAW Components

SAW filter

Short range devices

Series/type:	B3769
Ordering code:	B39311B3769Z810
Date:	December 21, 2012
Version:	2.0

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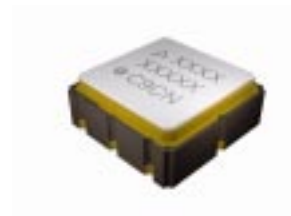
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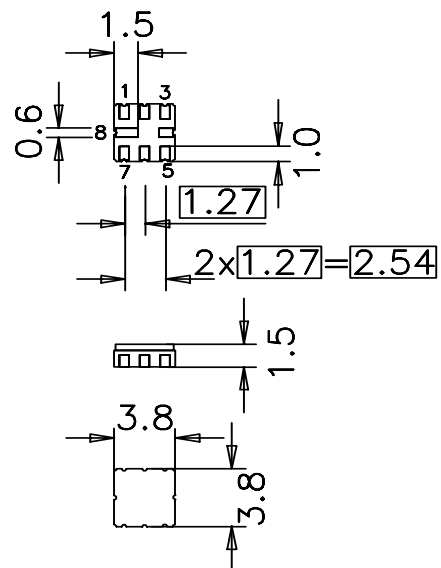
Data sheet


Application

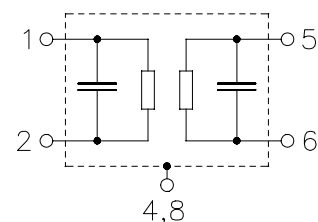
- Low-loss RF filter for remote control receivers
- Balanced and unbalanced operation possible


Features

- Package size 3.8 x 3.8 x 1.5 mm³
- Package code QCC8B
- RoHS compatible
- Approximate weight 0.07 g
- Package for **S**urface **M**ount **T**echnology (**SMT**)
- Ni, gold-plated terminals
- Lead free soldering compatible with J - STD20C
- Passivation layer ELPAS
- AEC-Q200 qualified component family
- **E**lectrostatic **S**ensitive **D**evice (**ESD**)


Pin configuration¹⁾

- 1 Input ground (recommended) or input
- 2 Input (recommended) or input ground
- 5 Output (recommended) or output ground
- 6 Output ground (recommended) or output
- 4,8 Case - ground
- 3,7 to be grounded



1) The recommended pin configuration usually offers best suppression of electrical crosstalk. The filter characteristics refer to this configuration.

Data sheet


Characteristics

Temperature range for specification:	$T_A = -40\text{ °C to }+95\text{ °C}$
Terminating source impedance:	$Z_S = 50\ \Omega$ and matching network
Terminating load impedance:	$Z_L = 50\ \Omega$ and matching network

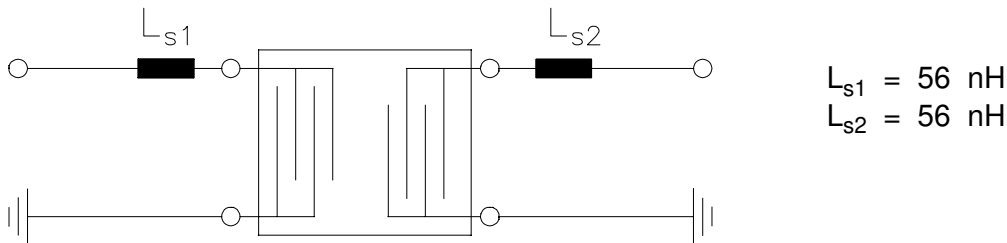
		min.	typ. @ 25 °C	max.	
Center frequency (center frequency between 3 dB points)	f_C	—	314.85	—	MHz
Minimum insertion attenuation incl. loss in matching elements	α_{\min}	—	1.9	2.9	dB
excl. loss in matching elements		—	1.3	2.3	dB
Pass band (relative to α_{\min})					
314.70 ... 315.00 MHz		—	0.5	2.0	dB
314.67 ... 315.03 MHz		—	0.7	3.0	dB
314.63 ... 315.07 MHz		—	1.0	6.0	dB
Pass bandwidth					
$\alpha_{\text{rel}} \leq 3\text{ dB}$		0.54	0.59	0.64	MHz
Relative attenuation (relative to α_{\min})	α_{rel}				
10.00 ... 294.35 MHz		48	53	—	dB
294.35 ... 304.35 MHz		45	50	—	dB
304.35 ... 312.65 MHz		25	30	—	dB
312.65 ... 313.05 MHz		33	40	—	dB
313.05 ... 314.15 MHz		13	18	—	dB
315.55 ... 322.85 MHz		13	17	—	dB
322.85 ... 335.00 MHz		38	45	—	dB
335.00 ... 600.00 MHz		43	48	—	dB
600.00 ... 1000.00 MHz		60	70	—	dB
1000.00 ... 2500.00 MHz		40	45	—	dB
Impedance for pass band matching¹⁾					
Input: $Z_{\text{IN}} = R_{\text{IN}} \parallel C_{\text{IN}}$		—	350 2.7	—	$\Omega \parallel \text{pF}$
Output: $Z_{\text{OUT}} = R_{\text{OUT}} \parallel C_{\text{OUT}}$		—	350 2.7	—	$\Omega \parallel \text{pF}$

¹⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After removal of the SAW filter the input impedance of the input and output matching network is calculated. The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details we refer to EPCOS application note #4.


Maximum ratings

Operable temperature range	T_A	-45/+125	°C	
Storage temperature range	T_{stg}	-45/+125	°C	
DC voltage	V_{DC}	6	V	
Source power	P_S	10	dBm	source impedance 50 Ω

Data sheet


Matching network to 50 Ω (element values depend on pcb layout and equivalent circuit)


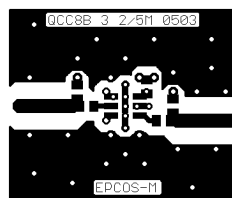
Minimising the crosstalk

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout. The major crosstalk mechanism is caused by the “ground-loop” problem.

Grounding loops are created if input-and output transducer GND are connected on the top-side of the PCB and fed to the system grounding plane by a common via hole. To avoid the common ground path, the ground pin of the input- and output transducer are fed to the system ground plane (bottom PCB plane) by their own via hole. The transducers’ grounding pins should be isolated from the upper grounding plane.

A common GND inductivity of 0.5nH degrades the ultimate rejection (crosstalk) by 20dB.

The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here. In this PCB layout the grounding loops are minimised to realise good ultimate rejection

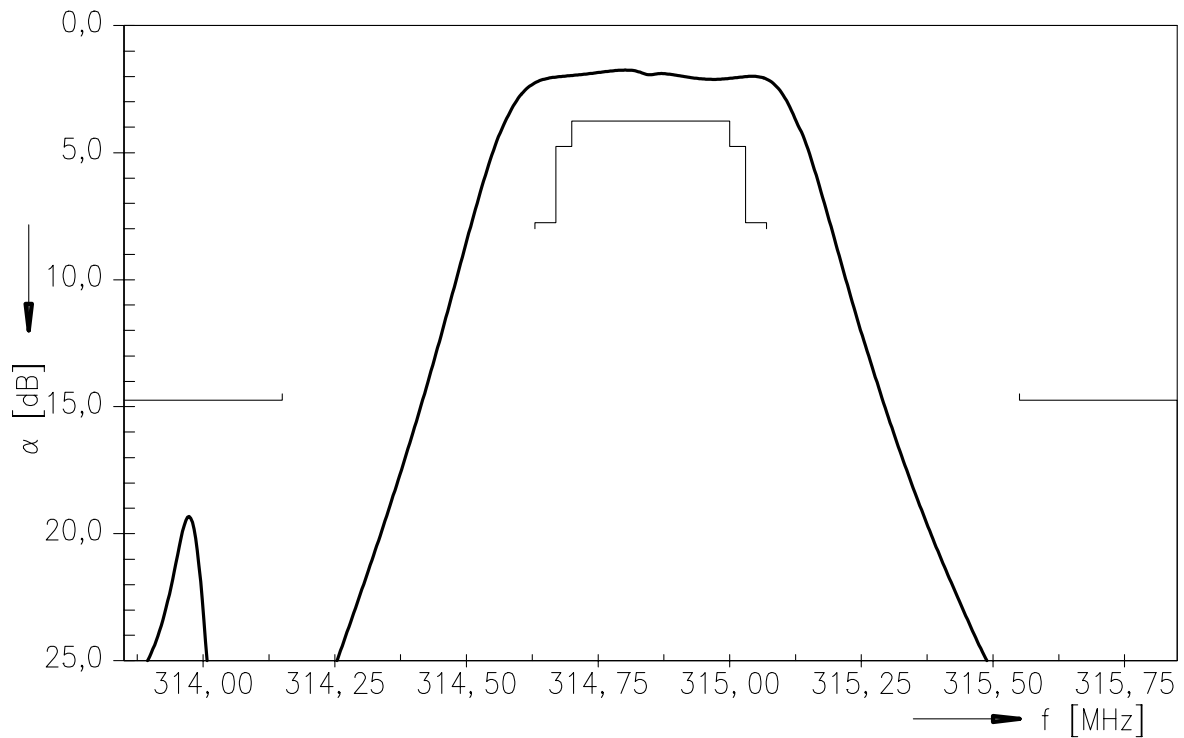
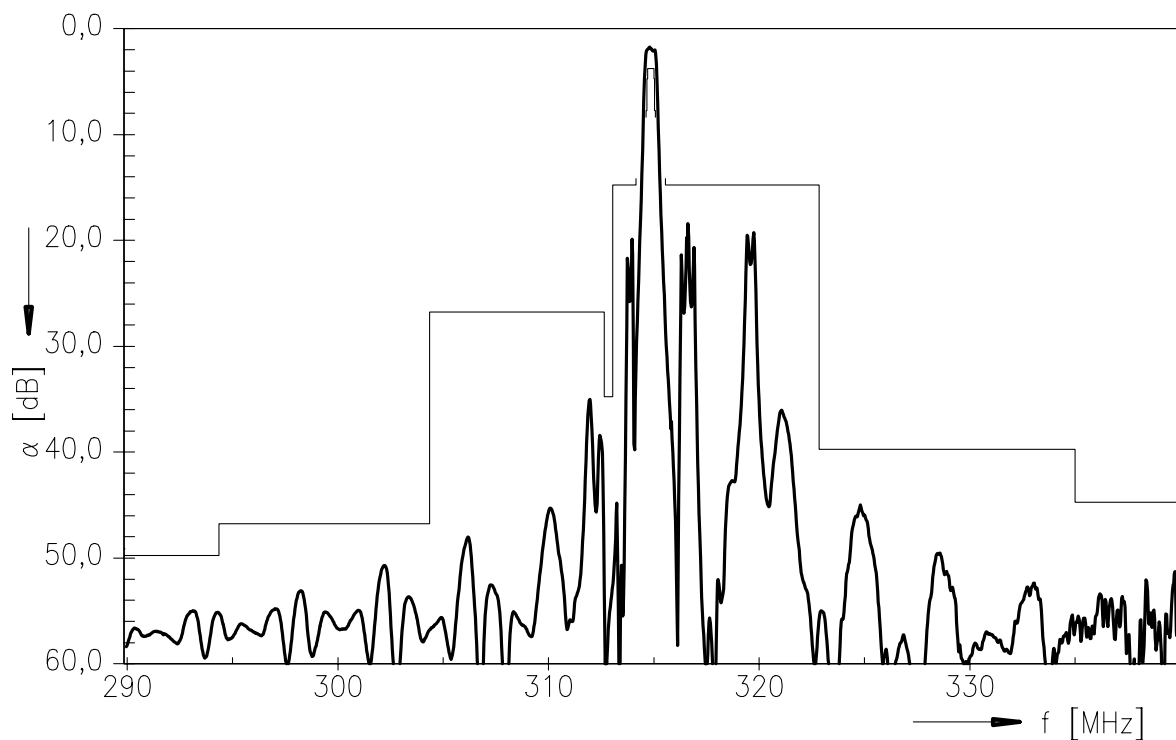


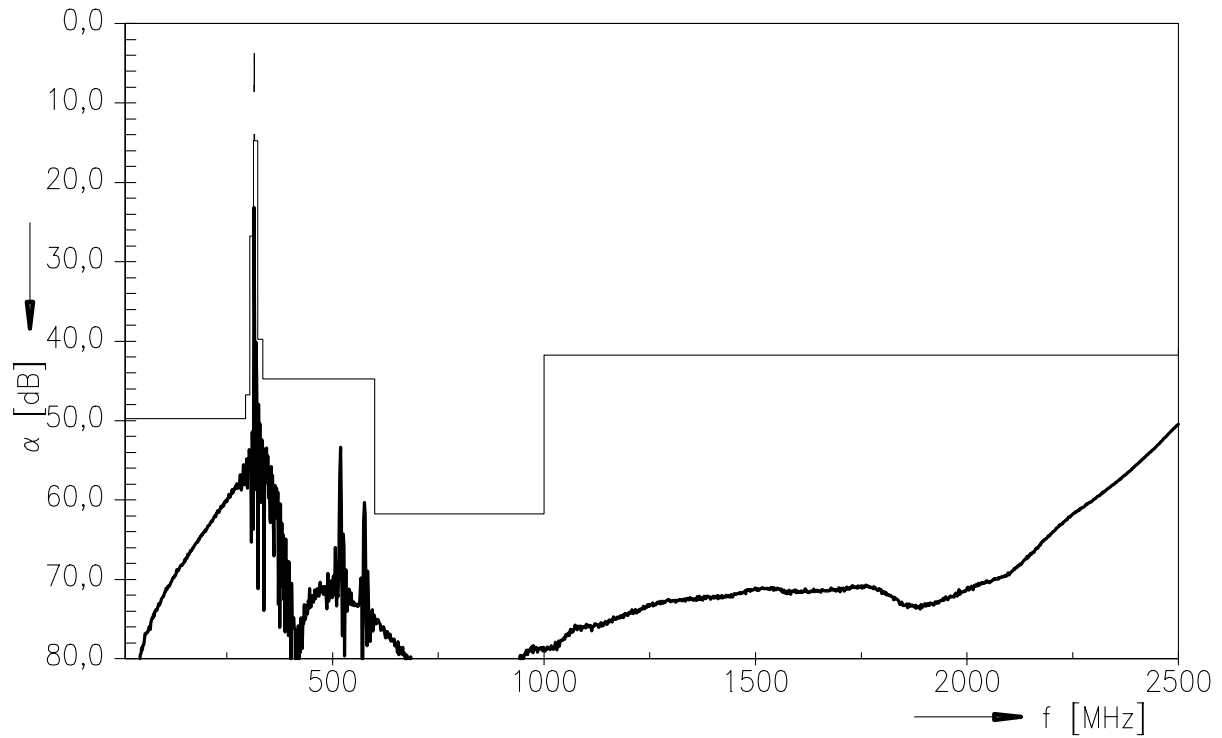
Optimised PCB layout for SAW filters in QCC8B package, pinning 2,5 (top side, scale 1:1)

The bottom side is a copper plane (system ground area). The input and output grounding pins are isolated and connected to the common ground by separated via holes.

For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.

Data sheet


Frequency response

Frequency response (wideband)



Frequency response (ultimate rejection)


Data sheet



ESD protection of SAW filters

SAW filters are **E**lectro **S**tatic **D**ischarge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, “ESD matching” has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below two figures show recommended “ESD matching” topologies.

Depending on the input impedance of the SAW filter and the source impedance, the needed component values have to be determined from case to case.

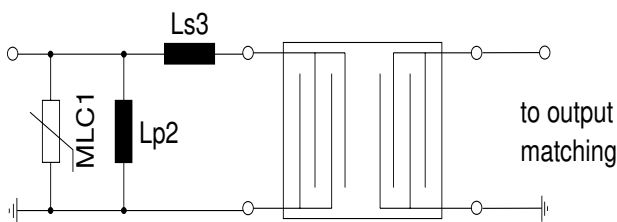


Fig. 1 MLC varistor plus ESD matching

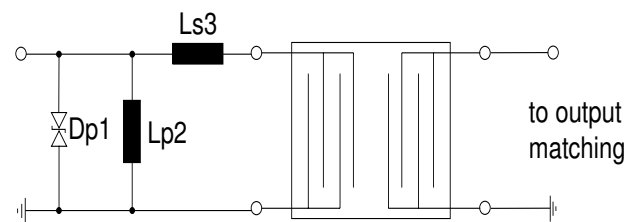


Fig. 2 Suppressor diode plus ESD matching

In cases where minor ESD occur, following simplified “ESD matching” topologies can be used alternatively.

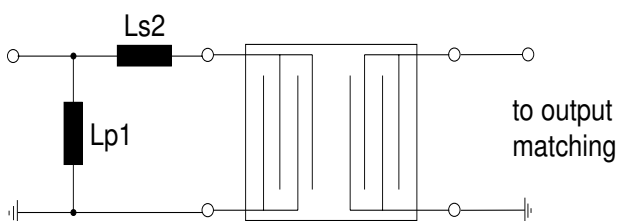


Fig. 3 shunt L – series L matching

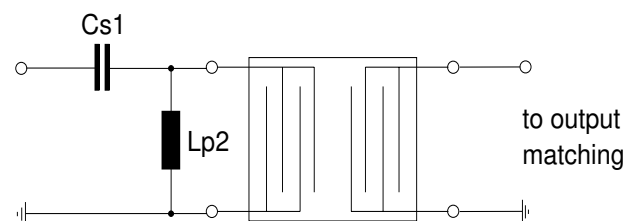


Fig. 4 series C – shunt L matching

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to EPCOS Application report:

“**ESD protection for SAW filters**”. This report can be found under www.epcos.com/rke. Click on “data sheets” and then “Applications” under category “Further information”.


References

Type	B3769
Ordering code	B39311B3769Z810
Marking and package	C61157-A7-A46
Packaging	F61074-V8167-Z000
Date codes	L_1126
S-parameters	B3769_NB.s2p B3769_WB.s2p See file header for port/pin assignment table
Soldering profile	S_6001
RoHS compatible	RoHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8 th , 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.
Matching coils	See Inductor pdf-catalog http://www.tdk.co.jp/tefe02/coil.htm#aname1 and Data Library for circuit simulation http://www.tdk.co.jp/etvcl/index.htm

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Published by EPCOS AG
Systems, Acoustics, Waves Business Group
P.O. Box 80 17 09, 81617 Munich, GERMANY

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