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SAW Components

SAW filter Short range devices

Series/type: Ordering code:

B3762 B39871B3762Z810

Date: Version: August 01, 2007 2.0

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SAW Components		B3762
SAW filter		868.30 MHz
Data sheet	SMD	

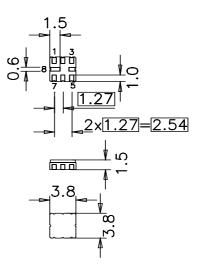
Application

- Low-loss RF filter for remote control receivers
- Balanced and unbalanced operation possible



Features

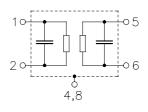
- Package size 3.8 x 3.8 x 1.5 mm³
- Package code QCC8B
- RoHS compatible
- Approximate weight 0.07 g
- Package for Surface Mount Technology (SMT)
- Ni, gold-plated terminals
- Lead free soldering compatible with J STD20C
- Passivation layer Protec
- AEC-Q200 qualified component family
- Electrostactic Sensitive Device (ESD)



Pin configuration¹⁾

- 1 Input (recommended) or input ground
- 2 Input ground (recommended) or input
- 5 Output (recommended) or output ground
- 6 Output ground (recommended) or output
- 4,8 Case ground
- 3,7 to be grounded

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¹⁾ The recommended pin configuration usually offers best suppression of electrical crosstalk. The filter characteristics refer to this configuration.



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Characteristics				
	50 Ω an	o +120 °C d matching n d matching n		
	min.	typ. @ 25 °C	max.	
Center frequencyf_C(center frequency between 3 dB points)		868.30		MHz
Minimum insertion attenuationα868.00868.60MHz		3.1	3.8	dB
Pass band (relative to α _{min}) 868.10 868.50 MHz 868.00 868.60 MHz		0.5 1.0	2.0 3.0	dB dB
Pass bandwidth $\alpha_{rel} \leq 3 \text{ dB}$	1.25	1.35	1.45	MHz
Relative attenuation (relative to α _{min}) α _{rel} 10.00 850.00 MHz 850.00 858.00 MHz 858.00 865.50 MHz	55 50 30	59 55 35	 	dB dB dB
871.00 880.00 MHz 880.00 890.00 MHz 890.00 1000.00 MHz	23 40 55	27 45 59		dB dB dB
Impedance for pass band matching ¹) Input: $Z_{IN} = R_{IN} C_{IN}$ Output: $Z_{OUT} = R_{OUT} C_{OUT}$	—	270 1.0 270 1.0		Ω pF Ω pF

¹⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After removal of the SAW filter the input impedance of the input and output matching network is calculated. The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details we refer to EPCOS application note #4.



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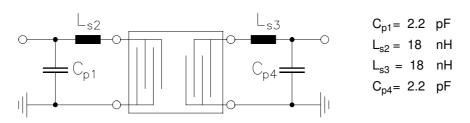
Maximum ratings

Operable temperature range	T _A	-45/+125	°C	
Storage temperature range	T _{stg}	-45/+125	°C	
DC voltage	V _{DC}	6	V	
Source power	Ps	10	dBm	source impedance 50 Ω



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Matching network to 50 Ω (element values depend on pcb layout and equivalent circuit)



Minimising the crosstalk

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout. The major crosstalk mechanism is caused by the "ground-loop" problem.

Grounding loops are created if input-and output transducer GND are connected on the top-side of the PCB and fed to the system grounding plane by a common via hole. To avoid the common ground path, the ground pin of the input- and output transducer are fed to the system ground plane (bottom PCB plane) by their own via hole. The transducers' grounding pins should be isolated from the upper grounding plane.

A common GND inductivity of 0.5nH degrades the ultimate rejection (crosstalk) by 20dB.

The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here. In this PCB layout the grounding loops are minimised to realise good ultimate rejection



Optimised PCB layout for SAW filters in QCC8B package, pinning 1,5 (top side, scale 1:1)

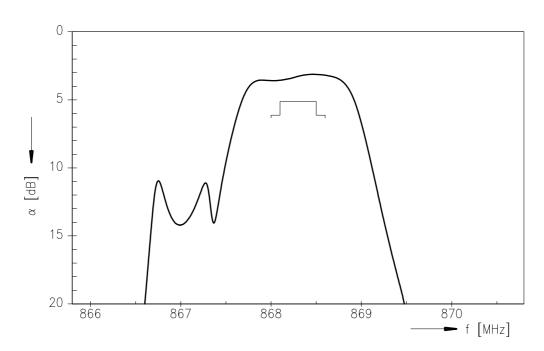
The bottom side is a copper plane (system ground area). The input and output grounding pins are isolated and connected to the common ground by separated via holes.

For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.

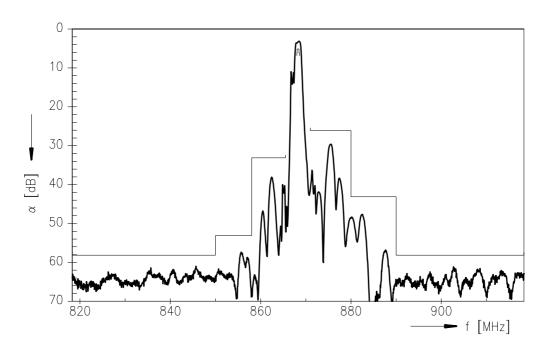




Frequency response



Frequency response (wideband)

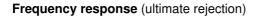


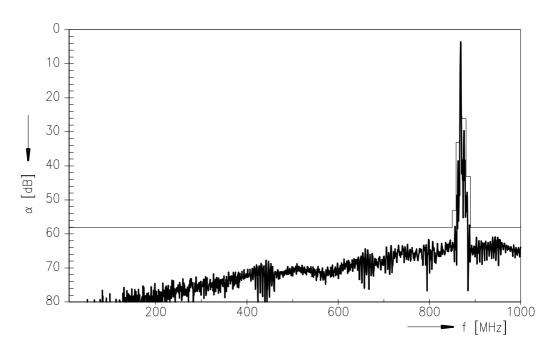
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ESD protection of SAW filters

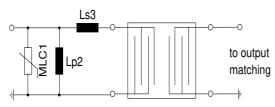
SAW filters are Electro Static Discharge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, "ESD matching" has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below two figures show recommended "ESD matching" topologies.

Depending on the input impedance of the SAW filter and the source impedance, the needed component values have to be determined from case to case.



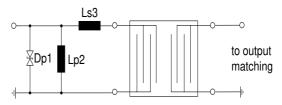


Fig. 1 MLC varistor plus ESD matching



to output

matching

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.

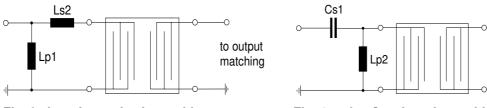
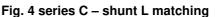


Fig. 3 shunt L – series L matching



Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to EPCOS Application report:

"**ESD protection for SAW filters**". This report can be found under <u>www.epcos.com/rke</u>. Click on "data sheets" and then "Applications" under category "Further information".

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SAW filter Data sheet

SMD

References

Туре	B3762
Ordering code	B39871B3762Z810
Marking and package	C61157-A7-A46
Packaging	F61074-V8167-Z000
Date codes	L_1126
S-parameters	B3762_SB.s2p B3762_WB.s2p
Soldering profile	S_6001
RoHS compatible	defined as compatible with the following documents: "DIRECTIVE 2002/95/EC OF THE EUROPEAN PARLIAMENT AND OF THE COUNCIL of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment. 2005/618/EC from April 18th, 2005, amending Directive 2002/95/EC of the European Parliament and of the Council for the purposes of establishing the maxi- mum concentration values for certain hazardous substances in electrical and electronic equipment."

For further information please contact your local EPCOS sales office or visit our webpage at www.epcos.com .

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