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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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$\mathsf{UltraCap}^{\circledR}$

Module 450 F/ 14 V

Series/Type:

Ordering code: B48621A4455Q006

Date: March 2005

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UltraCap[®] B48621A4455Q006

Module, 450 F/ 14 V

Features

- Screw terminal M8 x 15 (plus), M10 x 15 (minus)
- Active cell voltage balancing
- Case material polyethylene, black
- Power type
- 6 serial single cells of 2700 F
- Maintenance-free
- Short-circuit-proof
- Low ESR due to laser-welded interconnections

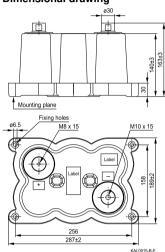
Options

Passive cell voltage balancing (by resistor)

Note

Please pay attention to the safety, transport and waste disposal instructions in chapter "Cautions".

Dimensional drawing



Dimensions in mm

Electrical specifications

Rated capacitance	(T _A = 25 °C; DCC) 1)	C _R	450	F
Tolerance of C _R			-10/+30	%
Rated voltage	(T _A = 25 °C)	V_R	14	V
Capacity			1800	mAh
Specific power	(IEC 62391-2)		1.6	kW/kg
Specific power	(IEC 62391-2)		1.6	kW/l
Stored energy	$(V = V_R)$	E	44100	J
Specific energy	$(V = V_R)$		2.0	Wh/kg
Specific energy	$(V = V_R)$		2.0	Wh/I
Surge voltage		V_{surge}	16	V
Maximum series resistance	$(T_A = 25 ^{\circ}C; 1 \text{kHz})$	ESR	1.4	$m\Omega$
Maximum series resistance	$(T_A = 25 ^{\circ}C; 50 \text{mHz})$	ESR _{DC}	2.4	$m\Omega$
Weight			6.0	kg
Volume			6.2	1
Operating temperature range		T _{op}	-30/+70	°C
Storage temperature	(V = 0 V)	T_{st}	-40/+70	°C
Lifetime (hours) 2)	$(T_A = 25 ^{\circ}C; V = V_R)$		90000	h
Lifetime (cycles) 3)	$(T_A = 25 ^{\circ}C; I = 100 A)$		500000	cycles

¹⁾ DCC: discharging with constant current.

²⁾ Requirements: $|\Delta C/C_R| \le 30\%$, ESR ≤ 2 times of specified limit, $I_{leak} \le 2$ times of initial value.

³⁾ Requirements: $|\Delta C/C_R| \le 30\%$, ESR ≤ 2 times of specified limit, $I_{leak} \le 2$ times of initial value (1 cycle: charging to V_R , 30 s rest, discharging to $V_R/2$, 30 s rest).