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# SIOV metal oxide varistors

Strap varistors, HighE, SuperioR, LS42 series

Series/Type: B722\*

Date: April 2011

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## HighE, SuperioR, LS42 series

#### Construction

- Rectangular varistor element
- Coating: epoxy resin, flame-retardant to UL 94 V-0
- Bolt-holed strap terminals for screw fixing or soldering
- Bent or straight strap terminals

#### **Features**

- Dimensions equivalent to SIOV-LS40
- High surge current up to 65 kA
- Wide operating voltage range 250 ... 460 V<sub>RMS</sub>
- Designed in accordance with the requirements of IEC 61643-1, class II, for low-voltage surge protection devices

## **Approvals**

- UL
- CSA

# **Delivery mode**

■ Vacuum-packed styrofoam box

#### General technical data

Climatic category	to IEC 60068-1	40/85/56	
Operating temperature	to CECC 42 000	−25 + 85	°C
Storage temperature		−25 +110	°C
Electric strength	to CECC 42 000	≥ 2.5	kV <sub>RMS</sub>
Insulation resistance	to CECC 42 000	≥ 10	$M\Omega$
Response time		< 25	ns



# HighE, SuperioR, LS42 series



# Electrical specifications and ordering codes Maximum ratings ( $T_A$ = 85 °C)

Ordering code	Туре	$V_{RMS}$	$V_{DC}$	i <sub>max</sub>	I <sub>max</sub> 1)	I <sub>nom</sub> <sup>2)</sup>	$W_{max}$	$P_{\text{max}}$
•				(8/20 µs)	(8/20 µs)	(8/20 µs)	(2 ms)	
	SIOV-	V	٧	Α	Α	Α	J	W
Bent strap terminals								
B72242L0251K100	LS42K250QP	250	320	65000	65000	20000	490	1.4
B72242L0271K100	LS42K275QP	275	350	65000	65000	20000	550	1.4
B72242L0321K100	LS42K320QP	320	420	65000	65000	20000	640	1.4
B72242L0381K100	LS42K385QP	385	505	65000	65000	20000	800	1.4
B72242L0421K100	LS42K420QP	420	560	65000	65000	20000	910	1.4
B72242L0441K100	LS42K440QP	440	585	65000	65000	20000	950	1.4
B72242L0461K100	LS42K460QP	460	615	65000	65000	20000	960	1.4
Straight strap terminals								
B72242L0251K102	LS42K250QPK2	250	320	65000	65000	20000	490	1.4
B72242L0271K102	LS42K275QPK2	275	350	65000	65000	20000	550	1.4
B72242L0321K102	LS42K320QPK2	320	420	65000	65000	20000	640	1.4
B72242L0381K102	LS42K385QPK2	385	505	65000	65000	20000	800	1.4
B72242L0421K102	LS42K420QPK2	420	560	65000	65000	20000	910	1.4
B72242L0441K102	LS42K440QPK2	440	585	65000	65000	20000	950	1.4
B72242L0461K102	LS42K460QPK2	460	615	65000	65000	20000	960	1.4

<sup>1)</sup>  $I_{\text{max}} = \text{Maximum discharge current to IEC 61643-1, class II}$ 

<sup>2)</sup> I<sub>nom</sub> = Nominal discharge current to IEC 61643-1, class II





# Strap varistors

B722\*

# HighE, SuperioR, LS42 series

# Characteristics ( $T_A = 25$ °C)

Ordering code	Туре	$V_{v}$	$\Delta V_{v}$	$V_{c,max}$	i <sub>c</sub>	$C_{typ}$
•		(1 mA)	(1 mA)	(i <sub>c</sub> )		(1 kHz)
	SIOV-	V	%	V	Α	pF
Bent strap terminals						
B72242L0251K100	LS42K250QP	390	±10	650	300	2900
B72242L0271K100	LS42K275QP	430	±10	710	300	2700
B72242L0321K100	LS42K320QP	510	±10	840	300	2300
B72242L0381K100	LS42K385QP	620	±10	1025	300	1900
B72242L0421K100	LS42K420QP	680	±10	1120	300	1800
B72242L0441K100	LS42K440QP	715	±10	1180	300	1700
B72242L0461K100	LS42K460QP	750	±10	1240	300	1600
Straight strap terminals						
B72242L0251K102	LS42K250QPK2	390	±10	650	300	2900
B72242L0271K102	LS42K275QPK2	430	±10	710	300	2700
B72242L0321K102	LS42K320QPK2	510	±10	840	300	2300
B72242L0381K102	LS42K385QPK2	620	±10	1025	300	1900
B72242L0421K102	LS42K420QPK2	680	±10	1120	300	1800
B72242L0441K102	LS42K440QPK2	715	±10	1180	300	1700
B72242L0461K102	LS42K460QPK2	750	±10	1240	300	1600





Ordering code

Bent strap terminals B72242L0251K100

B72242L0271K100

B72242L0321K100

B72242L0381K100

B72242L0421K100

B72242L0441K100

B72242L0461K100

B72242L0271K102

B72242L0321K102

B72242L0381K102

B72242L0421K102

B72242L0441K102

B72242L0461K102

Straight strap terminals B72242L0251K102

# HighE, SuperioR, LS42 series



L ±1.0

-2.1

-1.9

-1.5

-0.8

-0.5

-0.4

-0.2

5.3

5.5

5.9

6.5

6.8

7.0

7.2

mm

th<sub>max</sub>

mm

9.2

9.4

9.9

10.6

10.9

11.1

11.4

9.2

9.4

9.9

10.6

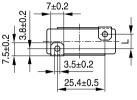
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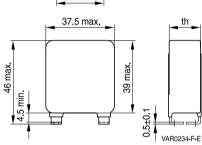
11.1

11.4

## **Dimensional drawings**

# Bent strap terminals

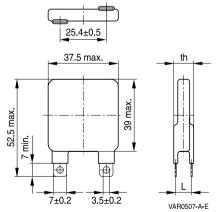




SIOV-LS42K ... QP

Weight: 25 ... 40 g

# Straight strap terminals



SIOV-LS42K ... QPK2

Please read Cautions and warnings and

Important notes at the end of this document.

Weight: 25 ... 40 g





# Strap varistors HighE, SuperioR, LS42 series

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# Reliability data

Test	Test methods/conditions	Requirement
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called $V_{\nu}$ (1 mA <sub>DC</sub> @ 0.2 2 s).	To meet the specified value
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) applied.	To meet the specified value
Max. DC operating voltage	The maximum allowable DC operating voltage $V_{DC}$ at UCT +5/ $-0$ °C is applied for 1000 $\pm$ 48 h. The leakage current $I_{leak}$ (t) during test is recorded. Then the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of $V_V$ shall be measured.	$\begin{split} I_{leak} & (t=1000 \ h) \leq I_{leak} \ (t=0 \ h) \\ &  \Delta V/V \ (1 \ mA)  \leq 10\% \end{split}$
Surge current derating, 8/20 μs	10 surge currents (8/20 µs), unipolar, interval ≥60 s, amplitude corresponding to derating curve for 10 impulses at 20 µs	∆V/V (1 mA)  ≤10% (measured in direction of surge current) No visible damage
Fast temperature cycling	IEC 60068-2-14, test Na, LCT/UCT, dwell time 30 min, 5 cycles	l∆V/V (1 mA)l ≤10% No visible damage
Damp heat, steady state	IEC 60068-2-78 The specimen shall be subjected to $40 \pm 2$ °C, 90 to 95% r. H. for $56 \pm 2$ days with 10% of the maximum continuous DC operating voltage $V_{DC}$ . Then stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of $V_V$ shall be measured.	IΔV/V (1 mA)I ≤10%

#### Note:

UCT = Upper category temperature LCT = Lower category temperature

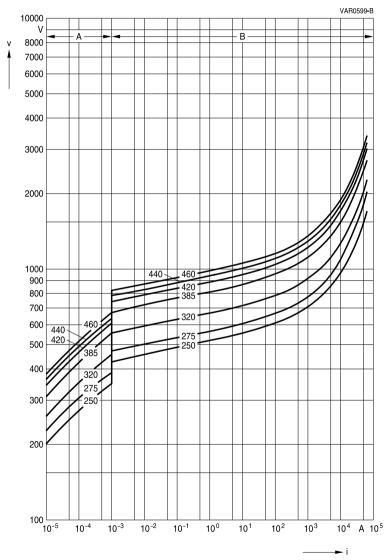


## HighE, SuperioR, LS42 series



#### v/i characteristics

v = f (i) for explanation of the characteristics refer to "General technical information", chapter 1.6.3 A = Leakage current, B = Protection level } for worst-case varistor tolerances



SIOV-LS42K250QP(K2) ... K460QP(K2)





Strap varistors

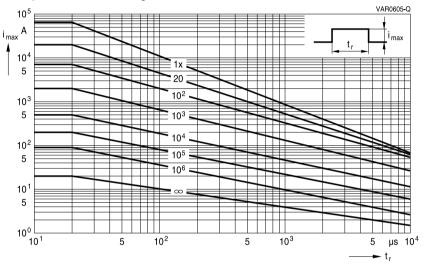
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# **Derating curves**

Maximum surge current  $i_{max} = f(t_r, pulse train)$ 

For explanation of the derating curves refer to "General technical information", section 1.8.1



SIOV-LS42K250QP(K2) ... K460QP(K2)



### HighE, SuperioR, LS42 series



## Cautions and warnings

#### General

- EPCOS metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
- 3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

## Storage

- 1. Store SIOVs only in original packaging. Do not open the package before storage.
- 2. Storage conditions in original packaging:

Storage temperature:  $-25~^{\circ}\text{C}$  ... +45  $^{\circ}\text{C}$ ,

Relative humidity: <75% annual average,

<95% on maximum 30 days a year.

Dew precipitation: is to be avoided.

- 3. Avoid contamination of an SIOV's during storage, handling and processing.
- 4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
- 5. The SIOV type series should be soldered within the time specified:

SIOV-S, -Q, -LS, -B, -SFS 24 months ETFV 12 months.

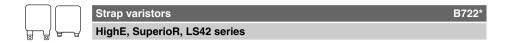
#### Handling

- 1. SIOVs must not be dropped.
- 2. Components must not be touched with bare hands. Gloves are recommended.
- 3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

## Soldering (where applicable)

- 1. Use rosin-type flux or non-activated flux.
- 2. Insufficient preheating may cause ceramic cracks.
- 3. Rapid cooling by dipping in solvent is not recommended.
- 4. Complete removal of flux is recommended.





## Mounting

- Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
- 2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

## Operation

- 1. Use SIOVs only within the specified temperature operating range.
- 2. Use SIOVs only within the specified voltage and current ranges.
- Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.



# HighE, SuperioR, LS42 series



# Symbols and terms

Symbol	Term
С	Capacitance
$C_{typ}$	Typical capacitance
i	Current
i <sub>c</sub>	Current at which V <sub>c, max</sub> is measured
l <sub>leak</sub>	Leakage current
i <sub>max</sub>	Maximum surge current (also termed peak current)
I <sub>max</sub>	Maximum discharge current to IEC 61643-1
$I_{nom}$	Nominal discharge current to IEC 61643-1
LCT	Lower category temperature
$L_{typ}$	Typical inductance
$P_{max}$	Maximum average power dissipation
$R_{ins}$	Insulation resistance
$R_{min}$	Minimum resistance
$T_A$	Ambient temperature
t <sub>r</sub>	Duration of equivalent rectangular wave
UCT	Upper category temperature
V	Voltage
$V_{clamp}$	Clamping voltage
V <sub>c, max</sub>	Maximum clamping voltage at specified current i <sub>c</sub>
$V_{DC}$	DC operating voltage
$V_{jump}$	Maximum jump start voltage
$V_{max}$	Maximum voltage
$V_{op}$	Operating voltage
$V_{RMS}$	AC operating voltage, root-mean-square value
$V_{\text{RMS, op, max}}$	Root-mean-square value of max. DC operating voltage incl. ripple current
$V_{\text{surge}}$	Super imposed surge voltage
$V_{V}$	Varistor voltage
$\Delta V_{V}$	Tolerance of varistor voltage
$W_{LD}$	Maximum load dump
$W_{max}$	Maximum energy absorption
e	Lead spacing
	Lead spacing

All dimensions are given in mm.

The commas used in numerical values denote decimal points.



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The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
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