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We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Surge arrester

2-electrode arrester

Series/Type: Ordering code: S50-A230X

B88069X1923T902

2016-08-19 Date:

Version: 02

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Surge arrester B88069X1923T902

2-electrode arrester S50-A230X

Product description

The S50-series has been especially designed to meet data transmission protection requirements. The optimized design features a high level of protection against fast rising transients usually caused by lightning disturbances. For use in high frequency data lines, the series offers ultra low capacitances and shows only marginally signal losses up to high frequencies. The devices are extremely reliable and are able to withstand high surge currents without destruction.

Features

- Small size (EIA 2220)
- Short response time
- High current capability
- Stable performance over service life
- Ultra low capacitance and insertion loss
- High insulation resistance
- Excellent SMD handling
- RoHS-compatible

Applications

Telecommunication:

- Ethernet, PoE, xDSL
- Cable modem, splitters, line cards
- Wireless antenna protection

Others:

- CCTV
- Switching power supply

Product characteristics

Physical dimensions	0.22 × 0.20 × 0.20 in	
(length × width × height)	5.7 × 5.0 × 5.0	mm
	EIA 2220 / 5750 metric	
Weight	~ 0.3	g
Operating temperature	−40 +125 °C	
Recommended storage ¹⁾ - temperature - humidity - period	+5 +35 45 80 ≤ 2	
Climatic category (IEC 60068-1)	40/125/21	
Moisture sensitivity level 2)	1	
Marking	without	

Notes:

²⁾ Tests according to JEDEC J-STD-020

⁵ Specified in terms of corrosion against Sn-plating



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Electrical specifications and stress test methods

Nominal DC spark-over voltage 3) 4)		230	V
Tolerance		± 20	%
Min. Max.		184 276	V
			V
Impulse spark-over v	oltage		
at 100 V/μs	- for 99% of measured values	< 550	V
	 typical values of distribution 	< 500	V
at 1 kV/μs	- for 99% of measured values	< 650	V
	 typical values of distribution 	< 600	V
Service life 5) 6)			
10 operati	ons 50 Hz, 1 s	5	Α
1 operati	on 50 Hz, 0.18 s (9 cycles)	10	Α
10 operati	ons 8/20 μs	5	kA
1 operati	on 8/20 µs ³⁾	10	kA
1 operati	on 10/350 μs	0.5	kA
300 operati	ons 10/1000 μs	100	Α
Insulation resistance	at 50 V _{DC}	> 1	$G\Omega$
Capacitance at 1 MHz		< 1	pF
Arc voltage at 1 A		~ 10	V
Glow to arc transition current		< 0.3	Α
Glow voltage		~ 50	V

Terms and current waveforms in accordance with ITU-T Rec. K. 12; IEC 61643-21; IEC 61643-311 and IEC 61663-2.

³⁾ At delivery AQL 0.65 level II, DIN ISO 2859
4) In ionized mode
5) Tests according to ITU-T Rec. K. 12 and UL 497B
6) DC spark-over voltage values may exceed ±50% after stress, but tubes still operates w/o venting

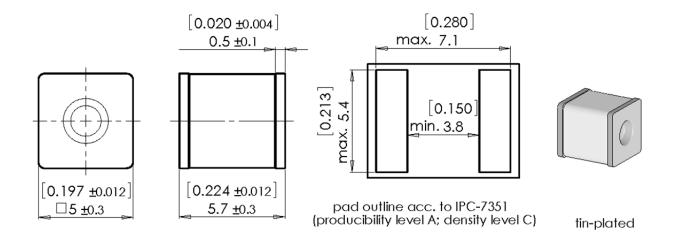


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2-electrode arrester

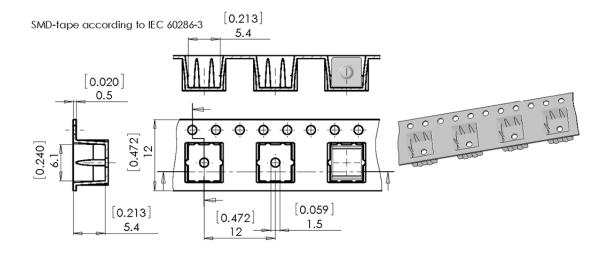
S50-A230X

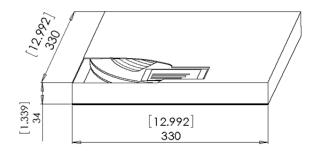
Dimensions in mm and inch [...]



Ordering code and packing advice

B88069X1923**T902** = 900 pcs. on SMD-tape





PPD AB PD / PPD AB PM Version: 02 / 2016-08-19

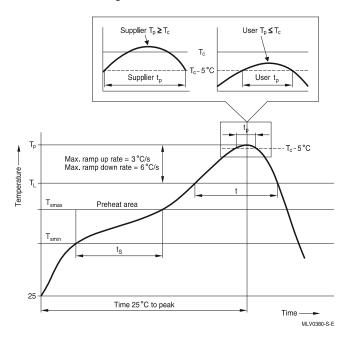


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2-electrode arrester S50-A230X

Soldering parameter

Reflow soldering



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time	$T_{smin} \\ T_{smax} \\ t_{smin} \text{ to } t_{smax}$	100 °C 150 °C 60 120 s	150 °C 200 °C 60 180 s
Average ramp-up rate	T _{smax} to T _p	max. 3 °C/ s	max. 3 °C/ s
Liquidous temperature Time at liquidous	T _L	183 °C 60 150 s	217 °C 60 150 s
Peak package body temperature *, Classification temperature **	T _p , T _C	220 235 °C **	245 260 °C **
Time (t _p) ** within 5 °C of the specified classification temperature (T _C)		20 s ***	30 s ***
Average ramp-down rate	T _p to T _{smax}	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min

^{* =} Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

Surface mounted components (SMD) may exhibit a temporary increase in the DC spark-over voltage after the solder reflow process. The components will recover within 24 hours. There is no quality defect nor change in protection levels during the temporary change in DC spark-over voltage.

Cautions and warnings

- Do not operate surge arresters in power supply networks, whose maximum operating voltage exceeds the minimum spark-over voltage of the surge arresters.
- Surge arresters may become hot in the event of longer periods of current stress (burn risk). In the event of overload the connectors may fail or the component may be destroyed.
- Surge arresters must be handled with care and must not be dropped.
- Do not continue to use damaged surge arresters.
- The shown SMD pad dimensions represent a safe way to mount the arrester and are a recommendation of the manufacturer. During the reflow process it must be assured that no solder material reduces the insulation distance between the pads below the arrester.
- SMD surge arresters should be soldered within 24 month after shipment.

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PPD AB PD / PPD AB PM Version: 02 / 2016-08-19

^{** =} For details please refer to JEDEC J-STD-020D.

^{** =} Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.



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