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## Silicon PIN Diode

- Current-controlled RF resistor for switching and attenuating applications
- Frequency range 1 MHz ... 2 GHz
- Especially useful as antenna switch in TV-sat tuners
- Very low harmonics
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101


BA595
BA885
BA895


| Type | Package | Configuration | $\boldsymbol{L}_{\mathbf{s}}(\mathrm{nH})$ | Marking |
| :--- | :--- | :--- | :---: | :--- |
| BA595 | SOD323 | single | 1.8 | white R |
| BA885 | SOT23 | single | 1.8 | PA |
| BA895 | SCD80 | single | 0.8 | RA |

Maximum Ratings at $T_{\mathrm{A}}=25^{\circ} \mathrm{C}$, unless otherwise specified

| Parameter | Symbol | Value | Unit |
| :--- | :--- | :---: | :--- |
| Diode reverse voltage | $V_{R}$ | 50 | V |
| Forward current | $I_{\mathrm{F}}$ | 50 | mA |
| Junction temperature | $T_{\mathrm{j}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
| Operating temperature range | $T_{\text {op }}$ | $-55 \ldots 125$ |  |
| Storage temperature | $T_{\text {sta }}$ | $-55 \ldots 150$ |  |

Thermal Resistance

| Parameter | Symbol | Value | Unit |
| :--- | :--- | :---: | :--- |
| Junction - soldering point ${ }^{1}$ ) | $R_{\text {thJS }}$ |  | K/W |
| BA595, BA885 |  | $\leq 370$ |  |
| BA895 |  | $\leq 95$ |  |

[^0]BA595/BA885/BA895...

Electrical Characteristics at $T_{\mathrm{A}}=25^{\circ} \mathrm{C}$, unless otherwise specified

| Parameter | Symbol | Values |  |  | Unit |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  |  |  | min. | typ. | max. |  |
| DC Characteristics | $I_{R}$ | - | - | 20 | nA |  |
| Reverse current <br> $V_{R}=30 \mathrm{~V}$ | $V_{\mathrm{F}}$ | - | - | 1.1 | V |  |
| Forward voltage |  |  |  |  |  |  |
| $I_{\mathrm{F}}=50 \mathrm{~mA}$ |  |  |  |  |  |  |


| Diode capacitance | $C_{T}$ |  |  |  | pF |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $V_{\mathrm{R}}=0 \mathrm{~V}, f=100 \mathrm{MHz}$ |  | - | 0.26 | 0.4 |  |
| $V_{\mathrm{R}}=10 \mathrm{~V}, f=1 \mathrm{MHz}$ |  | - | 0.22 | 0.6 |  |
| Reverse parallel resistance | $R_{P}$ |  |  |  | $\mathrm{k} \Omega$ |
| $V_{\mathrm{R}}=1 \mathrm{~V}, f=100 \mathrm{MHz}$ |  | - | 50 | - |  |
| $V_{\mathrm{R}}=0 \mathrm{~V}, f=1 \mathrm{GHz}$ |  | - | 10 | - |  |
| Forward resistance | $r_{\text {f }}$ |  |  |  | $\Omega$ |
| $I_{F}=1.5 \mathrm{~mA}, f=100 \mathrm{MHz}$ |  | - | 22 | 40 |  |
| $I_{F}=10 \mathrm{~mA}, f=100 \mathrm{MHz}$ |  | - | 4.5 | 7 |  |
| Charge carrier life time | $\tau_{\mathrm{rr}}$ | - | 1600 | - | ns |
| $I_{F}=10 \mathrm{~mA}, I_{\mathrm{R}}=6 \mathrm{~mA}$, measured at $I_{R}=3 \mathrm{~mA}$, |  |  |  |  |  |
| $R_{\mathrm{L}}=100 \Omega$ |  |  |  |  |  |
| I-region width | $W_{1}$ | - | 130 | - | $\mu \mathrm{m}$ |

Diode capacitance $C_{\top}=f\left(\mathrm{~V}_{\mathrm{R}}\right)$
$f=$ Parameter


Forward current $I_{F}=f\left(V_{F}\right)$
$T_{\mathrm{A}}=$ Parameter


Forward resistance $r_{\mathrm{f}}=f\left(l_{\mathrm{F}}\right)$
$f=$ Parameter


Forward current $I_{F}=f\left(T_{S}\right)$
BA595


Forward current $I_{\mathrm{F}}=f\left(T_{\mathrm{S}}\right)$
BA895


Permissible Pulse Load
$I_{\text {Fmax }} / I_{\text {FDC }}=f\left(t_{\mathrm{p}}\right)$
BA595


Permissible Puls Load $R_{\text {th } J S}=f\left(t_{\mathrm{p}}\right)$ BA595


Permissible Puls Load $R_{\text {thJS }}=f\left(t_{\mathrm{p}}\right)$ BA595


Permissible Pulse Load
$I_{\text {Fmax }} / I_{\text {FDC }}=f\left(t_{\mathrm{p}}\right)$
BA895


Package Outline


Foot Print


Marking Layout (Example)


## Standard Packing

Reel $\varnothing 180 \mathrm{~mm}=3.000 \mathrm{Pieces} /$ Ree
Reel $\varnothing 180 \mathrm{~mm}=8.000$ Pieces/Reel ( 2 mm Pitch)
Reel $\varnothing 330 \mathrm{~mm}=10.000$ Pieces/Reel


BA595/BA885/BA895...

Date Code marking for discrete packages with one digit (SCD80, SC79, SC751) CES-Code

| Month | 2003 | 2004 | 2005 | 2006 | 2007 | 2008 | 2009 | 2010 | 2011 | 2012 | 2013 | 2014 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 01 | a | p | A | P | a | p | A | P | a | p | A | P |
| 02 | b | q | B | Q | b | q | B | Q | b | q | B | Q |
| 03 | c | $r$ | C | R | c | $r$ | C | R | c | $r$ | C | R |
| 04 | d | s | D | S | d | s | D | S | d | s | D | S |
| 05 | e | t | E | T | e | t | E | T | e | t | E | T |
| 06 | $f$ | $u$ | F | U | $f$ | $u$ | F | U | $f$ | $u$ | F | U |
| 07 | g | v | G | V | g | v | G | V | g | v | G | V |
| 08 | h | x | H | X | h | x | H | X | h | x | H | X |
| 09 | j | y | $J$ | Y | j | y | J | Y | j | y | J | Y |
| 10 | k | z | K | Z | k | z | K | Z | k | z | K | Z |
| 11 | I | 2 | L | 4 | I | 2 | L | 4 | I | 2 | L | 4 |
| 12 | n | 3 | N | 5 | n | 3 | N | 5 | n | 3 | N | 5 |

1) New Marking Layout for SC75, implemented at October 2005.

## Package Outline



Foot Print


Marking Layout (Example)


Color ink or laser marking

## Standard Packing

Reel $\varnothing 180 \mathrm{~mm}=3.000$ Pieces/Reel
Reel $\varnothing 330 \mathrm{~mm}=10.000$ Pieces/Reel


Package Outline


1) Lead width can be 0.6 max. in dambar area

Foot Print


Marking Layout (Example)


Standard Packing
Reel $\varnothing 180 \mathrm{~mm}=3.000$ Pieces/Reel
Reel $\varnothing 330 \mathrm{~mm}=10.000$ Pieces/Reel


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[^0]:    ${ }^{1}$ For calculation of $R_{\text {thJA }}$ please refer to Application Note Thermal Resistance

