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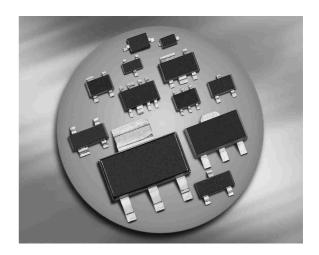




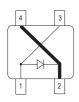
Silicon RF Switching Diode

- Designed for use in shunt configuration in high performance RF switches
- High shunt signal isolation
- Low shunt insertion loss
- Optimized for short open transformation using $\lambda/4$ lines
- Pb-free (RoHS compliant) package





BAR81W



Туре	Package	Configuration	L S(nH)	Marking
BAR81W	SOT343	single shunt-diode	0.15*	BBs

^{*} series inductance chip to ground

Maximum Ratings at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Diode reverse voltage	V_{R}	30	V
Forward current	I _F	100	mA
Total power dissipation	P _{tot}	100	mW
<i>T</i> _S ≤ 138°C			
Junction temperature	T_{i}	150	°C
Operating temperature range	T_{op}	-55 125	
Storage temperature	T _{stg}	-55 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ¹⁾	R _{thJS}	≤ 120	K/W

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 $^{^{1}\}mathrm{For}$ calculation of R_{thJA} please refer to Application Note Thermal Resistance

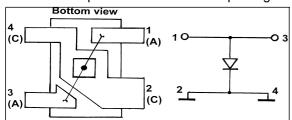


Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics		•	•	•	
Reverse current	I_{R}	-	-	20	nA
V _R = 20 V					
Forward voltage	V_{F}	-	0.93	1	V
<i>I</i> _F = 100 mA					
AC Characteristics					
Diode capacitance	C _T				pF
$V_{R} = 1 \text{ V}, f = 1 \text{ MHz}$		-	0.6	1	
$V_{R} = 3 \text{ V}, f = 1 \text{ MHz}$		-	0.57	0.9	
Forward resistance	r_{f}	-	0.7	1	Ω
$I_{\rm F}$ = 5 mA, f = 100 MHz					
Charge carrier life time	τ _{rr}	-	80	-	ns
$I_{\rm F}$ = 10 mA, $I_{\rm R}$ = 6 mA, measured at $I_{\rm R}$ = 3 mA,					
R_{L} = 100 Ω					
I-region width	W_{I}	-	3.5	-	μm
Shunt Insertion loss ¹⁾	<i>I</i> L	_	30	_	dB
<i>I</i> _F = 10 mA, <i>f</i> = 1.89 GHz					
Shunt isolation ¹⁾	I _{SO}	_	0.7	_	
V _R = 3 V, f = 1.89 GHz					

Configuration of the shunt-diode

- A perfect ground is essential for optimum isolation
- The anode pins should be used as passage for RF



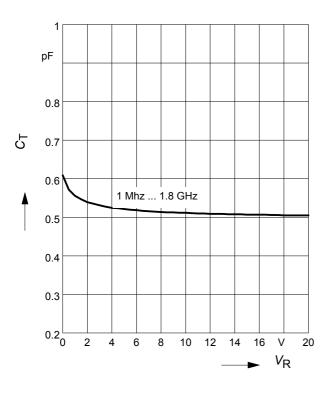
¹For more information please refer to Application Note 049.

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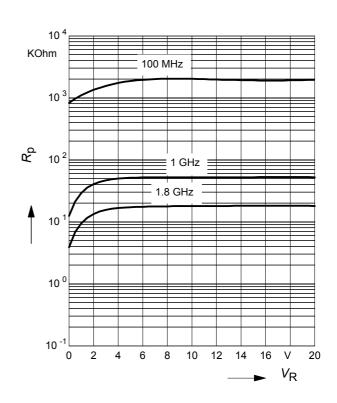
Diode capacitance $C_T = f(V_R)$

f = Parameter



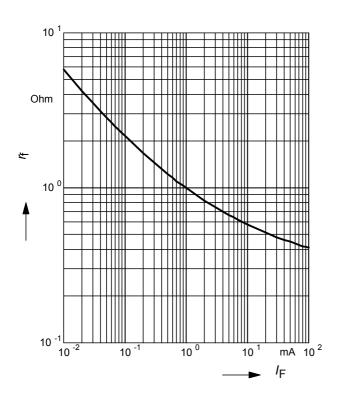
Reverse parallel resistance $R_P = f(V_R)$

f = Parameter



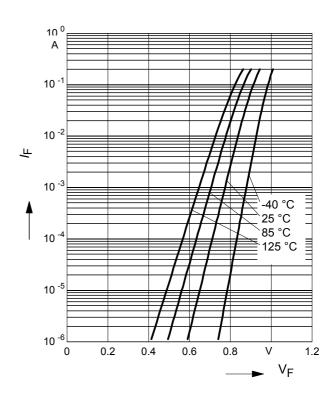
Forward resistance $r_f = f(I_F)$

f = 100MHz



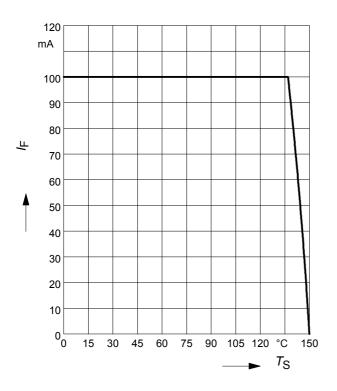
Forward current $I_F = f(V_F)$

 T_A = Parameter

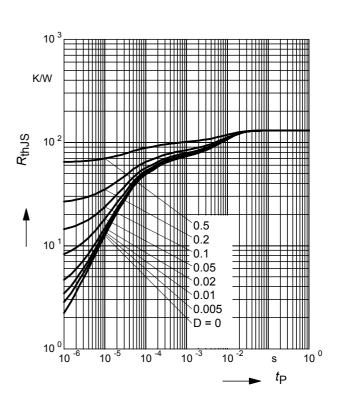




Forward current $I_F = f(T_S)$ BAR81W

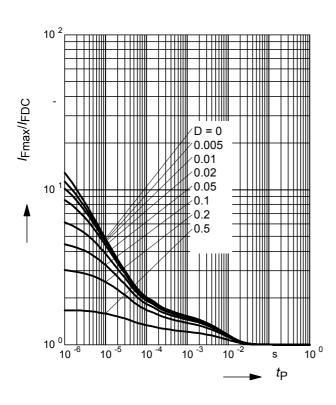


Permissible Puls Load $R_{thJS} = f(t_p)$ BAR81W



Permissible Pulse Load

$$I_{\text{Fmax}}/I_{\text{FDC}} = f(t_{\text{p}})$$
 BAR81W

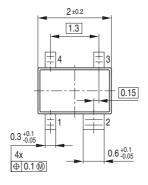


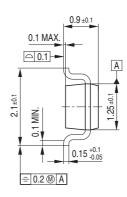
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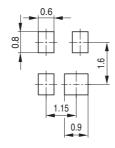
Package Outline



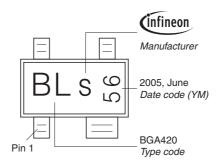




Foot Print

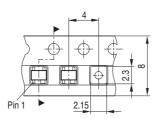


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel





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