imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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BAT60A...

Silicon Schottky Diode

- High current rectifier Schottky diode with extreme low $V_{\rm F}$ drop (typ. 0.12V at $I_{\rm F}$ = 10mA)
- For power supply applications
- For clamping and protection in low voltage applications
- For detection and step-up-conversion
- Pb-free (RoHS compliant) package¹⁾
- Qualified according AEC Q101



BAT60A



ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Package	Configuration	Marking
BAT60A	SOD323	single	white/3

Maximum Ratings at $T_A = 25^{\circ}$ C, unless otherwise specified

Parameter	Symbol	Value	Unit	
Diode reverse voltage ²⁾	V _R	10	V	
Forward current	/ _F	3	А	
Non-repetitive peak surge forward current	/ _{FSM}	5		
(<i>t</i> ≤ 10ms)				
Total power dissipation	P _{tot}	1350	mW	
$T_{\rm S} \le 28^{\circ}{\rm C}$				
Junction temperature	Ti	150	°C	
Operating temperature range	T _{op}	-55 85		
Storage temperature	T _{stg}	-55 150		

¹Pb-containing package may be available upon special request

²For $T_A > 25$ °C the derating of V_R has to be considered. Please refer to curve Permissible reverse voltage.



Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ¹⁾	R _{thJS}	≤ 90	K/W

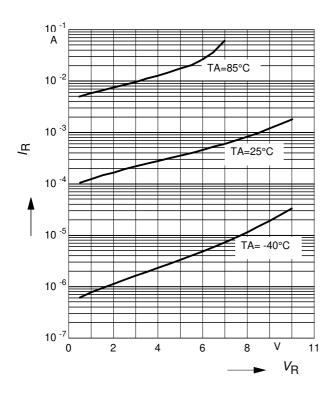
Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics				•	-
Reverse current ²⁾	I _R				mA
$V_{R} = 5 V$		-	0.3	1	
$V_{R} = 8 V$		-	0.6	2.6	
$V_{\rm R} = 5 \rm V, T_{\rm A} = 80 ^{\circ} \rm C$		-	18	-	
Forward voltage ²⁾	V _F				V
<i>I</i> _F = 10 mA		0.1	0.12	0.15	
<i>I</i> _F = 100 mA		0.15	0.2	0.23	
<i>I</i> _F = 1000 mA		0.22	0.3	0.37	
AC Characteristics			1	1	
Diode capacitance	CT	-	20	35	pF
<i>V</i> _R = 5 V, <i>f</i> = 1 MHz					

¹For calculation of $R_{\rm thJA}$ please refer to Application Note Thermal Resistance ²Pulsed test: t_p = 300 µs; D = 0.01



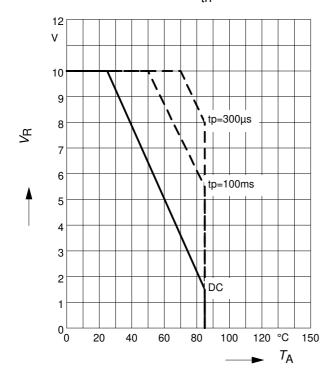
Reverse current $I_{\rm R} = f(V_{\rm R})$

 $T_{A} = Parameter$



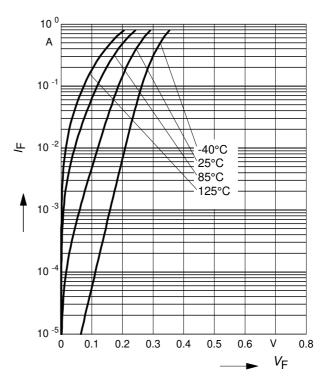
Permissible Reverse voltage $V_{\rm R} = f(T_{\rm A})$ $t_{\rm p}$ = Parameter; duty cycle < 0.01

Device mounted on PCB with $R_{\text{th}} = 160 \text{ K/W}$

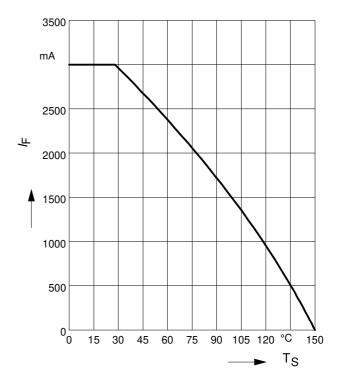


Forward current $I_{\rm F} = f (V_{\rm F})$

 T_A = Parameter



Forward current $I_{\rm F} = f(T_{\rm S})$

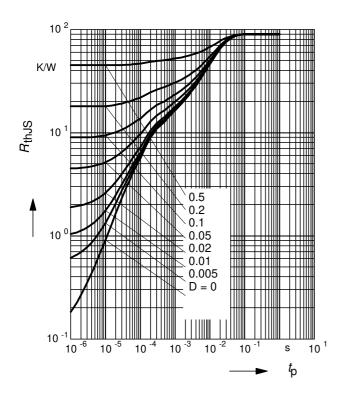


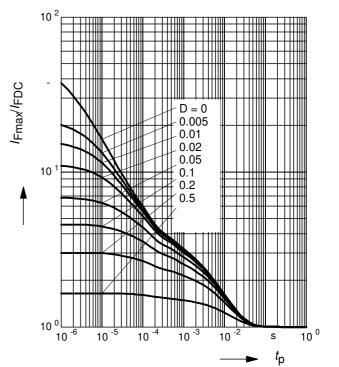


Permissible Puls Load $R_{thJS} = f(t_p)$

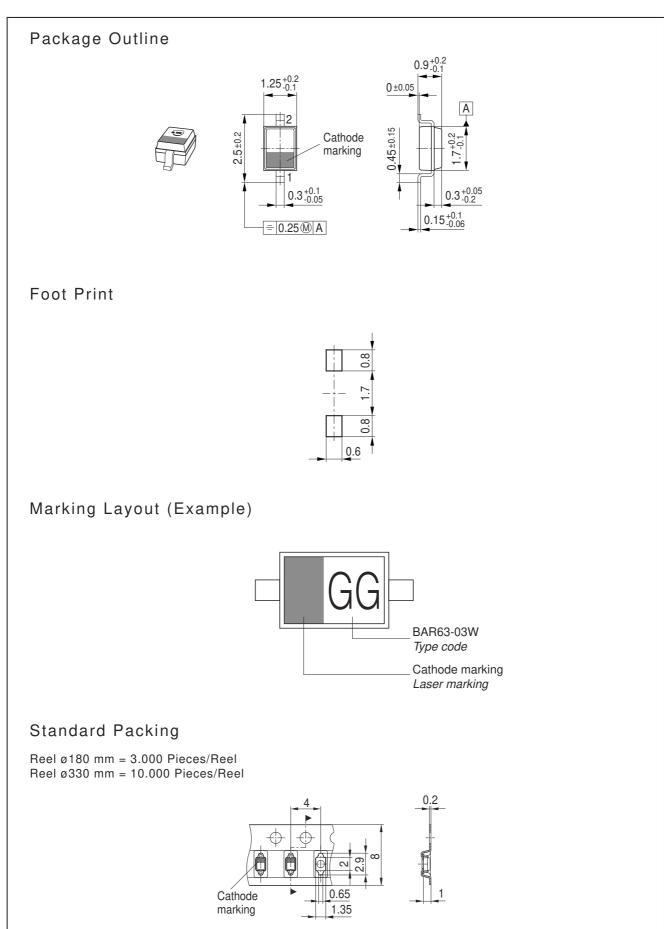
Permissible Pulse Load

 $I_{\text{Fmax}} / I_{\text{FDC}} = f(t_{\text{p}})$











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