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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







1. General description

Dual common cathode high-voltage switching diode, encapsulated in a leadless ultra small DFN1010D-3 (SOT1215) Surface-Mounted Device (SMD) plastic package with visible and soldarable side pads.

2. Features and benefits

- High switching speed: t_{rr} ≤ 50 ns
- Low leakage current: I_R ≤ 100 nA
- High reverse voltage: V_R ≤ 200 V
- Low capacitance: C_d ≤ 2 pF
- · Ultra small and leadless SMD plastic package
- Low package height of 0.37 mm
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- AEC-Q101 qualified

3. Applications

- · High-speed switching
- · General-purpose switching
- Voltage clamping
- Reverse polarity protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per diode	Per diode						
l _F	forward current	single diode loaded; T _j = 25 °C	[1]	-	-	330	mA
V_R	reverse voltage	T _j = 25 °C		-	-	200	V
V_{RRM}	repetitive peak reverse voltage			-	-	250	V
V _F	forward voltage	I_F = 200 mA; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 25 °C		-	-	1.25	V
I _R	reverse current	V _R = 200 V; pulsed; T _j = 25 °C		-	-	100	nA
t _{rr}	reverse recovery time	I_F = 30 mA; I_R = 30 mA; R_L = 100 Ω; $I_{R(meas)}$ = 3 mA; T_j = 25 °C		-	-	50	ns

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



Dual common cathode high-voltage switching diode

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode (diode 1)		
2	A2	anode (diode 2)		A1
3	CC	common cathode	4 3	cc
4	CC	common cathode	Transparent top view DFN1010D-3 (SOT1215)	A2

6. Ordering information

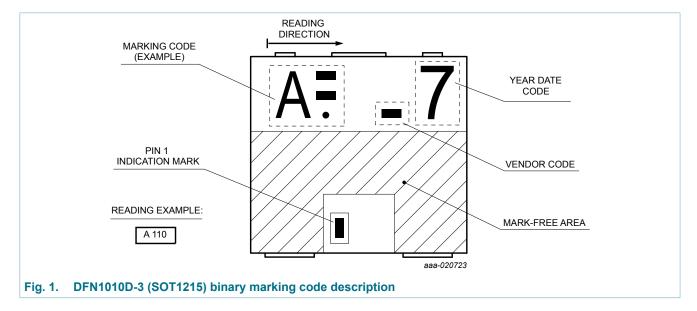
Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BAV23QA	DFN1010D-3	plastic, thermal enhanced ultra thin small outline package; 3 terminals; 0.75 mm pitch; 1.1 mm x 1 mm x 0.37 mm body	SOT1215		

7. Marking

Table 4. Marking codes

Type number	Marking code
BAV23QA	X 010



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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating Sytem (IEC 60134)

Symbol	Parameter	Conditions		Min	Max	Unit
Per diode						
V_{RRM}	repetitive peak reverse voltage	T _j = 25 °C		-	250	V
V _R	reverse voltage			-	200	V
l _F	forward current	single diode loaded; T _j = 25 °C	[1]	-	330	mA
		double diode loaded; T _j = 25 °C	[1]	-	190	mA
	non-repetitive peak	t_p = 1 μ s; $T_{j(init)}$ = 25 °C; square wave		-	9	Α
	forward current	t_p = 100 μ s; $T_{j(init)}$ = 25 °C; square wave		-	3	Α
		t _p = 10 ms; T _{j(init)} = 25 °C; square wave		-	1.7	Α
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	900	mA
Per device,	one diode loaded					,
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	350	mW
			[2]	-	610	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
fro	thermal resistance from junction to ambient	In free air	[1]	-	-	355	K/W
			[2]	-	-	205	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[3]	-	-	45	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated mounting pad for cathode 1cm².

^{2]} Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for cathode 1cm².

^[3] Soldering point of cathode tab.

Dual common cathode high-voltage switching diode

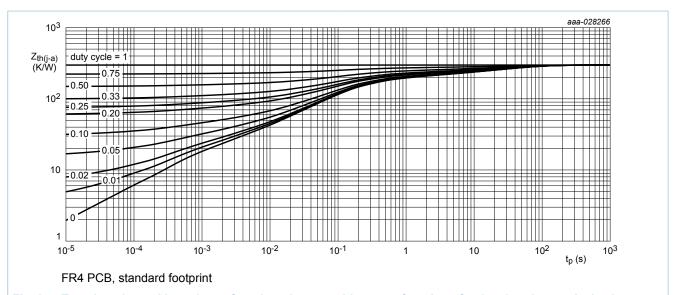


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

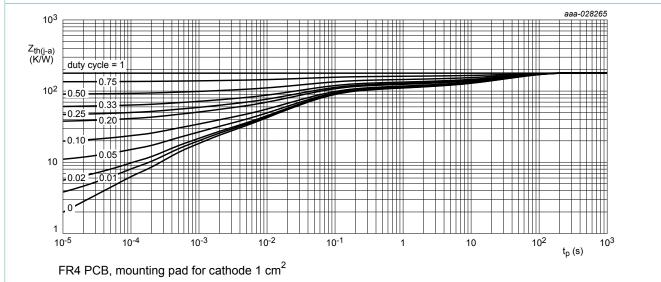


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
V _F	forward voltage	I_F = 100 mA; t_p ≤ 300 μs; δ ≤ 0.02; T_j = 25 °C	-	-	1	V
		I_F = 200 mA; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 25 °C	-	-	1.25	V
I _R	reverse current	V_R = 200 V; pulsed; T_j = 25 °C	-	-	100	nA
		V _R = 200 V; pulsed; T _j = 150 °C	-	-	100	μA
C _d	diode capacitance	V _R = 0 V; f = 1 MHz; T _j = 25 °C	-	-	2	pF
t _{rr}	reverse recovery time	I_F = 30 mA; I_R = 30 mA; R_L = 100 Ω; $I_{R(meas)}$ = 3 mA; T_j = 25 °C	-	-	50	ns

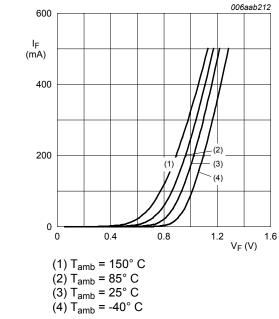


Fig. 4. Forward current as a function of forward voltage; typical values

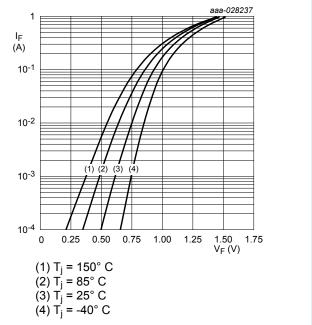


Fig. 5. Forward current as a function of forward voltage; typical values; (logarithmic scale)

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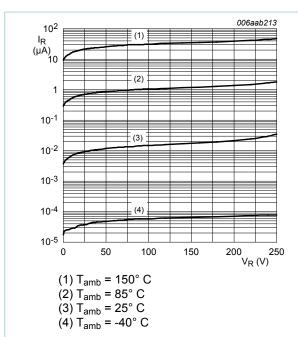
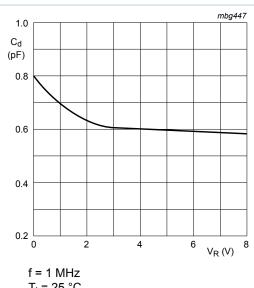
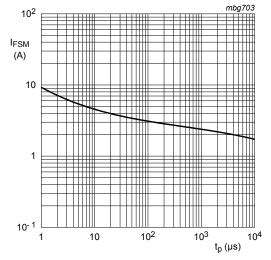


Fig. 6. Reverse current as a function of reverse voltage; typical values



 $T_i = 25$ °C.

Fig. 7. Diode capacitance as a function of reverse voltage; typical values.



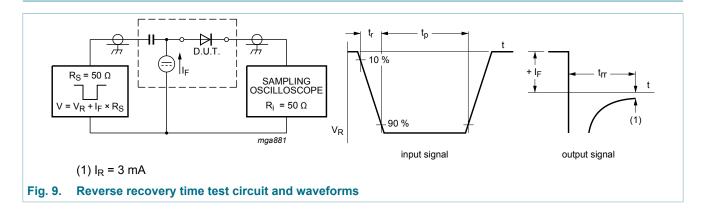
Based on square wave currents.

 $T_{j(init)} = 25 \, ^{\circ}C$

Non-repetitive peak forward current as a function of pulse duration; maximum values Fig. 8.

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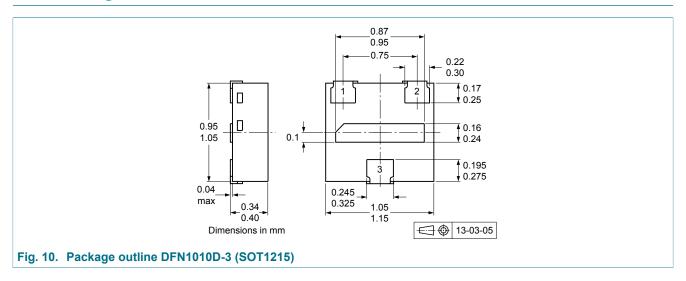
11. Test information



Quality information

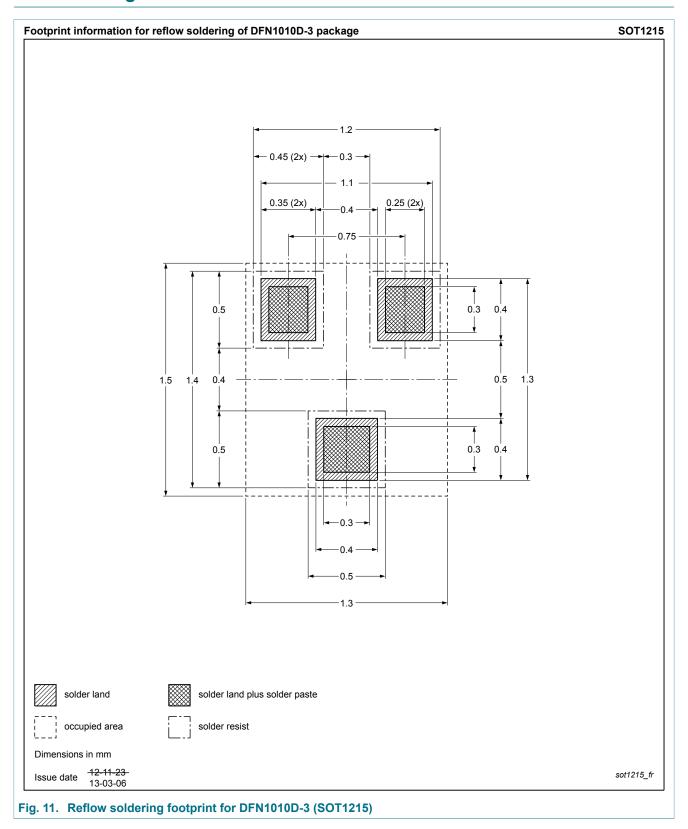
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BAV23QA v.1	20180409	Product data sheet	-	-

Dual common cathode high-voltage switching diode

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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