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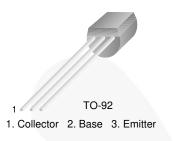
October 2014



BC327 PNP Epitaxial Silicon Transistor

Features

- Switching and Amplifier Applications
- · Suitable for AF-Driver Stages and Low-Power Output Stages
- Complement to BC337 / BC338



Ordering Information

Part Number	Top Mark	Package	Packing Method
BC327BU	BC327	TO-92 3L	Bulk
BC32716BU	BC32716	TO-92 3L	Bulk
BC32716TA	BC32716	TO-92 3L	Ammo
BC32725BU	BC32725	TO-92 3L	Bulk
BC32725TA	BC32725	TO-92 3L	Ammo
BC32740BU	BC32740	TO-92 3L	Bulk
BC32740TA	BC32740	TO-92 3L	Ammo

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CES}	Collector-Emitter Voltage	-50	V
V _{CEO}	Collector-Emitter Voltage	-45	V
V _{EBO}	Emitter-Base Voltage	-5	V
Ι _C	Collector Current (DC)	-800	mA
ТJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 to 150	°C

Thermal Characteristics⁽¹⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
р	Power Dissipation	625	mW
PD	Derate Above 25°C	5.0	mW/°C
R _{θJA}	Thermal Resistance, Junction-to-Ambient	200	°C/W

Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

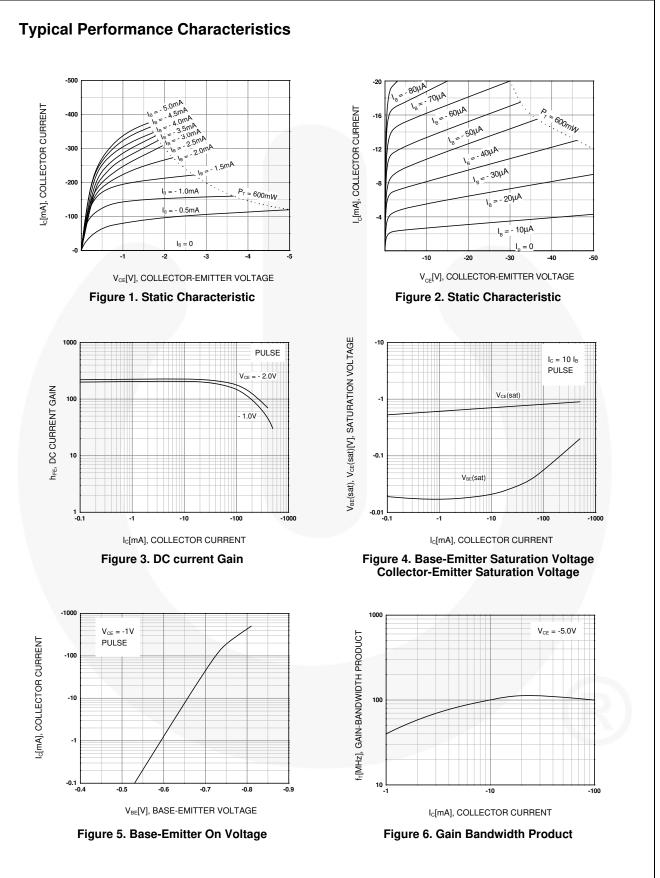
Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = -10 \text{ mA}, I_{\rm B} = 0$	-45			V
BV _{CES}	Collector-Emitter Breakdown Voltage	$I_{C} = -0.1 \text{ mA}, V_{BE} = 0$	-50			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_{E} = -10 \ \mu A, \ I_{C} = 0$	-5			V
I _{CES}	Collector Cut-Off Current	$V_{CE} = -45 \text{ V}, \text{ I}_{B} = 0$		-2	-100	nA
h _{FE1}	DC Current Gain	$V_{CE} = -1 V, I_{C} = -100 mA$	100		630	
h _{FE2}		$V_{CE} = -1 V, I_{C} = -300 mA$	60			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_{C} = -500 \text{ mA}, I_{B} = -50 \text{ mA}$			-0.7	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = -1 V, I_{C} = -300 mA$			-1.2	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -5 V$, $I_C = -10 mA$, f = 20 MHz		100		MHz
C _{ob}	Output Capacitance			12		pF

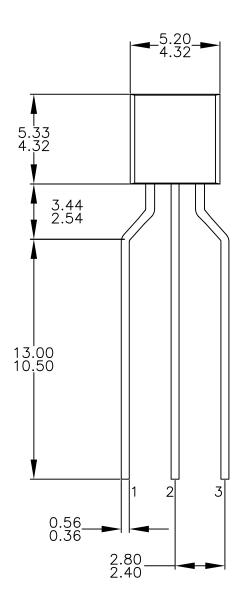
h_{FE} Classification

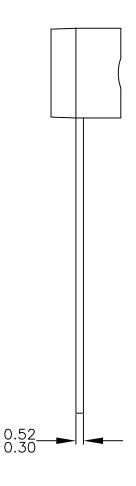
Classification	16	25	40
h _{FE1}	100 ~ 250	160 ~ 400	250 ~ 630
h _{FE2}	60 ~	100 ~	170 ~



BC327 — PNP Epitaxial Silicon Transistor

BC327 Rev. 1.1.0

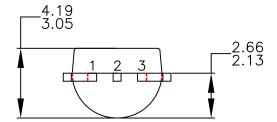


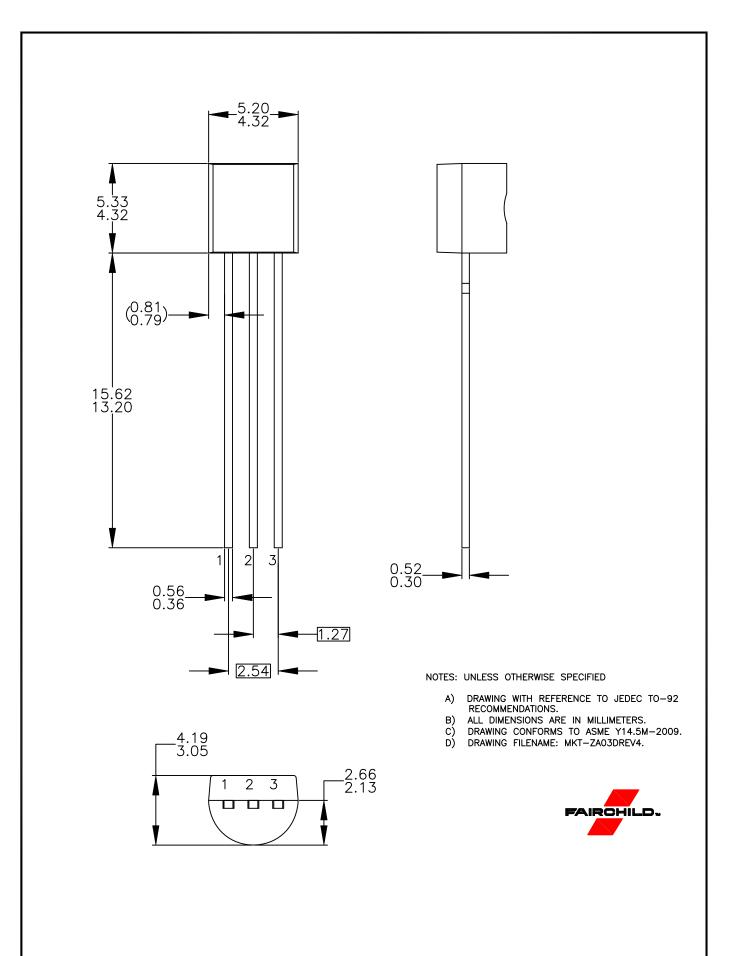


NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZA03FREV3. FAIRCHILD SEMICONDUCTOR. Α.

- В. С. D. Е.





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