# imall

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## High Current Transistors NPN Silicon

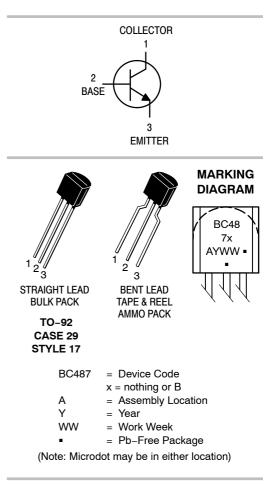
### Features

• Pb-Free Packages are Available\*



## **ON Semiconductor®**

http://onsemi.com



## ORDERING INFORMATION

| Device    | Package            | Shipping <sup>†</sup> |  |  |  |  |
|-----------|--------------------|-----------------------|--|--|--|--|
| BC487     | TO-92              | 5000 Units / Box      |  |  |  |  |
| BC487G    | TO-92<br>(Pb-Free) | 5000 Units / Box      |  |  |  |  |
| BC487B    | TO-92              | 5000 Units / Box      |  |  |  |  |
| BC487BG   | TO–92<br>(Pb–Free) | 5000 Units / Box      |  |  |  |  |
| BC487BRL1 | TO-92              | 2000/Tape & Reel      |  |  |  |  |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### MAXIMUM RATINGS

| Rating  | Symbol                            | Value       | Unit        |
|---|-----------------------------------|-------------|-------------|
| Collector – Emitter Voltage   | V <sub>CEO</sub>                  | 60          | Vdc         |
| Collector - Base Voltage  | V <sub>CBO</sub>                  | 60          | Vdc         |
| Emitter – Base Voltage  | V <sub>EBO</sub>                  | 5.0         | Vdc         |
| Collector Current – Continuous  | Ι <sub>C</sub>                    | 0.5         | Adc         |
| Total Device Dissipation @ T <sub>A</sub> = 25°C<br>Derate above 25°C | P <sub>D</sub>                    | 625<br>5.0  | mW<br>mW/°C |
| Total Device Dissipation @ T <sub>C</sub> = 25°C<br>Derate above 25°C | P <sub>D</sub>                    | 1.5<br>12   | W<br>mW/°C  |
| Operating and Storage Junction<br>Temperature Range                   | T <sub>J</sub> , T <sub>stg</sub> | -55 to +150 | °C          |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### THERMAL CHARACTERISTICS

| Characteristic                             | Symbol          | Max  | Unit |
|--|-----------------|------|------|
| Thermal Resistance,<br>Junction-to-Ambient | $R_{\theta JA}$ | 200  | °C/W |
| Thermal Resistance,<br>Junction-to-Case    | $R_{\theta JC}$ | 83.3 | °C/W |

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

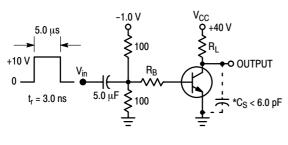
## BC487, BC487B

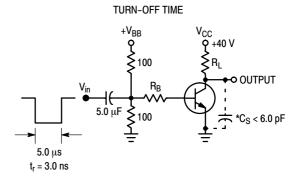
### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

| Characteristic  | Symbol               | Min                   | Тур                | Max                  | Unit |
|---|----------------------|-----------------------|--------------------|----------------------|------|
| OFF CHARACTERISTICS   | 1                    |                       | 1                  |                      |      |
| Collector – Emitter Breakdown Voltage (Note 1)<br>( $I_C = 10 \text{ mAdc}, I_B = 0$ )  | V <sub>(BR)CEO</sub> | 60                    | -                  | -                    | Vdc  |
| Collector – Base Breakdown Voltage $(I_C = 100 \ \mu Adc, I_E = 0)$   | V <sub>(BR)CBO</sub> | 60                    | -                  | -                    | Vdc  |
| Emitter – Base Breakdown Voltage<br>( $I_E = 10 \ \mu Adc, I_C = 0$ )   | V <sub>(BR)EBO</sub> | 5.0                   | -                  | -                    | Vdc  |
| Collector Cutoff Current<br>( $V_{CB} = 40 \text{ Vdc}, I_E = 0$ )  | I <sub>CBO</sub>     | -                     | _                  | 100                  | nAdc |
| ON CHARACTERISTICS*   |                      | •                     |                    |                      |      |
| $\label{eq:loss} \begin{array}{l} \text{DC Current Gain} \\ (I_C = 10 \text{ mAdc}, V_{CE} = 2.0 \text{ Vdc}) \\ (I_C = 100 \text{ mAdc}, V_{CE} = 2.0 \text{ Vdc}) \\ & & & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & \\ & & & & & \\ & & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & &$ | h <sub>FE</sub>      | 40<br>60<br>160<br>15 | -<br>-<br>260<br>- | -<br>400<br>400<br>- | _    |
| Collector – Emitter Saturation Voltage<br>( $I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc}$ )<br>( $I_C = 1.0 \text{ Adc}, I_B = 100 \text{ mAdc}$ )  | V <sub>CE(sat)</sub> |                       | 0.2<br>0.3         | 0.5                  | Vdc  |
| Base – Emitter Saturation Voltage<br>( $I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc}$ )<br>( $I_C = 1.0 \text{ Adc}, I_B = 100 \text{ mAdc}$ ) <sup>(1)</sup>  | V <sub>BE(sat)</sub> |                       | 0.85<br>0.9        | 1.2<br>-             | Vdc  |
| DYNAMIC CHARACTERISTICS   |                      |                       |                    |                      |      |
| Current–Gain – Bandwidth Product<br>(I <sub>C</sub> = 50 mAdc, V <sub>CE</sub> = 2.0 Vdc, f = 100 MHz)  | f <sub>T</sub>       | -                     | 200                | -                    | MHz  |
| Output Capacitance<br>(V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)   | C <sub>ob</sub>      | -                     | 7.0                | -                    | pF   |
| Input Capacitance $(V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz})$  | C <sub>ib</sub>      | -                     | 50                 | -                    | pF   |

1. Pulse Test: Pulse Width = 300 μs, Duty Cycle 2.0%.

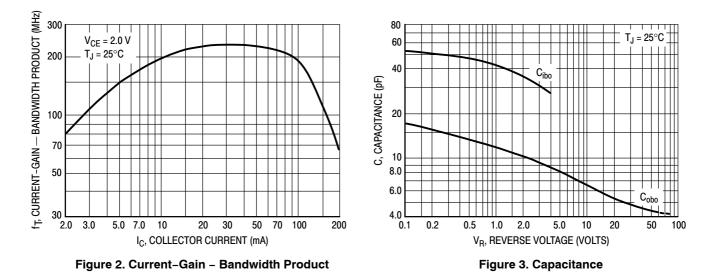


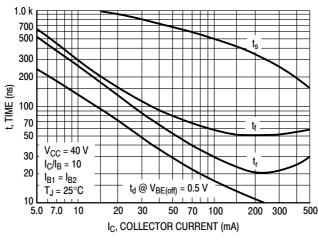


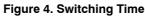


\*Total Shunt Capacitance of Test Jig and Connectors For PNP Test Circuits, Reverse All Voltage Polarities

Figure 1. Switching Time Test Circuits







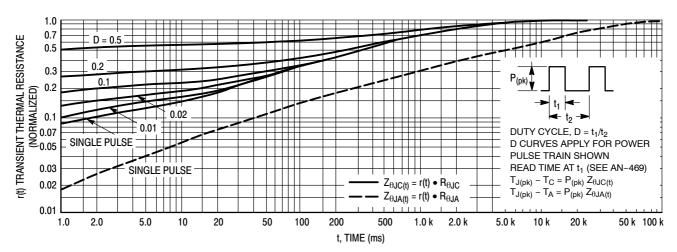


Figure 5. Thermal Response

## BC487, BC487B

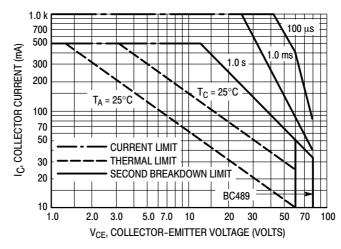
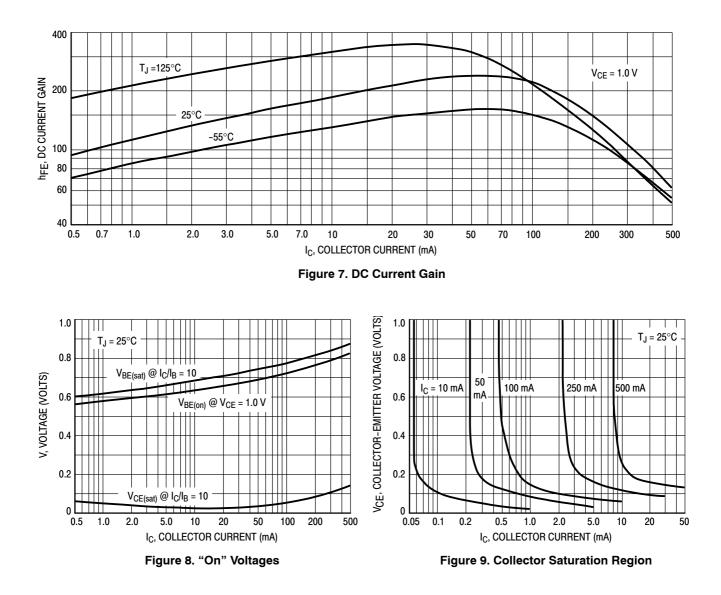
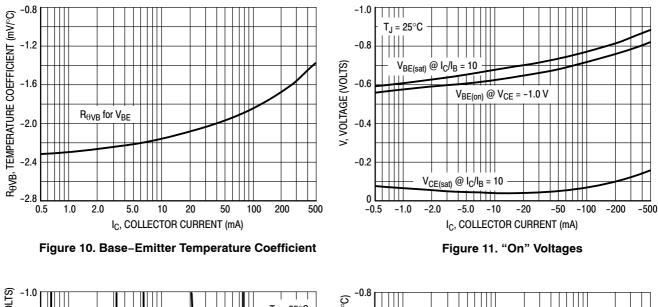


Figure 6. Active Region – Safe Operating Area



## BC487, BC487B



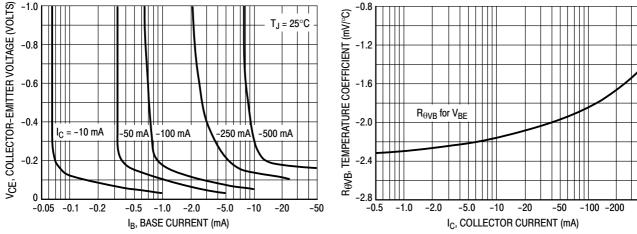
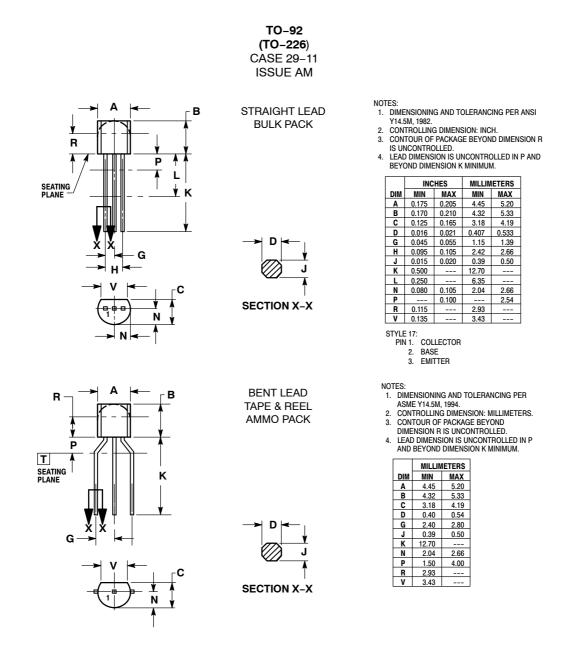


Figure 12. Collector Saturation Region

Figure 13. Base-Emitter Temperature Coefficient

-500

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