imall

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High Current Transistors NPN Silicon

Features

• These are Pb–Free Devices*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	80	Vdc
Collector-Base Voltage	V _{CBO}	80	Vdc
Collector-Emitter Voltage	V _{EBO}	5.0	Vdc
Collector Current – Continuous	Ι _C	0.5	Adc
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $T_A = 25^{\circ}C$	PD	625 5.0	mW mW/°C
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $T_A = 25^{\circ}C$	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

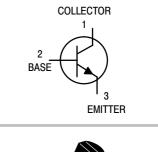
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	R_{\thetaJC}	83.3	°C/W

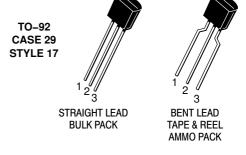
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



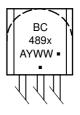
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



489x = 489A

A Y

WW

= Assembly Location

= Year

= Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
BC489G	TO–92 (Pb–Free)	5000 Units / Bulk
BC489RL1G	TO–92 (Pb–Free)	2000 / Tape & Reel
BC489AG	TO–92 (Pb–Free)	5000 Units / Bulk

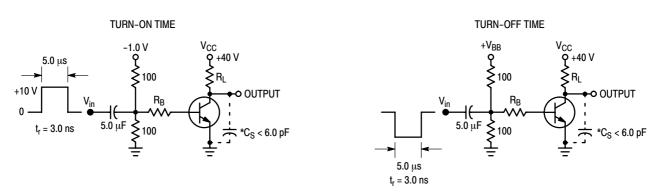
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

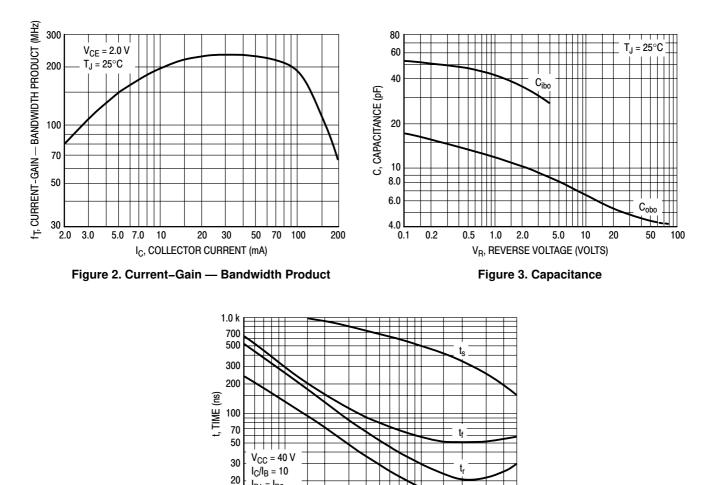
Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•			•	
Collector – Emitter Breakdown Voltage (Note 1) $(I_C = 10 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	80	_	_	Vdc
Collector – Base Breakdown Voltage $(I_C = 100 \ \mu Adc, I_E = 0)$	V _{(BR)CBO}	80	_	_	Vdc
Emitter – Base Breakdown Voltage $(I_E = 10 \ \mu Adc, I_C = 0)$	V _{(BR)EBO}	5.0	-	-	Vdc
Collector Cutoff Current $(V_{CB} = 60 \text{ V}, I_E = 0)$	I _{CBO}	_	_	100	nAdc
ON CHARACTERISTICS					•
$ \begin{array}{l} \text{DC Current Gain} \\ (I_C = 10 \text{ mAdc}, V_{CE} = 2.0 \text{ Vdc}) \\ (I_C = 100 \text{ mAdc}, V_{CE} = 2.0 \text{ Vdc}) \\ \end{array} \\ \end{array} \\ \begin{array}{l} \text{BC489} \\ \text{BC489A} \end{array} $	h _{FE}	40 60 100	- - 160	_ 400 250	-
(I _C = 1.0 Adc, V _{CE} = 5.0 Vdc)		15	-	-	
Collector – Emitter Saturation Voltage $(I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc})$ $(I_C = 1.0 \text{ Adc}, I_B = 100 \text{ mAdc})$	V _{CE(sat)}		0.2 0.3	0.5 -	Vdc
Collector – Emitter Saturation Voltage ($I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc}$) ($I_C = 1.0 \text{ Adc}, I_B = 100 \text{ mAdc}$) (Note 1)	V _{BE(sat)}		0.85 0.9	1.2 -	Vdc
DYNAMIC CHARACTERISTICS			-		
Current–Gain – Bandwidth Product ($I_C = 50 \text{ mAdc}, V_{CE} = 2.0 \text{ Vdc}, f = 100 \text{ MHz}$)	f _T	-	200	_	MHz
Output Capacitance $(V_{CB} = 10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C _{ob}	-	7.0	-	pF
Input Capacitance ($V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz}$)	C _{ib}	-	50	_	pF

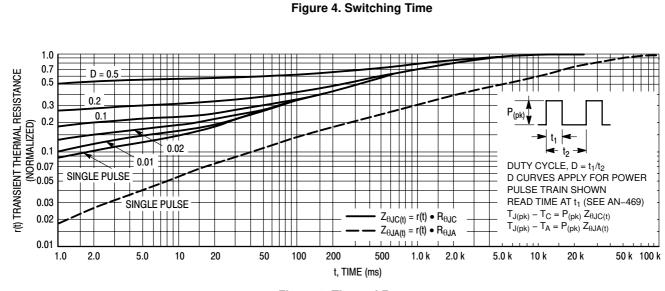
1. Pulse Test: Pulse Width = 300 μs, Duty Cycle 2.0%.



*Total Shunt Capacitance of Test Jig and Connectors For PNP Test Circuits, Reverse All Voltage Polarities

Figure 1. Switching Time Test Circuits





td @ VBE(off) = 0.5 V

50

IC, COLLECTOR CURRENT (mA)

70 100

300

200

500

30

20

 $I_{B1} = I_{B2}$ $T_J = 25^{\circ}C$

5.0 7.0 10

10



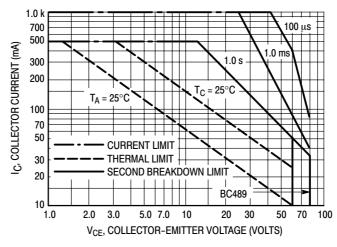
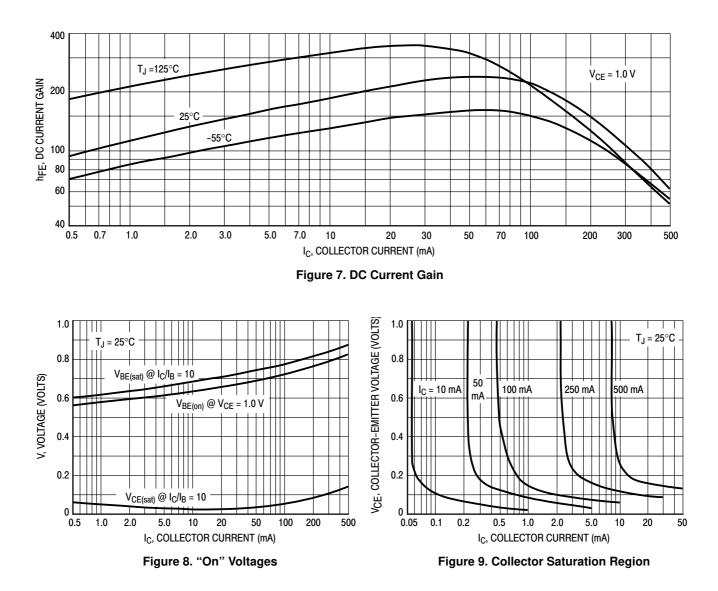


Figure 6. Active Region — Safe Operating Area



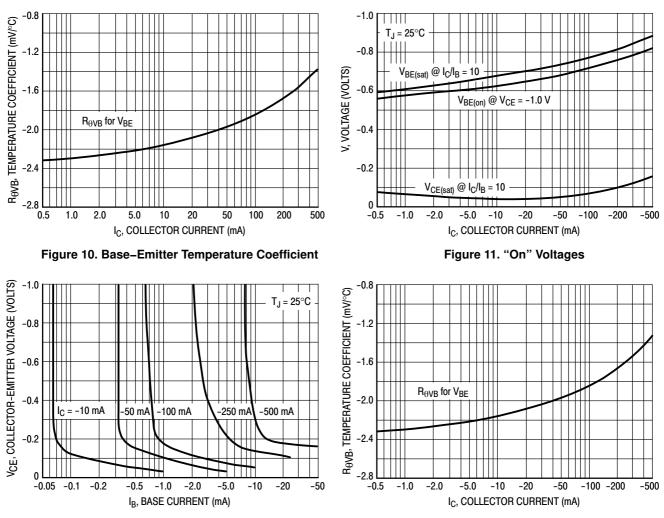
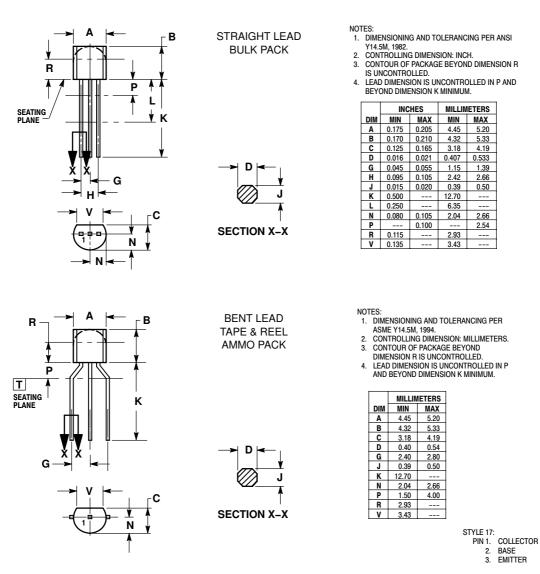


Figure 12. Collector Saturation Region

Figure 13. Base–Emitter Temperature Coefficient

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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