# imall

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PNP Epitaxial Silicon Transistor

# Features

- Switching and Amplifier
- High-Voltage: BC556, V<sub>CEO</sub> = -65 V
- Low-Noise: BC559, BC560
- Complement to BC546, BC547, BC548, BC549, and BC550



Straight Lead Be Bulk Packing Tap Amm

Bent Lead Tape & Reel Ammo Packing

# **Ordering Information**

Part Number	Marking	Package	Packing Method	
BC556ABU	BC556A	TO-92 3L	Bulk	
BC556ATA	BC556A	TO-92 3L	Ammo	
BC556BTA	BC556B	TO-92 3L	Ammo	
BC556BTF	BC556B	TO-92 3L	Tape and Reel	
BC556BTFR	BC556B	TO-92 3L	Tape and Reel	
BC557ATA	BC557A	TO-92 3L	Ammo	
BC557BTA	BC557B	TO-92 3L	Ammo	
BC557BTF	BC557B	TO-92 3L	Tape and Reel	
BC558BTA BC558B		TO-92 3L	Ammo	
BC559BTA BC559B		TO-92 3L	Ammo	
BC559CTA	BC559C	TO-92 3L	Ammo	
BC560CTA BC560C		TO-92 3L	Ammo	

# BC556 / BC557 / BC558 / BC559 / BC560 — PNP Epitaxial Silicon Transistor

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Paramet	Value	Unit	
		BC556	-80	
V <sub>CBO</sub>	Collector-Base Voltage	BC557 / BC560	-50	V
		BC558 / BC559	-30	1
		BC556	-65	
$V_{CEO}$	Collector-Emitter Voltage	BC557 / BC560	-45	V
		BC558 / BC559	-30	
$V_{\text{EBO}}$	Emitter-Base Voltage		-5	V
۱ <sub>C</sub>	Collector Current (DC)		-100	mA
I <sub>CP</sub>	Peak Collector Current (Pulse)		-200	mA
I <sub>BP</sub>	Peak Base Current (Pulse)		-200	mA
Т <sub>Ј</sub>	Junction Temperature		150	°C
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C

# Thermal Characteristics<sup>(1)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
р	Total Power Dissipation	500	mW
P <sub>D</sub>	Derate Above 25°C	4.0	mW/°C
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient250		°C/W

# Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

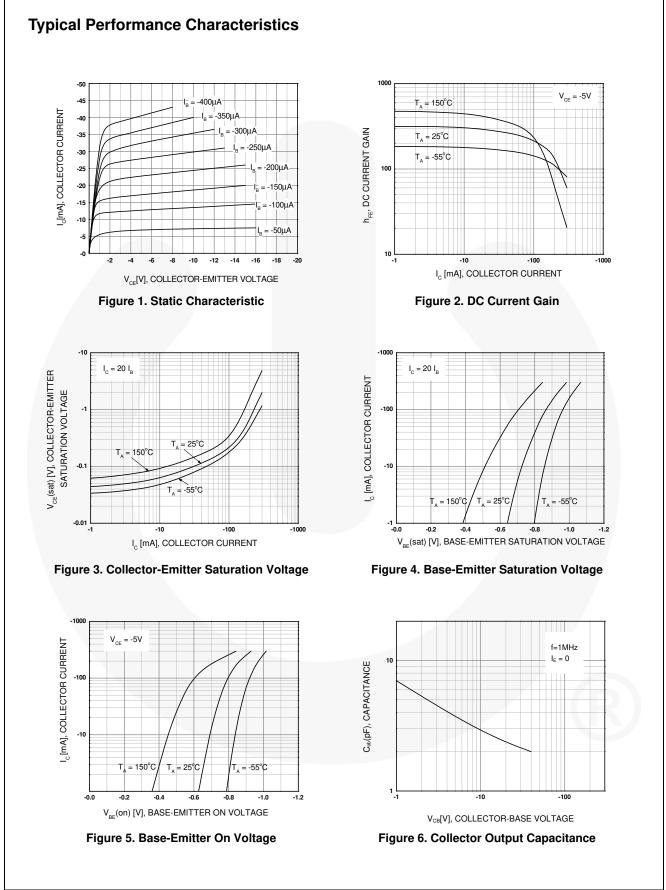
# **Electrical Characteristics**

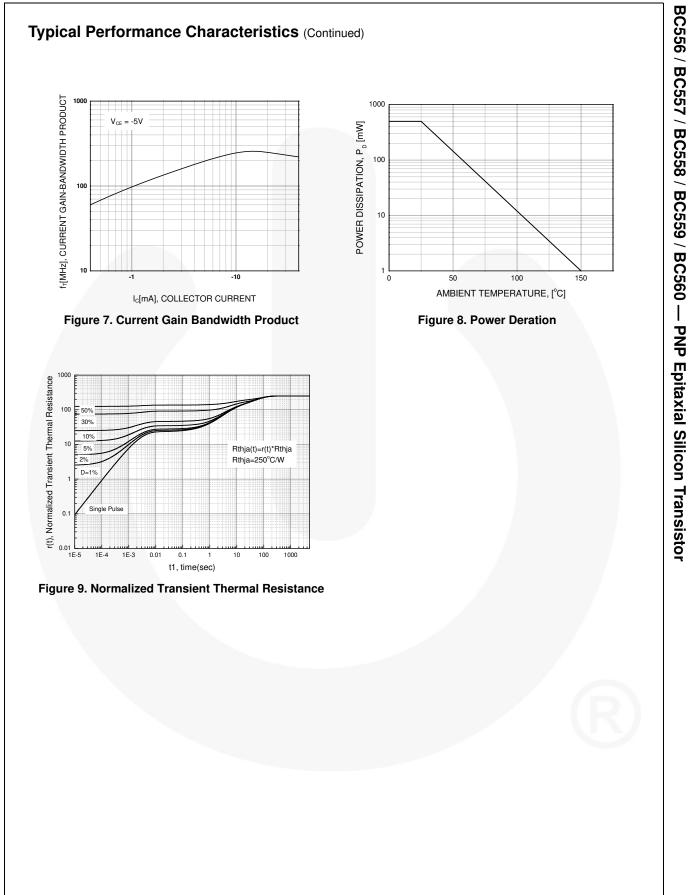
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

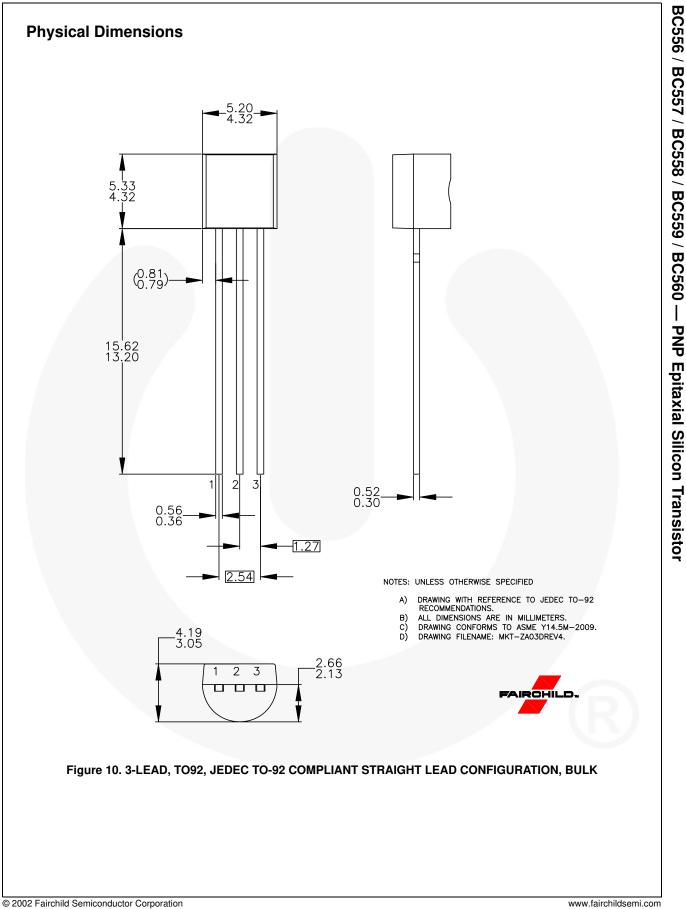
Symbol	Parameter		Conditions	Min.	Тур.	Max.	Unit
I <sub>CBO</sub>	Collector Cut-Off Current		$V_{CB} = -30 \text{ V}, \text{ I}_{E} = 0$			-15	nA
h <sub>FE</sub>	DC Curr	ent Gain	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -2 \text{ mA}$	110		800	
V = -(cat)	V <sub>CE</sub> (sat) Collector-Emitter Saturation Voltage		I <sub>C</sub> = -10 mA, I <sub>B</sub> = -0.5 mA		-90	-300	mV
VCE(Sal)			I <sub>C</sub> = -100 mA, I <sub>B</sub> = -5 mA		-250	-650	
V (cot)	V <sub>BE</sub> (sat) Collector-Base Saturation Voltage		$I_{\rm C} = -10$ mA, $I_{\rm B} = -0.5$ mA		-700		m)/
V <sub>BE</sub> (sat)	Collecto	-base Saturation voltage	I <sub>C</sub> = -100 mA, I <sub>B</sub> = -5 mA		-900		mV
V (op)	V <sub>BE</sub> (on) Base-Emitter On Voltage		$V_{CE} = -5 \text{ V}, I_{C} = -2 \text{ mA}$	-600	-660	-750	mV
VBE(OII)			$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$			-800	
f <sub>T</sub>	Current Gain Bandwidth Product		$V_{CE}$ = -5 V, I <sub>C</sub> = -10 mA, f = 10 MHz		150		MHz
C <sub>ob</sub>	Output Capacitance		$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$			6	pF
	Noise	BC556 / BC557 / BC558	$V_{CE} = -5 V, I_{C} = -200 \mu A,$		2	10	dB
NF Noise Figure		BC559 / BC560	f = 1 kHz, $R_G = 2 k\Omega$		1	4	
	Figure	BC559	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -200 \mu\text{A},$		1.2	4.0	
	BC560	$R_{G} = 2 k\Omega, f = 30 \text{ to } 15000 \text{ MHz}$		1.2	2.0		

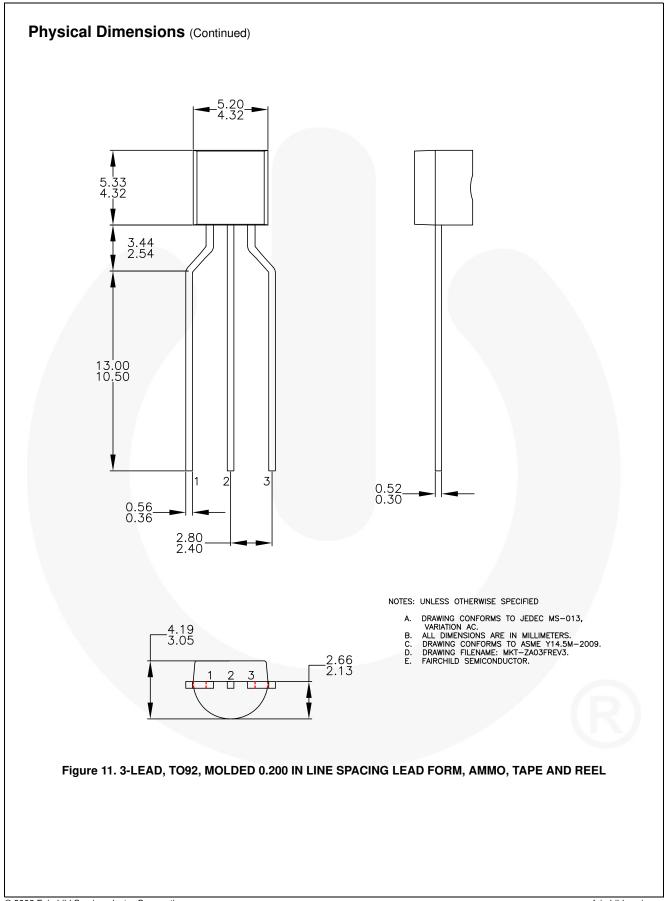
# h<sub>FE</sub> Classification

Classification	А	В	С
h <sub>FE</sub>	110 ~ 220	200 ~ 450	420 ~ 800









BC556 / BC557 / BC558 / BC559 / BC560 — PNP Epitaxial Silicon Transistor

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