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# **Darlington Transistors**

## **NPN Silicon**

## **Features**

• Pb-Free Packages are Available\*

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	$V_{CEO}$	55	Vdc
Collector - Base Voltage	V <sub>CBO</sub>	80	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	12	Vdc
Collector Current – Continuous	I <sub>C</sub>	1.0	Adc
Total Power Dissipation @ T <sub>A</sub> = 25°C Derate above T <sub>A</sub> = 25°C	P <sub>D</sub>	625 5.0	mW mW/°C
Total Power Dissipation @ T <sub>A</sub> = 25°C Derate above T <sub>A</sub> = 25°C	P <sub>D</sub>	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

## THERMAL CHARACTERISTICS

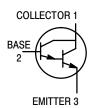
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



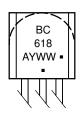
## ON Semiconductor®

## http://onsemi.com





## **MARKING DIAGRAM**



A = Assembly Location

= Year

WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>		
BC618	TO-92	5000 Units / Bulk		
BC618G	TO-92 (Pb-Free)	5000 Units / Bulk		
BC618RL1	TO-92	2000 / Tape & Reel		
BC618RL1G	TO-92 (Pb-Free)	2000 / Tape & Reel		

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# 

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	<u>.</u>			-	
Collector – Emitter Breakdown Voltage $(I_C = 10 \text{ mAdc}, V_{BE} = 0)$	V <sub>(BR)</sub> CEO	55	-	_	Vdc
Collector – Base Breakdown Voltage $(I_C = 100 \mu Adc, I_E = 0)$	V <sub>(BR)</sub> CBO	80	-	-	Vdc
Emitter – Base Breakdown Voltage $(I_E = 10 \mu Adc, I_C = 0)$	V <sub>(BR)EBO</sub>	12	-	-	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 60 Vdc, V <sub>BE</sub> = 0)	I <sub>CES</sub>	_	_	50	nAdc
Collector Cutoff Current (V <sub>CB</sub> = 60 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	-	_	50	nAdc
Emitter Cutoff Current (V <sub>EB</sub> = 10 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	-	_	50	nAdc
ON CHARACTERISTICS	<u>.</u>				•
DC Current Gain (I <sub>C</sub> = 200 mA, I <sub>B</sub> = 0.2 mA)	V <sub>CE(sat)</sub>	_	_	1.1	Vdc
Base – Emitter Saturation Voltage (I <sub>C</sub> = 200 mA, I <sub>B</sub> = 0.2 mA)	V <sub>BE(sat)</sub>	-	-	1.6	Vdc
DC Current Gain $ \begin{array}{l} (I_C = 100 \ \mu A, \ V_{CE} = 5.0 \ Vdc) \\ (I_C = 10 \ mA, \ V_{CE} = 5.0 \ Vdc) \\ (I_C = 200 \ mA, \ V_{CE} = 5.0 \ Vdc) \\ (I_C = 1.0 \ A, \ V_{CE} = 5.0 \ Vdc) \end{array} $	h <sub>FE</sub>	2000 4000 10000 4000	- - - -	- 50000 -	-
DYNAMIC CHARACTERISTICS	·				
Current-Gain - Bandwidth Product (I <sub>C</sub> = 500 mA, V <sub>CE</sub> = 5.0 Vdc, P = 100 MHz)	f <sub>T</sub>	150	-	_	MHz
Output Capacitance $(V_{CB} = 10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz})$	C <sub>ob</sub>	_	4.5	7.0	pF
Input Capacitance (V <sub>EB</sub> = 5.0 V, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ib</sub>	_	5.0	9.0	pF

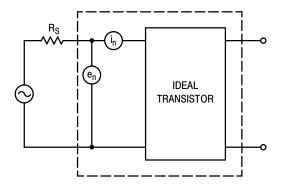


Figure 1. Transistor Noise Model

## **NOISE CHARACTERISTICS**

 $(V_{CE} = 5.0 \text{ Vdc}, T_A = 25^{\circ}C)$ 

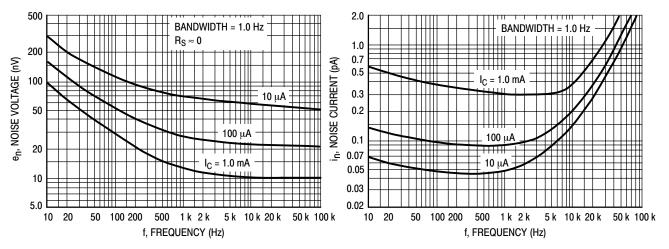


Figure 2. Noise Voltage

Figure 3. Noise Current

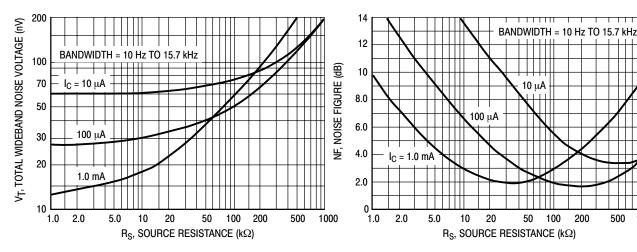
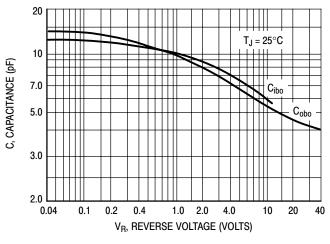


Figure 4. Total Wideband Noise Voltage

Figure 5. Wideband Noise Figure

500 1000

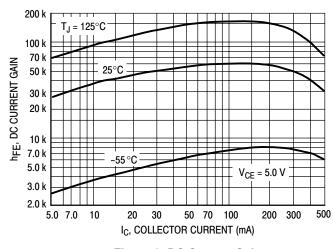
## **SMALL-SIGNAL CHARACTERISTICS**



V<sub>CE</sub> = 5.0 V |hfe|, SMALL-SIGNAL CURRENT GAIN f = 100 MHz  $T_J = 25^{\circ}C$ 2.0 1.0 8.0 0.6 0.4 0.2 2.0 100 500 0.5 1.0 IC, COLLECTOR CURRENT (mA)

Figure 6. Capacitance

Figure 7. High Frequency Current Gain



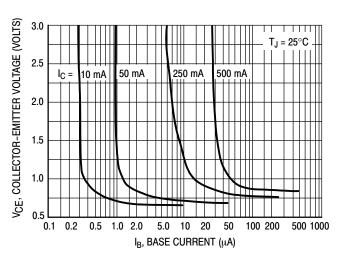
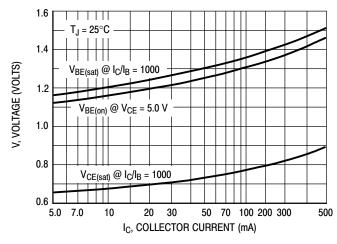


Figure 8. DC Current Gain

Figure 9. Collector Saturation Region



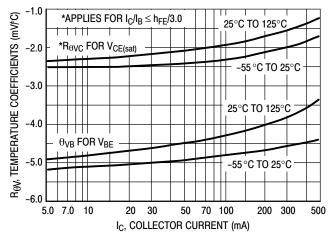


Figure 10. "On" Voltages

Figure 11. Temperature Coefficients

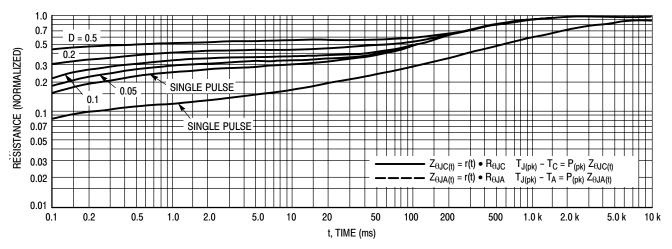


Figure 12. Thermal Response

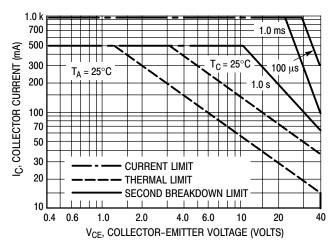
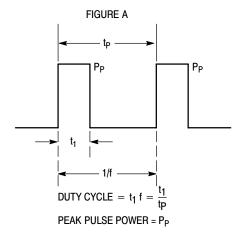


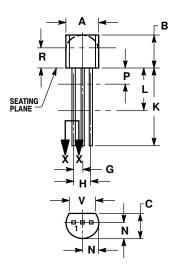
Figure 13. Active Region Safe Operating Area



Design Note: Use of Transient Thermal Resistance Data

#### PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AL





- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R
  IS UNCONTROLLED.
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
V	0.135		3.43	

STYLE 17:

PIN 1. COLLECTOR

BASE 2.

EMITTER

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